Product data sheet

1. General description

NPN single switching transistor in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

PNP complement: PMBT3906MB.

2. Features and benefits

- Single general-purpose switching transistor
- Ultra small SMD plastic package
- Board-space reduction
- Low package height of 0.37 mm
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- · Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	200	mA
h _{FE}	DC current gain	$V_{CE} = 1 \text{ V}; I_{C} = 10 \text{ mA}$	100	180	300	



40 V, 200 mA NPN switching transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	1	C
2	Е	emitter	3	в
3	С	collector	Transparent top view	E E
			DFN1006B-3 (SOT883B)	sym021

6. Ordering information

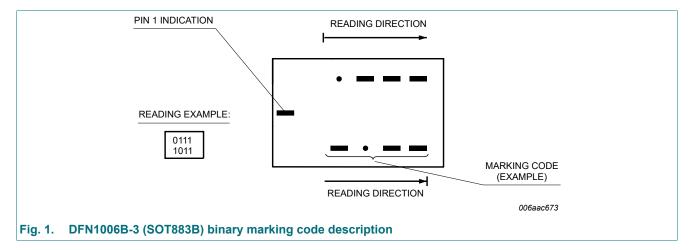
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMBT3904MB		plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT3904MB	0100 0111



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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	200	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	250	mW
			[1] [3]	-	590	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Reflow soldering is the only recommended soldering method.

^[2] Device mounted on an FR4 Printed-Circuit board (PCB), single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

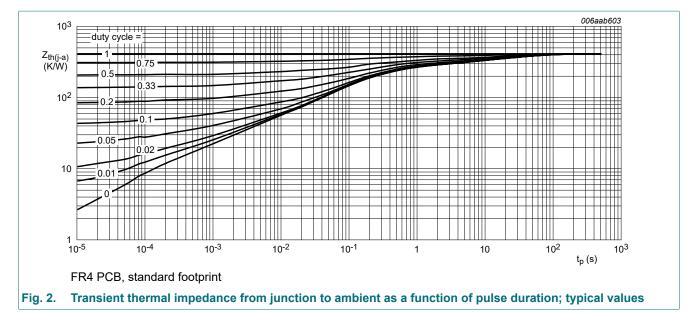
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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
11(J-a)	thermal resistance from	in free air	[1] [2]	-	-	500	K/W
	junction to ambient		[1] [3]	-	-	212	K/W

- [1] Reflow soldering is the only recommended soldering method.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



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10. Characteristics

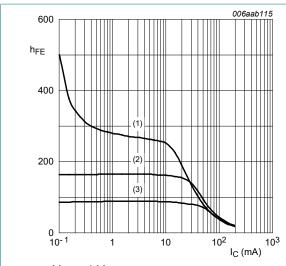
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified

Parameter	Conditions		Min	Тур	Max	Unit
collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A		-	-	50	nA
emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A		-	-	50	nA
DC current gain	V _{CE} = 1 V; I _C = 0.1 mA		60	180	-	
	V _{CE} = 1 V; I _C = 1 mA		80	180	-	
	V _{CE} = 1 V; I _C = 10 mA		100	180	300	
	V _{CE} = 1 V; I _C = 50 mA		60	105	-	
	V _{CE} = 1 V; I _C = 100 mA	[1]	30	50	-	
collector-emitter	I _C = 10 mA; I _B = 1 mA		-	75	200	mV
saturation voltage	I _C = 50 mA; I _B = 5 mA		-	120	300	mV
base-emitter saturation	I _C = 10 mA; I _B = 1 mA		650	750	850	mV
voltage	I _C = 50 mA; I _B = 5 mA		-	850	950	mV
delay time			-	-	35	ns
rise time	V _{CC} = 3 V		-	-	35	ns
turn-on time			-	-	70	ns
storage time			-	-	200	ns
fall time			-	-	50	ns
turn-off time			-	-	250	ns
collector capacitance	$V_{CB} = 5 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	-	4	pF
emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz		-	-	8	pF
transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz		300	-	-	MHz
noise figure	V_{CE} = 5 V; I_{C} = 100 μA; R_{S} = 1 kΩ; f = 10 Hz to 15.7 kHz		-	-	5	dB
	collector-base cut-off current emitter-base cut-off current DC current gain collector-emitter saturation voltage base-emitter saturation voltage delay time rise time turn-on time storage time fall time turn-off time collector capacitance emitter capacitance transition frequency	collector-base cut-off current $V_{CB} = 30 \text{ V; } I_E = 0 \text{ A}$ $\text{emitter-base cut-off current}$ DC current gain $V_{CE} = 1 \text{ V; } I_C = 0.1 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 10 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 10 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 50 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 50 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 100 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 50 \text{ mA}$ $V_{CE} = 1 \text{ V; } I_C = 100 \text{ mA}$ $I_C = 10 \text{ mA; } I_B = 1 \text{ mA}$ $I_C = 50 \text{ mA; } I_B = 5 \text{ mA}$ $I_C = 50 \text{ mA; } I_B = 5 \text{ mA}$ $I_C = 10 \text{ mA; } I_{Bon} = 1 \text{ mA; } I_{Boff} = -1 \text{ mA; } I_{CE} = 3 \text{ V}$ $V_{CE} = 5 \text{ V; } I_C = 0 \text{ A; } I_C = 0 $	$ \begin{array}{c} \text{collector-base cut-off} \\ \text{current} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	$ \begin{array}{c} \text{collector-base cut-off} \\ \text{current} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c} \text{collector-base cut-off current} \\ \text{emitter-base cut-off current} \\ \\ \text{Pocition} \\ \text{Pocition} \\ \text{Collector-base cut-off current} \\ \\ \text{Pocition} \\ Pocition$

^[1] Pulsed test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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$$V_{CE} = 1 V$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = -55 °C$$

Fig. 3. DC current gain as a function of collector current; typical values

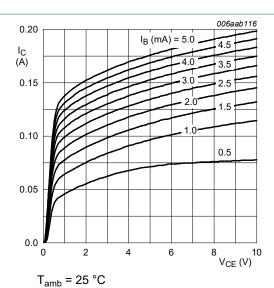
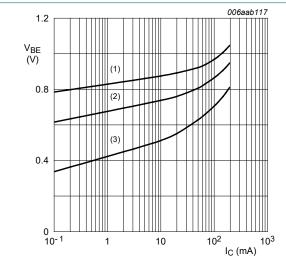


Fig. 4. Collector current as a function of collectoremitter voltage; typical values



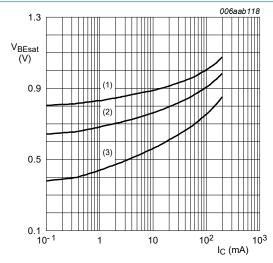
$$V_{CE}$$
 = 1 V

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

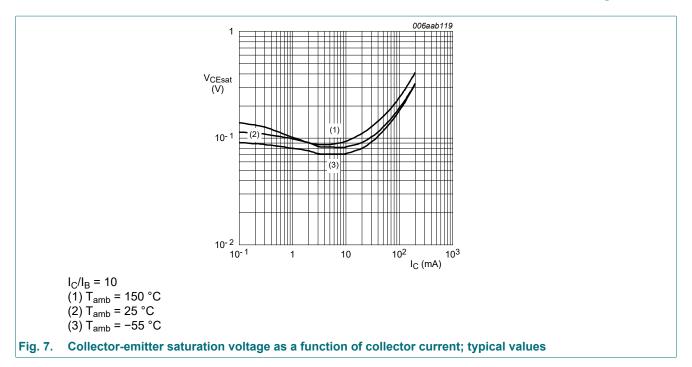
(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

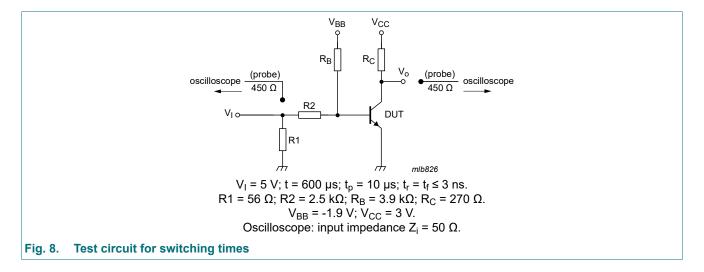
$$(3) T_{amb} = 150 °C$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

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11. Test information

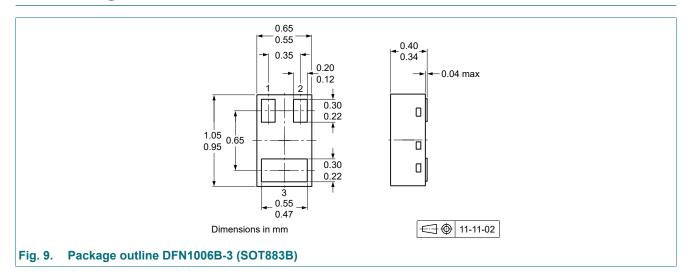


Quality information

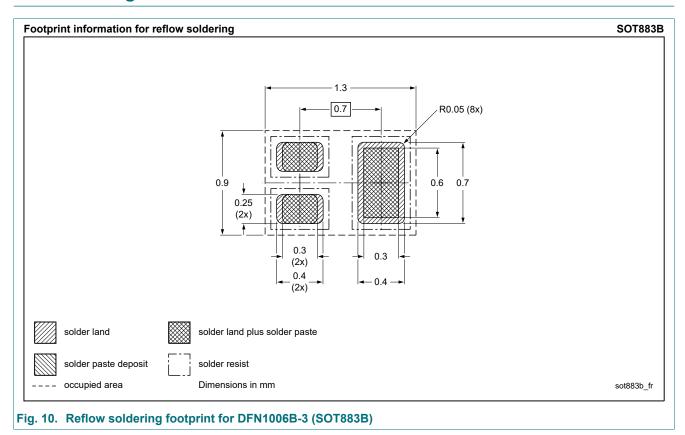
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMBT3904MB v.2	20181116	Product data sheet	-	PMBT3904MB v.1				
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 							
PMBT3904MB v.1	20120307	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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Date of release: 16 November 2018

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