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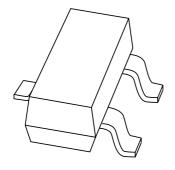
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PMBT5551 NPN high-voltage transistor

Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 21



NXP Semiconductors Product data sheet

# NPN high-voltage transistor

## **PMBT5551**

#### **FEATURES**

- Low current (max. 300 mA)
- High voltage (max. 160 V).

## **APPLICATIONS**

- · General purpose
- Telephony.

## **DESCRIPTION**

NPN high-voltage transistor in a SOT23 plastic package. PNP complement: PMBT5401.

## **MARKING**

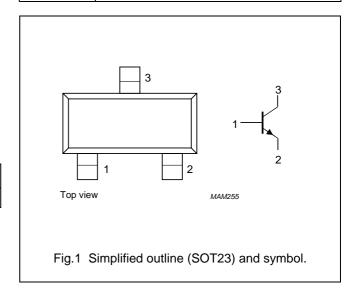
TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PMBT5551	*G1

#### Note

- 1. \* = p: Made in Hong Kong.
  - \* = t : Made in Malaysia.
  - \* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE		PACKAGE DESCRIPTION VERSION				
NUMBER	NAME					
PMBT5551	_	plastic surface mounted package; 3 leads	SOT23			

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	180	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	160	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	6	V
I <sub>C</sub>	collector current (DC)		_	300	mA
I <sub>CM</sub>	peak collector current		_	600	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

NXP Semiconductors Product data sheet

# NPN high-voltage transistor

PMBT5551

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

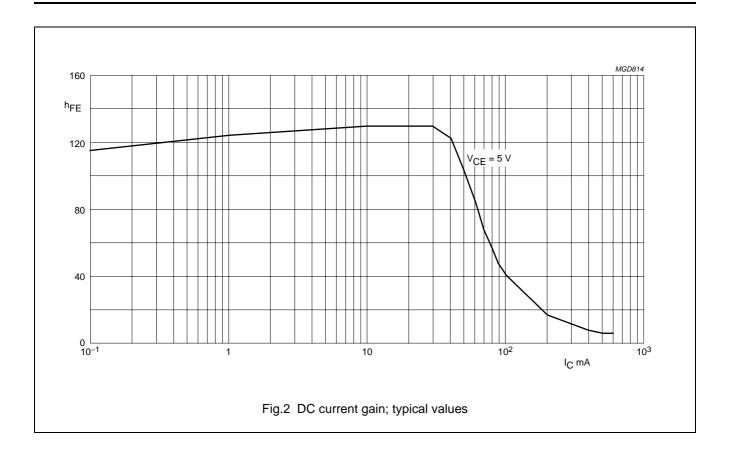
## **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 120 V	_	50	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 120 V; T <sub>amb</sub> = 100 °C	_	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 4 V	_	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; (see Fig.2)			
		$I_C = 1 \text{ mA}$	80	_	
		I <sub>C</sub> = 10 mA	80	250	
		$I_C = 50 \text{ mA}$	30	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	_	150	mV
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA	_	200	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 1 mA	_	1	V
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 5 mA	_	1	V
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	_	6	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 500 \text{ mV}$ ; $f = 1 \text{ MHz}$	_	30	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	100	300	MHz
F	noise figure	$I_C$ = 200 μA; $V_{CE}$ = 5 V; $R_S$ = 2 kΩ; $f$ = 10 Hz to 15.7 kHz	_	8	dB

# NPN high-voltage transistor

# PMBT5551



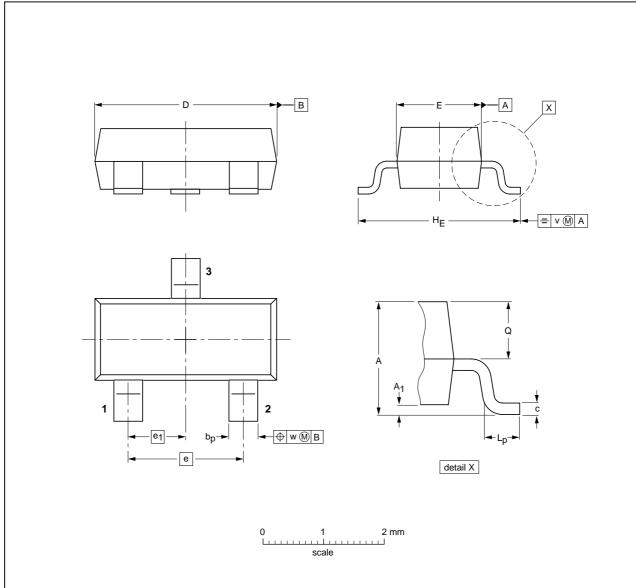
# NPN high-voltage transistor

PMBT5551

#### **PACKAGE OUTLINE**

## Plastic surface-mounted package; 3 leads

SOT23



## **DIMENSIONS** (mm are the original dimensions)

ι	JNIT	Α	A <sub>1</sub> max.	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	v	w
	mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFERENCES EUROPEAN 1991				
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>-04-11-04</del> 06-03-16

2004 Jan 21 5

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NXP Semiconductors Product data sheet

# NPN high-voltage transistor

**PMBT5551** 

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

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## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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