# PMCM4401UPE

# 20 V, P-channel Trench MOSFET 7 October 2016

**Product data sheet** 

# 1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a 4 bumps Wafer Level Chip-Size Package (WLCSP) using Trench MOSFET technology.

#### 2. Features and benefits

- Low threshold voltage
- Ultra small package: 0.78 × 0.78 × 0.35 mm
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

## 3. Applications

- Battery switch
- High-speed line driver
- Low-side loadswitch
- Switching circuits

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	-20	V
$V_{GS}$	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	-4	Α
Static characte	Static characteristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = -4.5 V; $I_D$ = -3 A; $T_j$ = 25 °C		-	75	95	mΩ

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
A1	G	gate	1 2	D -
A2	S	source	A	
B1	D	drain		G $\downarrow$ $\downarrow$ $\downarrow$
B2	S	source	В	
			Transparent top view WLCSP4 (OL- PMCM4401UPE)	S 017aaa259

# 6. Ordering information

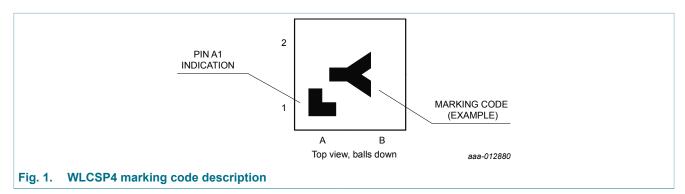
**Table 3. Ordering information** 

Type number	Package	ckage				
	Name	Description	Version			
PMCM4401UPE	WLCSP4	WLCSP4: wafer level chip-size package; 4 bumps (2 x 2)	OL- PMCM4401UPE			

# 7. Marking

Table 4. Marking codes

Table II III II I	
Type number	Marking code
PMCM4401UPE	S



## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-20	V
$V_{GS}$	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-4	Α
		$V_{GS}$ = -4.5 V; $T_{amb}$ = 25 °C	[1]	-	-3.2	Α
		$V_{GS}$ = -4.5 V; $T_{amb}$ = 100 °C	[1]	-	-2	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	-13	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	400	mW
			[1]	-	1300	mW
		T <sub>sp</sub> = 25 °C		-	12500	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drain o	diode					
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	-1.2	Α

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

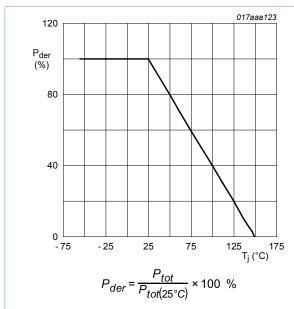


Fig. 2. MOSFET transistor: Normalized total power dissipation as a function of junction temperature

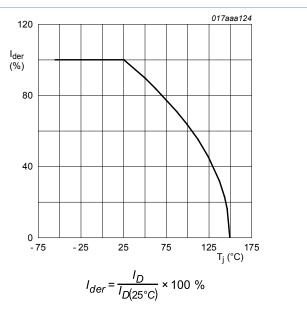


Fig. 3. MOSFET transistor: Normalized continuous drain current as a function of junction temperature

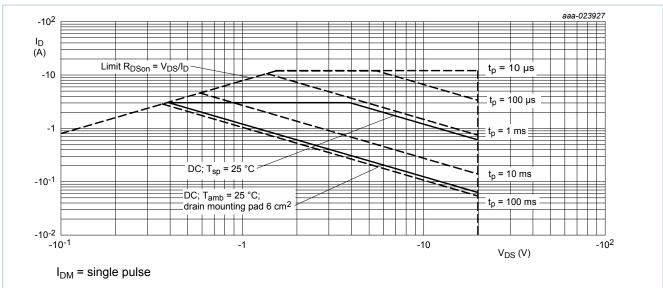


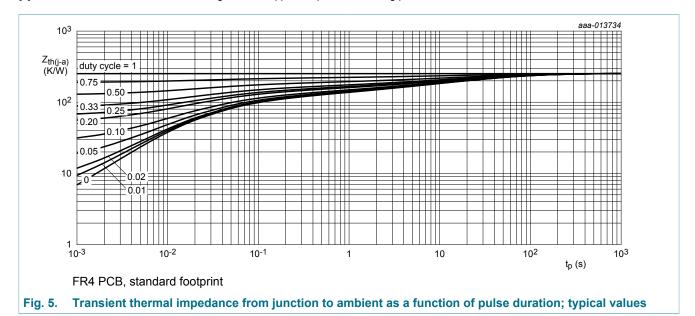
Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

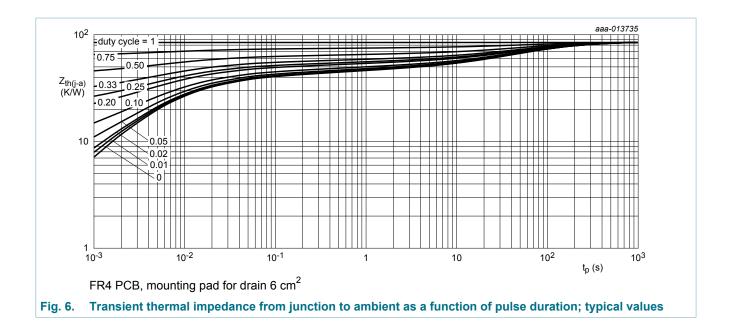
### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance		[1]	-	250	300	K/W
	from junction to ambient		[2]	-	70	85	K/W
			[3]	-	85	100	K/W
		in free air; t ≤ 5 s	[3]	-	50	60	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	5	10	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain, 4-layer, 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.





## 10. Characteristics

#### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	racteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D = -250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^{\circ}C$	-20	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = -250 \ \mu A; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}C$	-0.4	-0.6	-0.9	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = -20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-	-1	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-10	μΑ
		$V_{GS}$ = 8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	10	μΑ
		$V_{GS} = -4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	-1	μΑ
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
		$V_{GS}$ = -2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-200	nA
		$V_{GS}$ = 2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	200	nA
R <sub>DSon</sub>	drain-source on-state	$V_{GS}$ = -4.5 V; $I_{D}$ = -3 A; $T_{j}$ = 25 °C	-	75	95	mΩ
	resistance	$V_{GS}$ = -4.5 V; $I_D$ = -3 A; $T_j$ = 150 °C	-	100	120	mΩ
		$V_{GS}$ = -2.5 V; $I_{D}$ = -2 A; $T_{j}$ = 25 °C	-	95	130	mΩ
		$V_{GS}$ = -1.8 V; $I_{D}$ = -0.1 A; $T_{j}$ = 25 °C	-	130	190	mΩ
g <sub>fs</sub>	forward transconductance	$V_{DS}$ = -6 V; $I_{D}$ = -3 A; $T_{j}$ = 25 °C	-	10.8	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	7	-	Ω
Dynamic c	haracteristics					,
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = -10 V; $I_{D}$ = -3 A; $V_{GS}$ = -4.5 V;	-	5.9	10	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.6	-	nC
$Q_{GD}$	gate-drain charge		-	1.7	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS} = -10 \text{ V}; f = 1 \text{ MHz}; V_{GS} = 0 \text{ V};$	-	420	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	64	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	58	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = -10 V; $I_{D}$ = -3.3 A; $V_{GS}$ = -4.5 V;	-	4	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 ^{\circ}C$	-	18	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	31	-	ns
t <sub>f</sub>	fall time		-	13	-	ns
Source-dra	ain diode		'			
$V_{SD}$	source-drain voltage	I <sub>S</sub> = -1.2 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	-	-0.8	-1.2	V

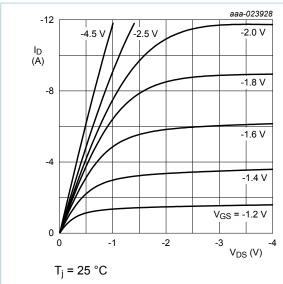


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

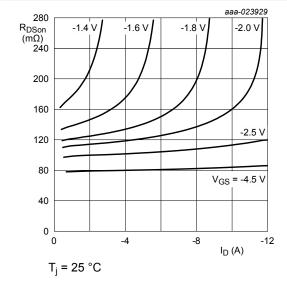


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

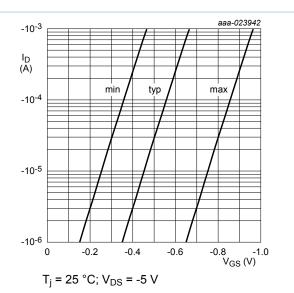


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

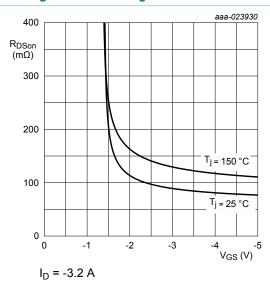


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

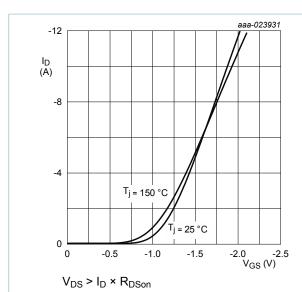


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

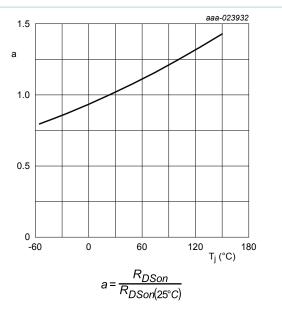


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

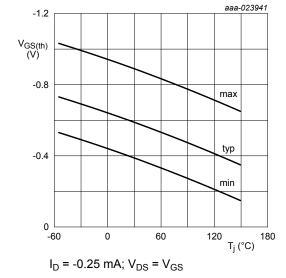


Fig. 13. Gate-source threshold voltage as a function of junction temperature

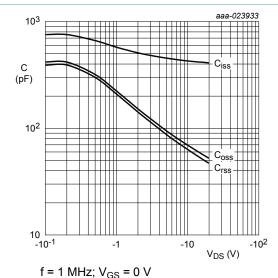


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

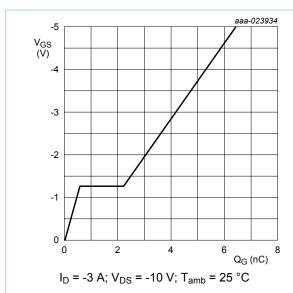


Fig. 15. Gate-source voltage as a function of gate charge; typical values

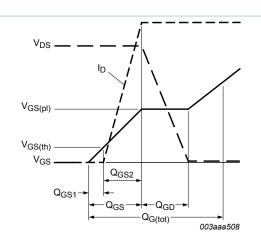


Fig. 16. MOSFET transistor: Gate charge waveform definitions

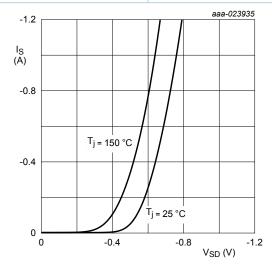
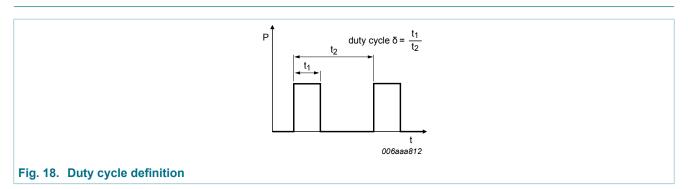


Fig. 17. Source current as a function of source-drain voltage; typical values

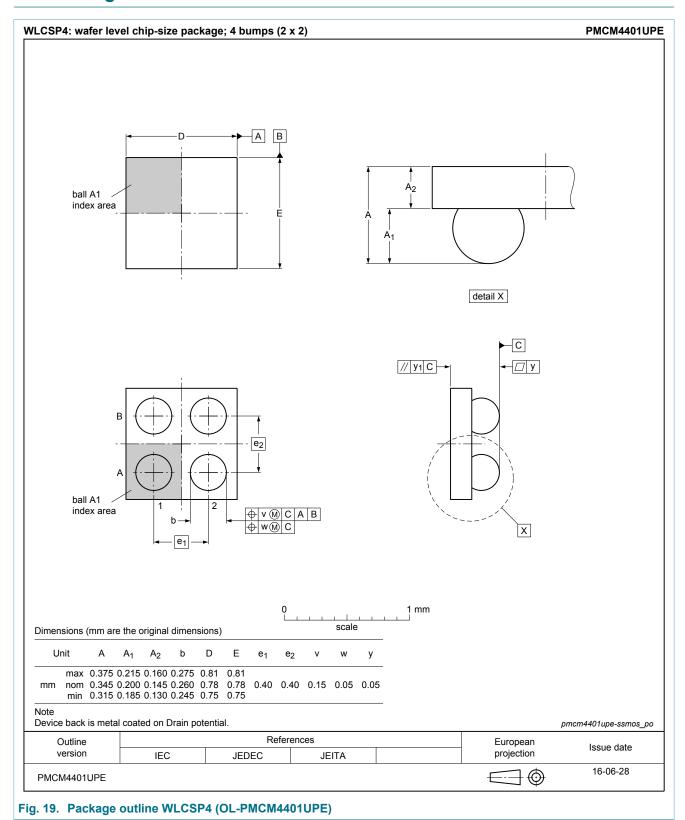
## 11. Test information

 $V_{GS} = 0 V$ 

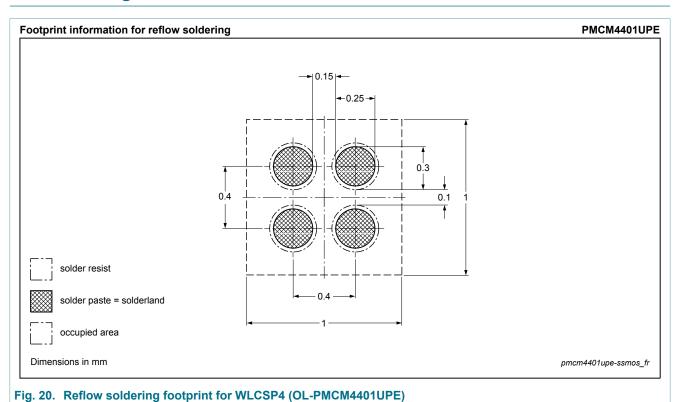


PMCM4401UPE

# 12. Package outline



# 13. Soldering



# 14. Revision history

#### Table 8. Revision history

Tuble of Novicion filetory								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMCM4401UPE v.2	20161007	Product data sheet	-	PMCM4401UPE v.1				
Modification:	• R <sub>dson</sub> at V <sub>GS</sub> = -4.5 V; I <sub>D</sub> = -3 A; T <sub>j</sub> = 25 °C corrected to 95 m $\Omega$ .							
PMCM4401UPE v.1	20160704	Product data sheet	-	-				

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#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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