

20 V, 0.5 A low VF MEGA Schottky barrier rectifier 5 December 2016 Produ

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection encapsulated in small SOD123 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: $I_F \le 0.5 \text{ A}$
- Reverse voltage: V_R ≤ 20 V
- Low forward voltage typ. V_F = 355 mV
- Low reverse current typ. I_R = 40 μA
- Small SMD plastic package
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications
- Automotive applications

4. Quick reference data

| Table 1. Qui | ck reference data | | | | | | |
|----------------|-------------------|---|-----|-----|-----|-----|------|
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
| I _F | forward current | T _{sp} ≤ 55 °C | | - | - | 0.5 | А |
| V _R | reverse voltage | T _j = 25 °C | | - | - | 20 | V |
| V _F | forward voltage | I_{F} = 500 mA; t_{p} $\leq~$ 300 $\mu s;$ δ $\leq~$ 0.02 $;$ T_{j} = 25 $^{\circ}C$ | | - | 355 | 390 | mV |
| I _R | reverse current | V_R = 20 V; pulsed; T _j = 25 °C | [1] | - | 40 | 200 | μA |

[1] Very short test pulse to prevent junction self-heating.

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5. Pinning information

| Table 2. Pinning information | | | | | | | | |
|------------------------------|--------|------------------------|--------------------|----------------|--|--|--|--|
| Pin | Symbol | Description | Simplified outline | Graphic symbol | | | | |
| 1 | К | cathode ^[1] | | 1 🛃 2 | | | | |
| 2 | A | anode | SOD123 | sym001 | | | | |

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | | |
|-------------|---------|--|---------|--|--|--|
| | Name | Description | Version | | | |
| PMEG2005EGW | SOD123 | Plastic surface-mounted package; 2 leads | SOD123 | | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PMEG2005EGW | G1 |

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Мах | Unit |
|--------------------|-------------------------------------|---|-----|-----|-----|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 20 | V |
| l _F | forward current | T _{sp} ≤ 55 °C | | - | 0.5 | А |
| I _{F(AV)} | average forward current | δ = 0.5 ; f = 20 kHz; T _{amb} ≤ 120 °C; square wave | [1] | - | 0.5 | A |
| | | δ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~$ 145 °C; square wave | | - | 0.5 | A |
| I _{FRM} | repetitive peak forward current | t _p ≤ 1 ms; δ ≤ 0.25 | | - | 7 | A |
| I _{FSM} | non-repetitive peak forward current | t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave | | - | 10 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2] | - | 400 | mW |
| | | | [1] | - | 660 | mW |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

| | ai characteristics | | | | | | |
|-----------------------|--|------------|---------|-----|-----|-----|------|
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
| R _{th(j-a)} | thermal resistance from junction to ambient | | [1] [2] | - | - | 310 | K/W |
| | | | [1] [3] | - | - | 190 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | [4] | - | - | 29 | K/W |

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

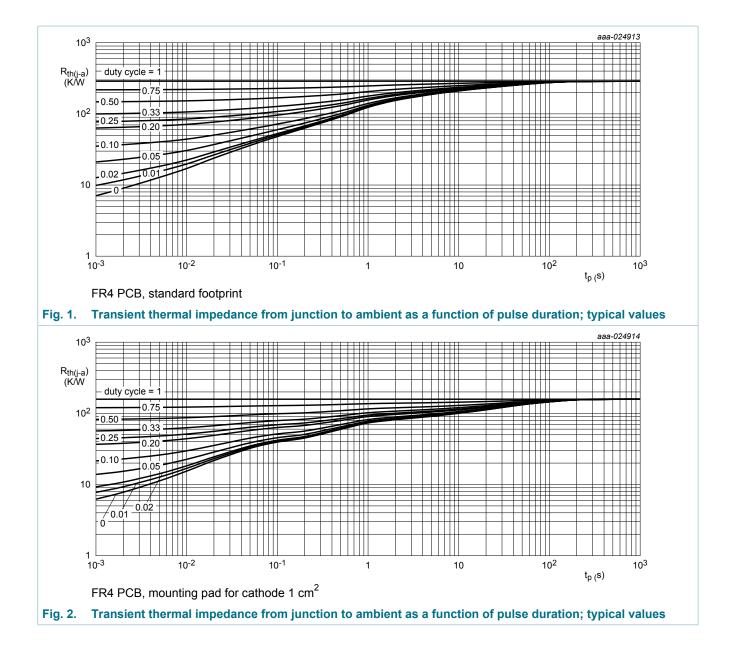
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

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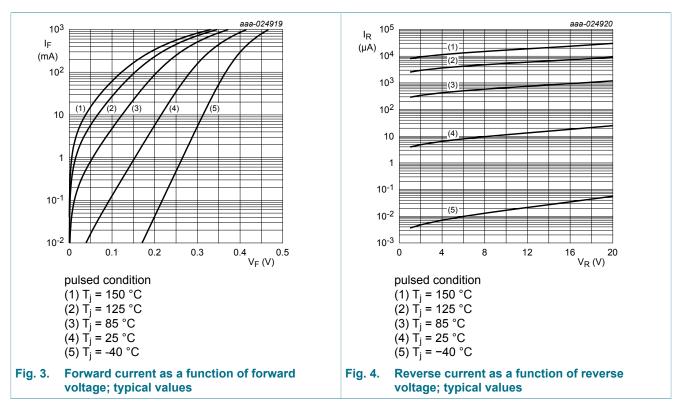


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10. Characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--------------------|---------------------------|---|-----|-----|-----|-----|------|
| V _{(BR)R} | reverse breakdown voltage | I_{R} = 1 mA; t_{p} $\leq~300~\mu s; \delta \leq~0.02$; T_{j} = 25 °C | | 20 | - | - | V |
| VF | forward voltage | I_{F} = 0.1 mA; t_{p} \leq 300 $\mu\text{s};$ δ \leq 0.02 ; T_{j} = 25 °C | | - | 90 | 130 | mV |
| | | I_{F} = 1 mA; t_{p} $\leq~$ 300 μ s; $\delta~\leq~0.02~$; T_{j} = 25 $^{\circ}C$ | | - | 150 | 190 | mV |
| | | I_{F} = 10 mA; t_{p} $\leq~$ 300 μ s; $\delta~\leq~$ 0.02 $\ ;$ T_{j} = 25 $^{\circ}C$ | | - | 210 | 240 | mV |
| | | I_F = 100 mA; $t_p \leq ~300~\mu s; ~\delta \leq ~0.02~;$ T_j = 25 °C | | - | 280 | 330 | mV |
| | | I_F = 500 mA; $t_p \leq ~300~\mu s;~\delta \leq ~0.02~;$ T_j = 25 °C | | - | 355 | 390 | mV |
| I _R | reverse current | V _R = 10 V; pulsed; T _j = 25 °C | [1] | - | 15 | 40 | μA |
| | | V_R = 20 V; pulsed; T _j = 25 °C | [1] | - | 40 | 200 | μA |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _i = 25 °C | | - | 66 | 80 | pF |

[1] Very short test pulse to prevent junction self-heating.

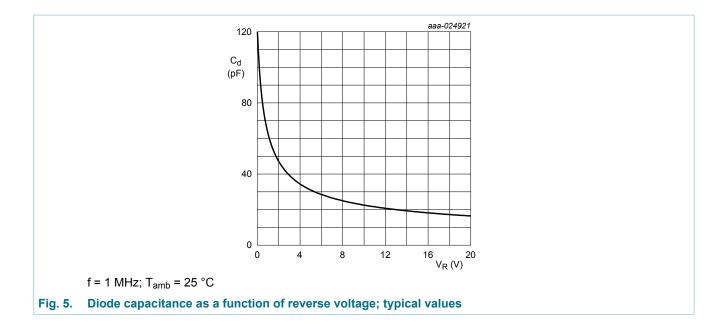


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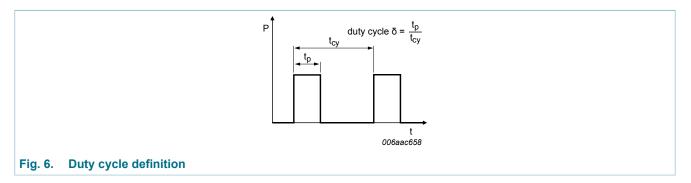
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11. Test information



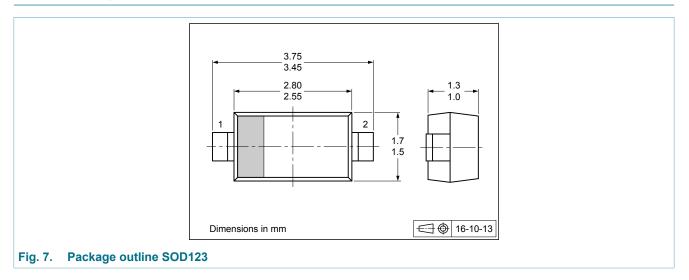
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

Quality information

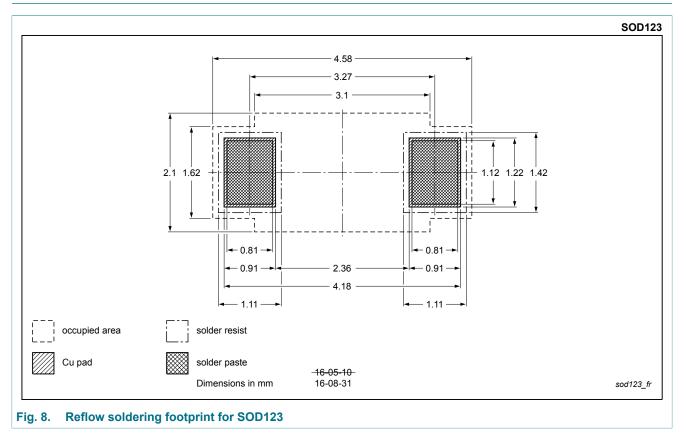
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline



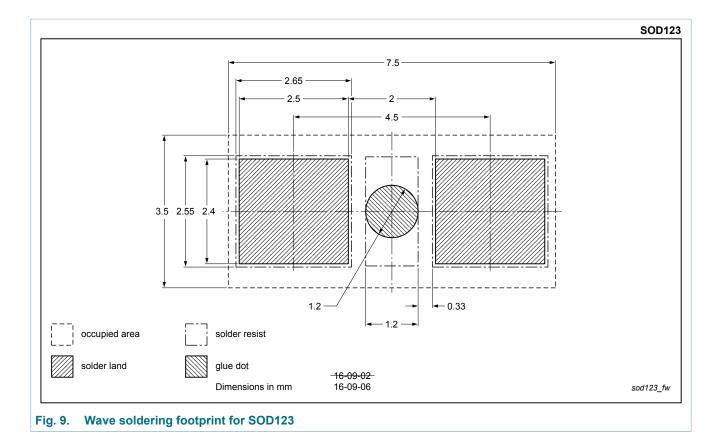
13. Soldering



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14. Revision history

| Table 8. Revision histo | ory | | | | | | |
|-------------------------|------------------------|------------------------|---------------|-----------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| PMEG2005EGW v.2 | 20161205 | Product data sheet | - | PMEG2005EGW v.1 | | | |
| Modifications: | Product status changed | | | | | | |
| PMEG2005EGW v.1 | 20161122 | Preliminary data sheet | - | - | | | |

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15. Legal information

Data sheet status

| Document status ^{[1] [2]} | Product status ^[3] | Definition |
|---------------------------------------|----------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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