## 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Extremely low leakage current I<sub>R</sub> = 235 nA
- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 60 V
- Low forward voltage V<sub>F</sub> = 600 mV
- · High power capability due to clip-bonding technology
- High temperature T<sub>i</sub> ≤ 175 °C
- Small and flat lead SMD plastic package
- AEC-Q101 qualified
- Capable for reflow and wave soldering

# 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- · Reverse polarity protection
- · Low power consumption applications

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; $T_{sp} \le 165$ °C; square wave	-	-	2	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	-	60	V
V <sub>F</sub>	forward voltage	$I_F = 0.7 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	525	590	mV
I <sub>R</sub>	reverse current	$V_R = 40 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	50	-	nA



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		к <b>.}</b> -А
2	Α	anode	1 2	sym001
			CFP5 (SOD128)	

<sup>[1]</sup> The marking bar indicates the cathode.

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMEG6020AELP	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128			

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG6020AELP	DL

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	60	V
I <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> = 160 °C		-	2.83	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; $T_{amb} \le 110$ °C; square wave	[1]	-	2	А
		$\delta$ = 0.5; f = 20 kHz; T <sub>sp</sub> $\leq$ 165 °C; square wave		-	2	Α
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	50	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	750	mW
			[3]	-	1.25	W
			[1]	-	2.5	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ιι ( <u>)</u> -α <i>)</i>	thermal resistance from	]	[1] [2]	-	-	200	K/W
	junction to ambient		[1] [3]	-	-	120	K/W
			[1] [4]	-	-	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [5] Soldering point of cathode tab.

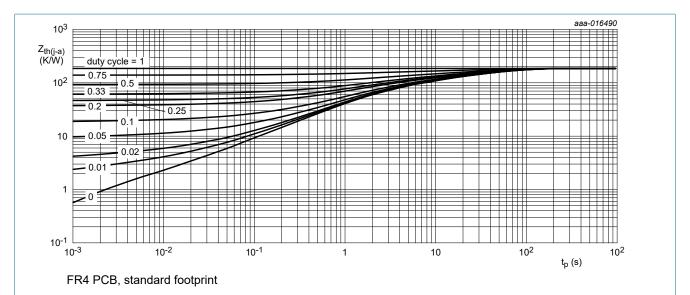
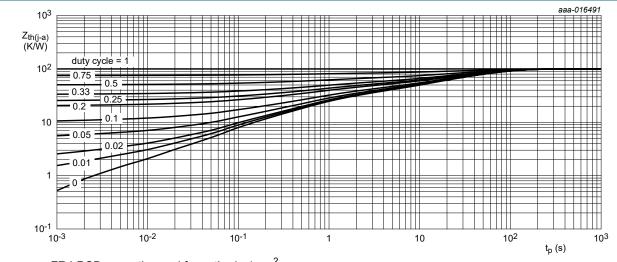


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

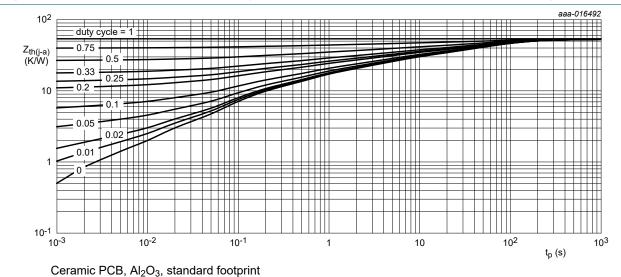
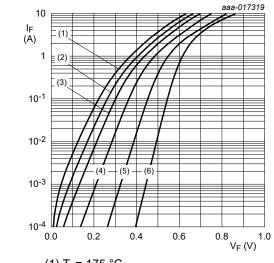


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R$ = 1 mA; $t_p$ = 300 μs; δ = 0.02; $T_j$ = 25 °C	60	-	-	V
V <sub>F</sub>	forward voltage	$I_F$ = 0.1 A; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	450	510	mV
		$I_F$ = 0.5 A; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	510	570	mV
		$I_F$ = 0.7 A; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	525	590	mV
		$I_F$ = 1 A; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	545	610	mV
		$I_F$ = 1.6 A; $t_p \le 300 \ \mu s$ ; δ ≤ 0.02; $T_j$ = 25 °C	-	580	650	mV
		$I_F$ = 2 A; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	600	670	mV
		$I_F$ = 2 A; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 125 °C	-	510	630	mV
I <sub>R</sub>	reverse current	$V_R = 10 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	15	-	nA
		$V_R = 40 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	50	-	nA
		$V_R = 60 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	235	700	nA
		$V_R = 60 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 125 ^{\circ}\text{C}$	-	285	1400	μΑ
C <sub>d</sub>	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$	-	220	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	135	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	88	-	pF
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	9	-	ns
$V_{FRM}$	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$	-	565	-	mV



(1)  $T_i = 175 \,^{\circ}C$ 

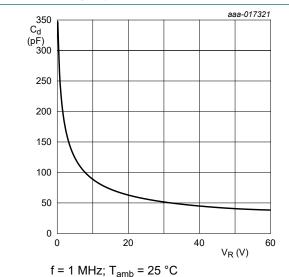
(2)  $T_i = 150 \,^{\circ}\text{C}$ 

 $(3) T_i = 125 °C$ 

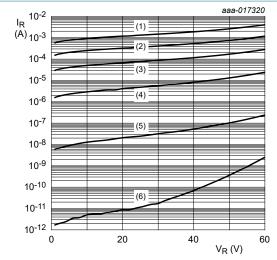
 $(4) T_i = 85 ^{\circ}C$ (5)  $T_i = 25 °C$ 

(6)  $T_i = -40 \, ^{\circ}\text{C}$ 

Fig. 4. Forward current as a function of forward voltage; typical values



Diode capacitance as a function of reverse Fig. 6. voltage; typical values



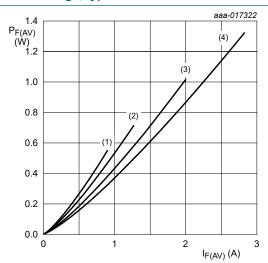
(1)  $T_j = 175 \,^{\circ}\text{C}$ (2)  $T_j = 150 \,^{\circ}\text{C}$ 

 $(3) T_i = 125 °C$ 

(4)  $T_i = 85$  °C

(5)  $T_i = 25 °C$ (6)  $T_i = -40 \,^{\circ}\text{C}$ 

Fig. 5. Reverse current as a function of reverse voltage; typical values



T<sub>i</sub> = 175 °C

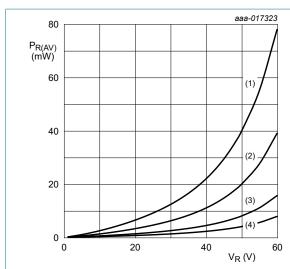
 $(1) \delta = 0.1$ 

 $(2) \delta = 0.2$ 

 $(3) \delta = 0.5$ 

 $(4) \delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 150 °C

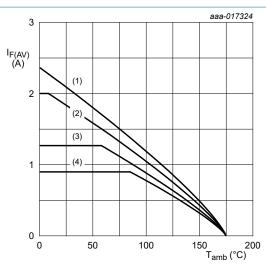
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

 $(4) \delta = 0.1$ ; f = 20 kHz

Average reverse power dissipation as a Fig. 8. function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$ 

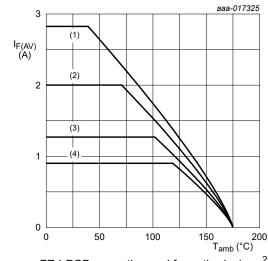
 $(1) \delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Average forward current as a function of Fig. 9. ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup> T<sub>i</sub> = 175 °C

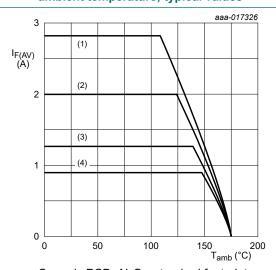
 $(1) \delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>j</sub> = 175 °C

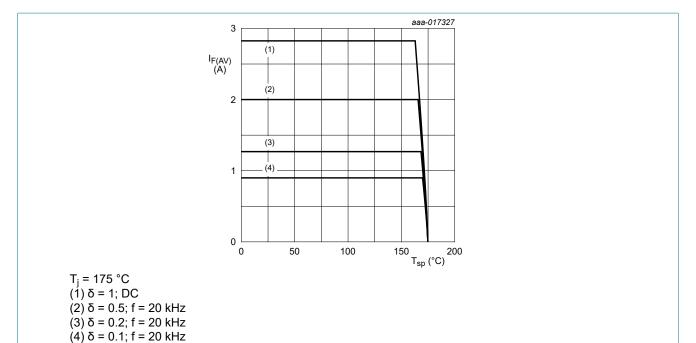
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

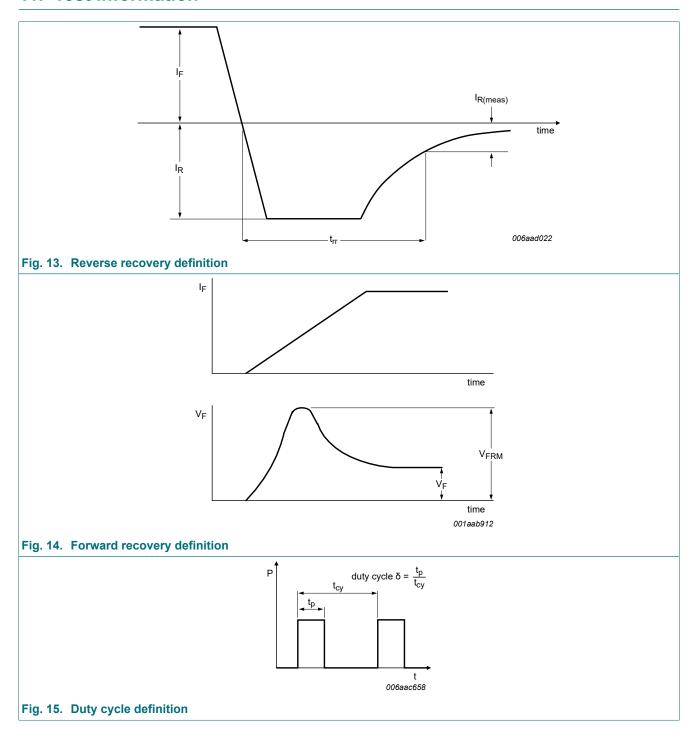
(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



### 11. Test information

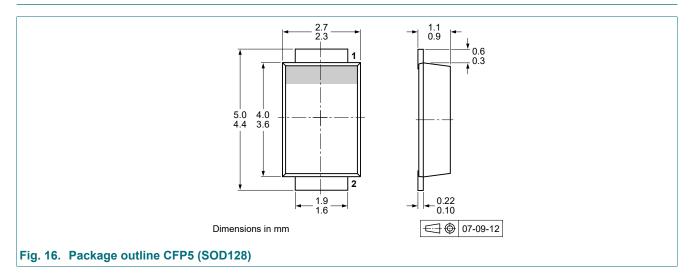


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

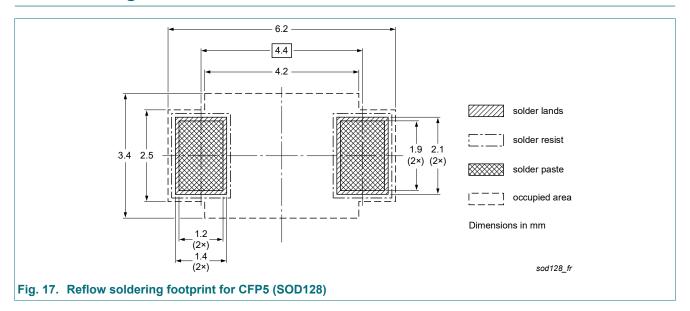
#### **Quality information**

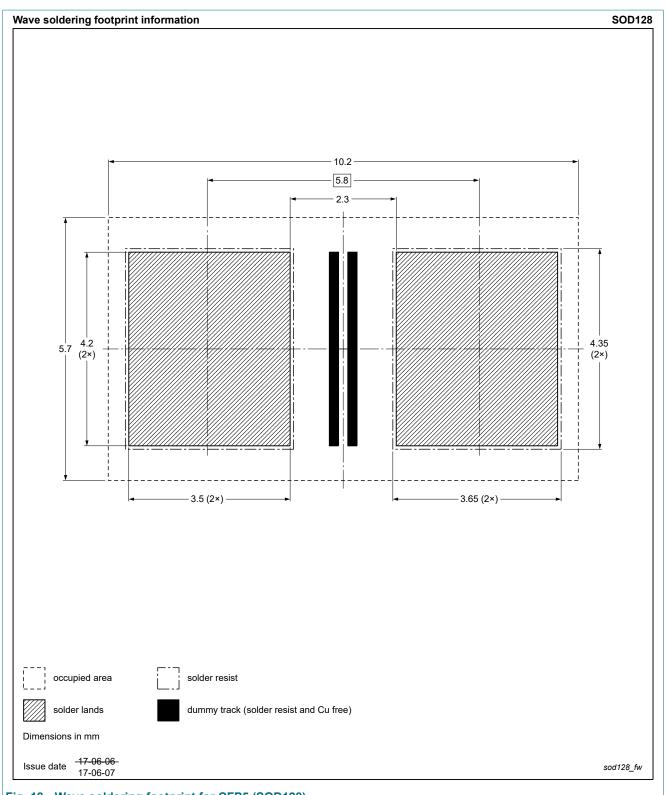
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



## 13. Soldering





# 14. Revision history

## Table 8. Revision history

table of Nevision History								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMEG6020AELP v.2	20190304	Product data sheet	-	PMEG6020AELP v.1				
Modifications:		Features and benefits: Capable for reflow and wave soldering added Soldering: Wave soldering footprint added						
PMEG6020AELP v.1	20150508	Product data sheet	-	-				

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMEG6020AELP

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For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 4 March 2019

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