

**Product data sheet** 

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	30	V
V <sub>GS</sub>	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	1	Α
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 0.9 \text{ A}; T_j = 25 \text{ °C}$		-	212	254	mΩ

<sup>[1]</sup> Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



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# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	□ 3	D I
2	S	source		
3	D	drain	1 2 SC-70 (SOT323)	G S 017aaa255

## 6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMF250XNE	SC-70	plastic surface-mounted package; 3 leads	SOT323			

# 7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PMF250XNE	Z%W

[1] % = placeholder for manufacturing site code

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## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	30	V
$V_{GS}$	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	$V_{GS} = 4.5 \text{ V}; T_{amb} = 25 \text{ °C}; t \le 5 \text{ s}$	[1]	-	1	Α
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	0.9	Α
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	0.5	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	4	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	275	mW
			[1]	-	342	mW
		T <sub>sp</sub> = 25 °C		-	1.1	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drain	n diode			'		
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.3	Α

<sup>[1]</sup> Device mounted on an FR4 Printed Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

<sup>[2]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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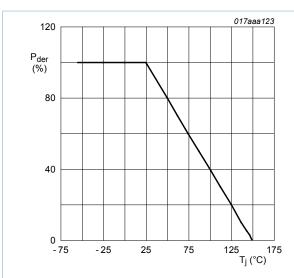


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

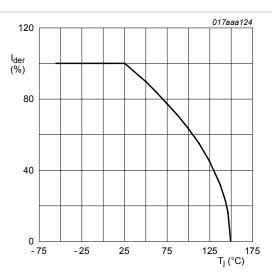


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

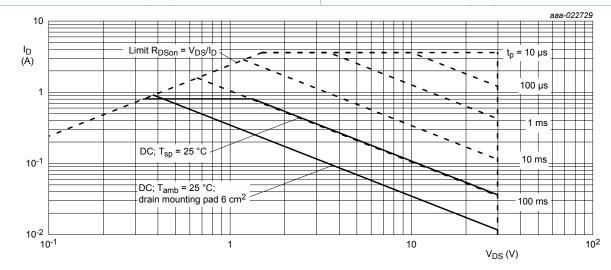


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

### 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance	in free air	[1]	-	397	457	K/W
	from junction to ambient		[2]	-	318	366	K/W
		t ≤ 5 s	[2]	-	256	294	K/W

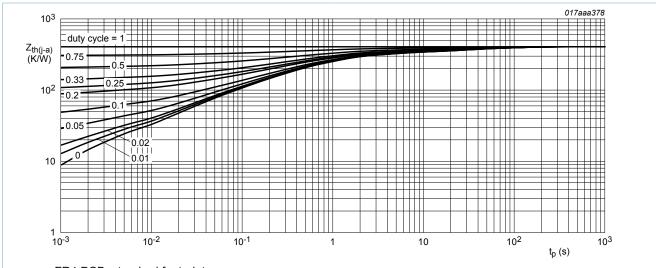
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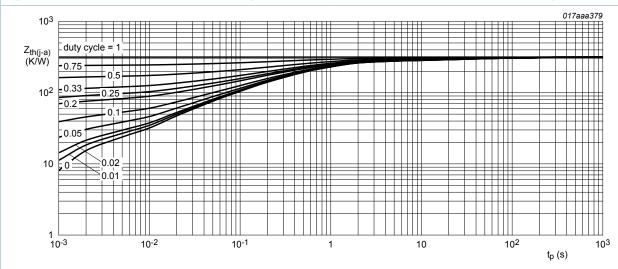
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	102	117	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 6 cm<sup>2</sup>

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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### 10. Characteristics

### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
$V_{(BR)DSS}$	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	30	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	0.75	1	1.25	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 30 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-10	μA
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	5	μΑ
		V <sub>GS</sub> = -4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-5	μA
R <sub>DSon</sub> drain-source on-state resistance	drain-source on-state	$V_{GS}$ = 4.5 V; $I_D$ = 0.9 A; $T_j$ = 25 °C	-	212	254	mΩ
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 0.9 A; T <sub>j</sub> = 150 °C	-	346	416	mΩ
		$V_{GS}$ = 2.5 V; $I_D$ = 0.8 A; $T_j$ = 25 °C	-	269	321	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_D$ = 0.9 A; $T_j$ = 25 °C	-	3.5	-	S
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 0.9 A; V <sub>GS</sub> = 4.5 V;	-	1.05	1.65	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C	-	0.15	-	nC
$Q_{GD}$	gate-drain charge		-	0.27	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 15 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	81	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	13	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	8.5	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 15 V; I <sub>D</sub> = 0.9 A; V <sub>GS</sub> = 4.5 V;	-	7	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega$ ; $T_j = 25 °C$	-	14	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	17	-	ns
t <sub>f</sub>	fall time		-	6	-	ns
Source-dra	in diode		(			
V <sub>SD</sub>	source-drain voltage	$I_S = 0.3 \text{ A; } V_{GS} = 0 \text{ V; } T_i = 25 \text{ °C}$	-	0.7	1.2	V

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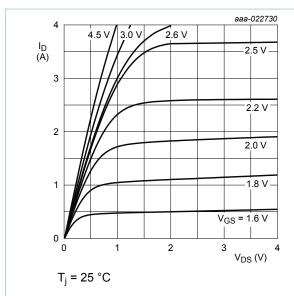


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

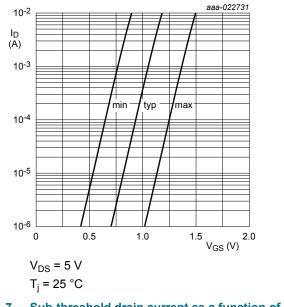


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

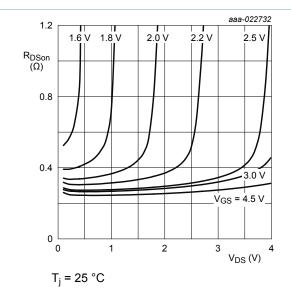


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

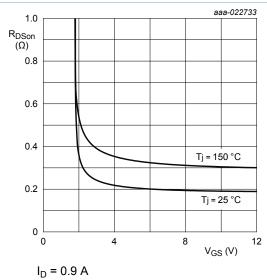


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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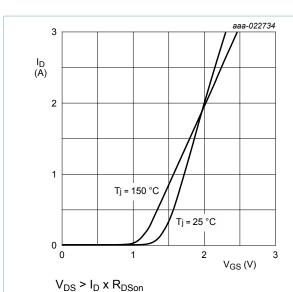


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

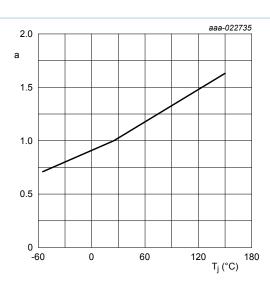


Fig. 11. Normalized drain-source on-state resistance as a function of ambient temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

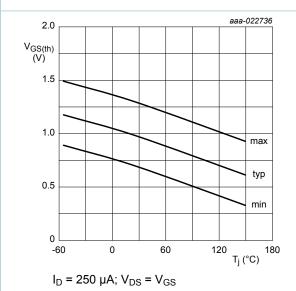


Fig. 12. Gate-source threshold voltage as a function of ambient temperature

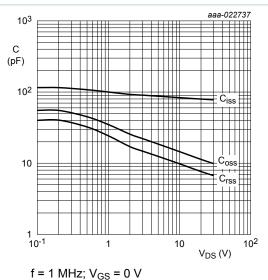


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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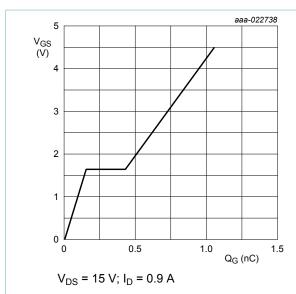


Fig. 14. Gate-source voltage as a function of gate charge; typical values

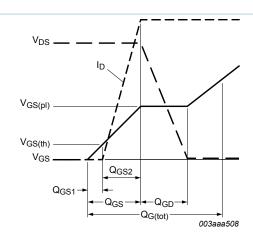


Fig. 15. Gate charge waveform definitions

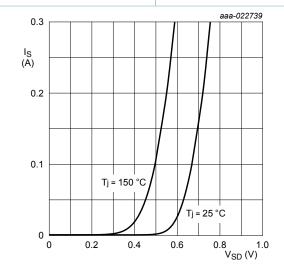
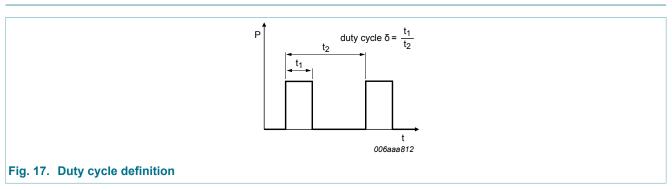


Fig. 16. Source current as a function of source-drain voltage; typical values

### 11. Test information

 $V_{GS} = 0 V$ 

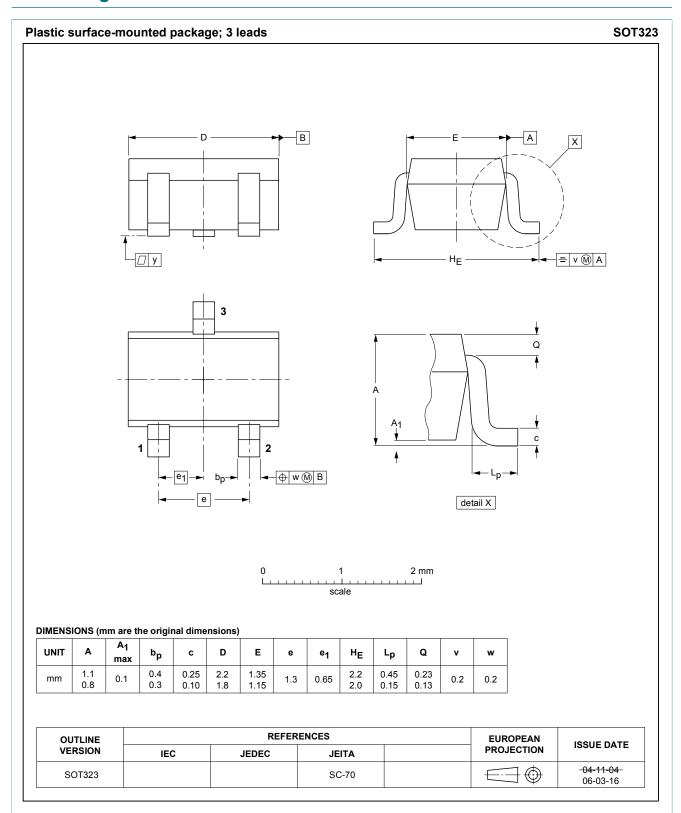


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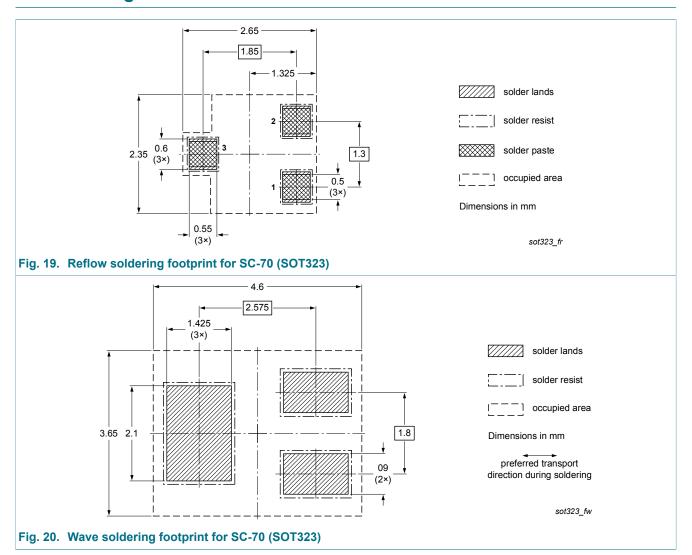
### 12. Package outline



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### 13. Soldering



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# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMF250XNE v.1	20160428	Product data sheet	-	-

#### **30V N-channel Trench MOSFET**

### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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