

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Leadless ultra small and ultra thin SMD plastic package: 1.1 × 1.0 × 0.37 mm
- Exposed drain pad for excellent thermal conduction
- ElectroStatic Discharge (ESD) protection 1.5 kV HBM
- Drain-source on-state resistance R_{DSon} = 59 m Ω
- Very low gate-source threshold voltage for portable applications V_{GS(th)} = -0.68 V

3. Applications

- High-side load switch and charging switch for portable devices
- Power management in battery driven portables
- LED driver
- DC-to-DC converter

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-12	V
V _{GS}	gate-source voltage	_		-8	-	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C	[1]	-	-	-3.2	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -3.2 A; T _j = 25 °C		-	59	72	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		D
2	S	source		
3	D	drain	4 3	G (The second s
4	D	drain	Transparent top view DFN1010D-3 (SOT1215)	S 017aaa259

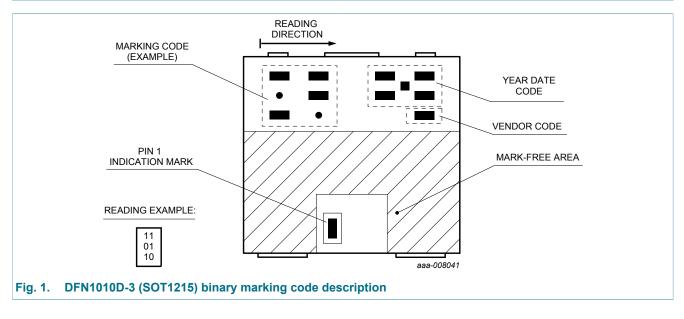
6. Ordering information

Table 3. Ordering in	formation		
Type number Package			
	Name	Description	Version
PMXB65UPE	DFN1010D-3	DFN1010D-3: plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body 1.1 x 1.0 x 0.37 mm	SOT1215

7. Marking

Table 4. Marking codes

Type number	Marking code
PMXB65UPE	01 10 00



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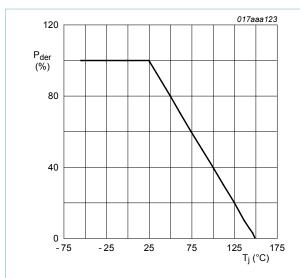
8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

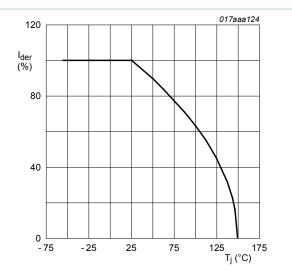
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-12	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C	[1]	-	-3.2	А
		V_{GS} = -4.5 V; T_{amb} = 100 °C	[1]	-	-2.1	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-13	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	317	mW
			[1]	-	1070	mW
		T _{sp} = 25 °C		-	8330	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode					_
I _S	source current	T _{amb} = 25 °C	[1]	-	-1	А

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.





$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$





$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100 \%$$

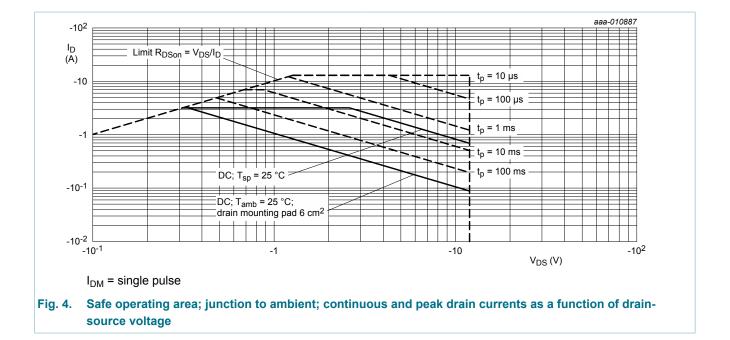
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9. Thermal characteristics

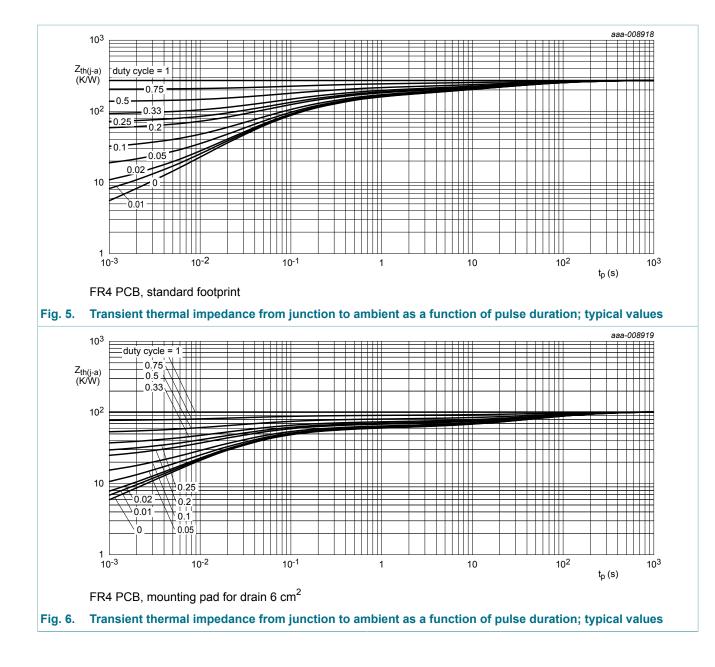
Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	271	312	K/W
	from junction to ambient		[2]	-	102	117	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	10	15	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics	· · · ·				
V _{(BR)DSS}	drain-source breakdown voltage	I_D = -250 µA; V_{GS} = 0 V; T_j = 25 °C	-12	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = -250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	-0.4	-0.68	-1	V
I _{DSS}	drain leakage current	V _{DS} = -12 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
		V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V_{GS} = -4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-1	μA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	1	μA
R _{DSon}	drain-source on-state	V_{GS} = -4.5 V; I _D = -3.2 A; T _j = 25 °C	-	59	72	mΩ
resista	resistance	V_{GS} = -4.5 V; I _D = -3.2 A; T _j = 150 °C	-	80	98	mΩ
		V_{GS} = -2.5 V; I _D = -2.7 A; T _j = 25 °C	-	78	98	mΩ
		V_{GS} = -1.8 V; I _D = -0.4 A; T _j = 25 °C	-	120	200	mΩ
		V _{GS} = -1.5 V; I _D = -50 mA; T _j = 25 °C	-	198	450	mΩ
		V _{GS} = -1.2 V; I _D = -10 mA; T _j = 25 °C	-	880	-	mΩ
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -2 A; T _j = 25 °C	-	9.4	-	S
R _G	gate resistance	f = 1 MHz	-	8.7	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = -6 V; I _D = -3.2 A; V _{GS} = -4.5 V;	-	6.7	12	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1	-	nC
Q _{GD}	gate-drain charge		-	1.9	-	nC
C _{iss}	input capacitance	V_{DS} = -6 V; f = 1 MHz; V_{GS} = 0 V;	-	634	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	167	-	pF
C _{rss}	reverse transfer capacitance		-	146	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -6 V; I _D = -3.2 A; V _{GS} = -4.5 V;	-	6.2	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	22	-	ns
t _{d(off)}	turn-off delay time		-	27	-	ns
t _f	fall time		-	17	-	ns
Source-drai	n diode					
V _{SD}	source-drain voltage	I _S = -1 A; V _{GS} = 0 V; T _i = 25 °C	_	-0.7	-1.2	V

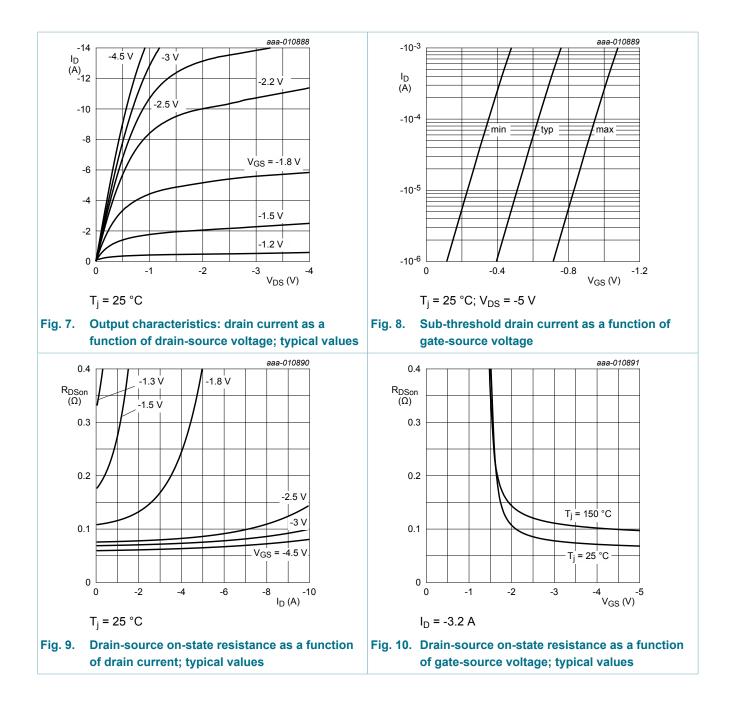
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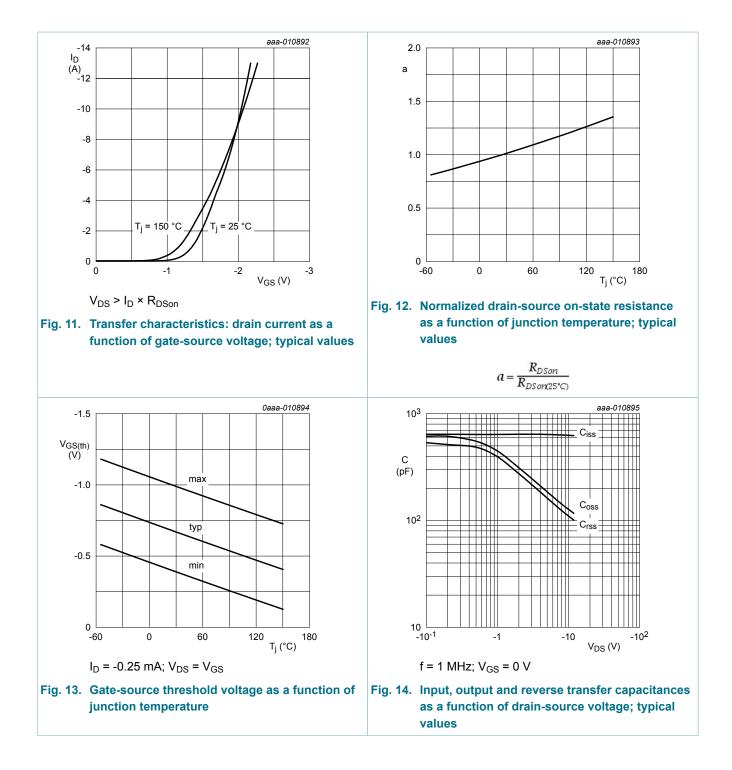
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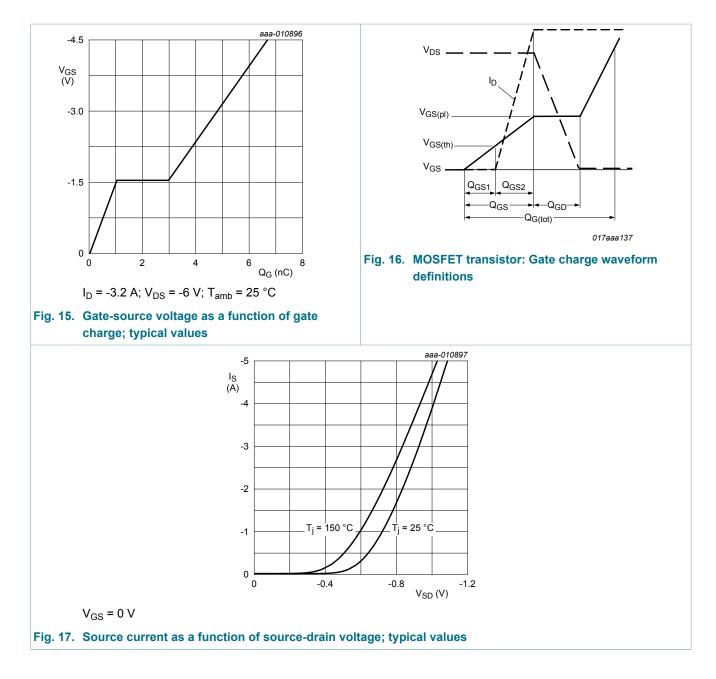
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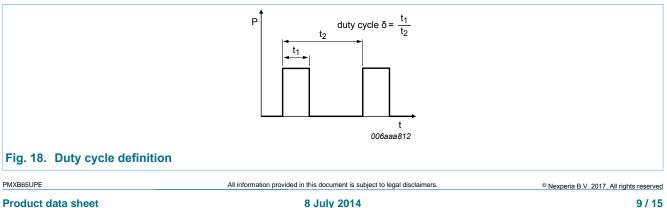
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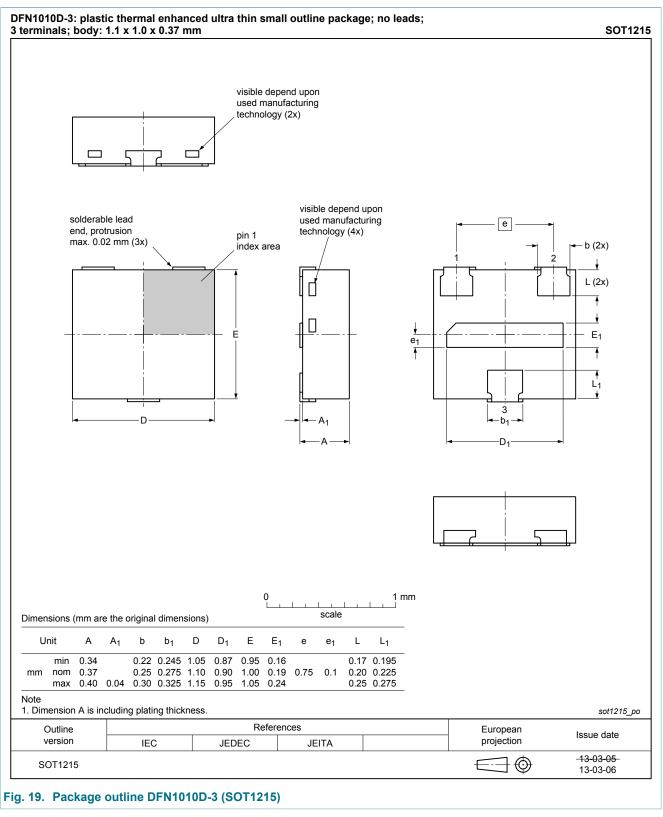
11. Test information



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12. Package outline



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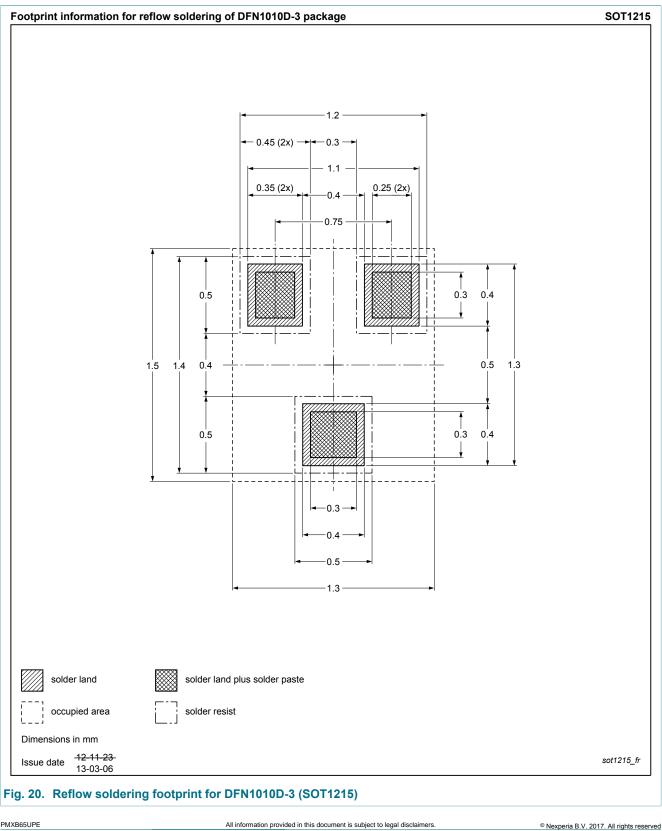
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13. Soldering



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14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMXB65UPE v.3	20140708	Product data sheet	-	PMXB65UPE v.2
Modifications:	Product status char	nged		
PMXB65UPE v.2	20140218	Preliminary data sheet	-	PMXB65UPE v.1
PMXB65UPE v.1	20140204	Preliminary data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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