

PSMN5R6-100PS

N-channel 100 V 5.6 mΩ standard level MOSFET in TO220
30 November 2012 Product data sheet

1. Product profile

1.1 General description

Standard level N-channel MOSFET in a TO-220 package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Improved dynamic avalanche performance
- Suitable for standard level gate drive sources

1.3 Applications

- DC-to-DC converters
- Load switching
- Motor control
- Server power supplies

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	100	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; <u>Fig. 1</u>	[1]	-	-	100	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	306	W
Static charact	eristics		-	'		'	
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11; Fig. 12		-	4.3	5.6	mΩ
Dynamic char	acteristics						
Q_{GD}	gate-drain charge	V _{GS} = 10 V; I _D = 80 A; V _{DS} = 50 V;		-	43	-	nC
Q _{G(tot)}	total gate charge	Fig. 13; Fig. 14		-	141	-	nC
Avalanche Ru	ggedness			'		'	
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_{D} = 100 A; $V_{sup} \le$ 100 V; R_{GS} = 50 Ω; unclamped		-	-	469	mJ



[1] Continious current limited by package.

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	D I
2	D	drain		
3	S	source		G-U-A
mb	D	mounting base; connected to drain		mbb076 S
			TO-220AB (SOT78)	

3. Ordering information

Table 3. Ordering information

Table 6. Ordering in	ioiiiiatioii					
Type number	Package					
	Name	Description	Version			
PSMN5R6-100PS	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78			

4. Marking

Table 4. Marking codes

Type number	Marking code
PSMN5R6-100PS	PSMN5R6-100PS

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	100	V
V_{DGR}	drain-gate voltage	$T_j \ge 25$ °C; $T_j \le 175$ °C; $R_{GS} = 20$ kΩ		-	100	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _j = 100 °C; <u>Fig. 1</u>		-	95	Α
		V _{GS} = 10 V; T _{mb} = 25 °C; <u>Fig. 1</u>	[1]	-	100	Α

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Symbol	Parameter	Conditions		Min	Max	Unit
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 °C$; Fig. 3		-	539	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	306	W
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
Source-drai	in diode					
I _S	source current	T _{mb} = 25 °C	[1]	-	100	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	539	Α
Avalanche I	Ruggedness			'		
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_{D} = 100 A; $V_{sup} \le$ 100 V; R_{GS} = 50 Ω; unclamped		-	469	mJ

[1] Continious current limited by package.

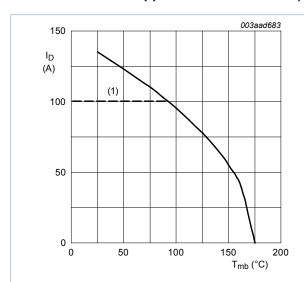


Fig. 1. Continuous drain current as a function of mounting base temperature

 $V_{GS} \ge 10 \text{ V}$; (1) capped at 100 A due to package.

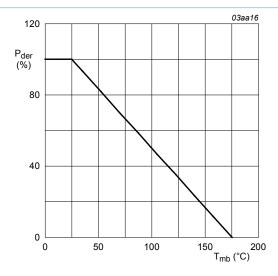


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

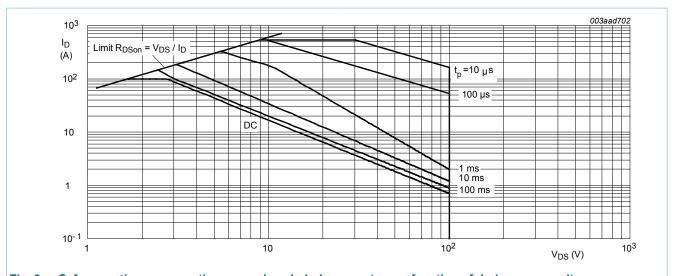


Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

 $T_{mb} = 25$ °C; I_{DM} is a single pulse; (1) Capped at 100 A due to package

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 4	_	0.3	0.49	K/W

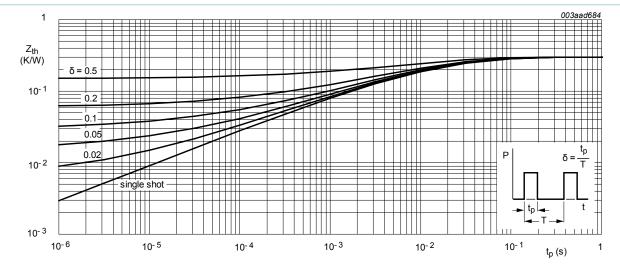


Fig. 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics					
V _{(BR)DSS}	drain-source	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	100	-	-	V
	breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = -55 °C	90	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 8; Fig. 9	2	3	4	V
V_{GSth}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 9	-	-	4.6	V
I _{DSS}	drain leakage current	V _{DS} = 100 V; V _{GS} = 0 V; T _j = 25 °C	-	0.02	10	μA
		V _{DS} = 100 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μA
I _{GSS}	gate leakage current	V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
		V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon} drain-source on-state resistance		V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; Fig. 10	-	-	15.7	mΩ
	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11; Fig. 12	-	4.3	5.6	mΩ	
R _G	gate resistance	f = 1 MHz	-	0.97	-	Ω
Dynamic c	haracteristics		,			•
Q _{G(tot)}	total gate charge	I _D = 80 A; V _{DS} = 50 V; V _{GS} = 10 V;	-	141	-	nC
Q_{GS}	gate-source charge	Fig. 13; Fig. 14	-	36	-	nC
Q_{GD}	gate-drain charge		-	43	-	nC
C _{iss}	input capacitance	V _{DS} = 50 V; V _{GS} = 0 V; f = 1 MHz; T _j = 25 °C; <u>Fig. 15</u> ; <u>Fig. 16</u>	-	8061	-	pF
C _{oss}	output capacitance	$V_{DS} = 50 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}; Fig. 15$	-	561	-	pF
C _{rss}	reverse transfer capacitance	V _{DS} = 50 V; V _{GS} = 0 V; f = 1 MHz; T _j = 25 °C; <u>Fig. 15</u> ; <u>Fig. 16</u>	-	330	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 50 \text{ V}; R_L = 0.6 \Omega; V_{GS} = 10 \text{ V};$	-	31	-	ns
t _r	rise time	$R_{G(ext)} = 1.5 \Omega$	-	46	-	ns
t _{d(off)}	turn-off delay time		-	83	-	ns
t _f	fall time		-	34	-	ns
Source-dra	in diode			-	1	
V_{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _i = 25 °C; <u>Fig. 17</u>	-	0.79	1.2	V

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t _{rr}	reverse recovery time	$I_S = 25 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$	_	67	-	ns
Q _r	recovered charge	$V_{DS} = 50 \text{ V}$	-	182	-	nC

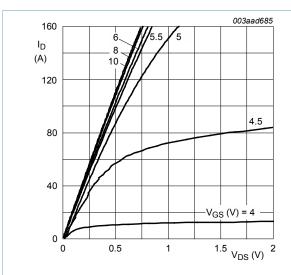


Fig. 5. Output characteristics: drain current as a function of drain-source voltage; typical values

$$T_i = 25$$
 °C

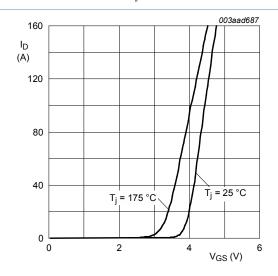


Fig. 7. Transfer characteristics: drain current as a function of gate-source voltage; typical values

$$V_{DS} > I_D \times R_{DSon}$$

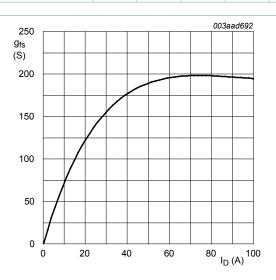


Fig. 6. Forward transconductance as a function of drain current; typical values

$$T_j = 25$$
 °C; $V_{DS} = 25$ V

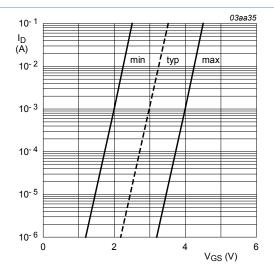


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

$$T_j = 25 \,^{\circ}C; V_{DS} = 5V$$

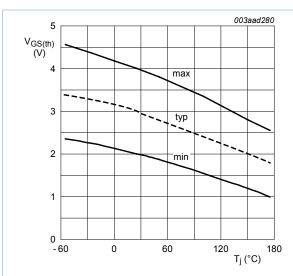


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$$I_D = 1 \text{ mA}; \ V_{DS} = V_{GS}$$

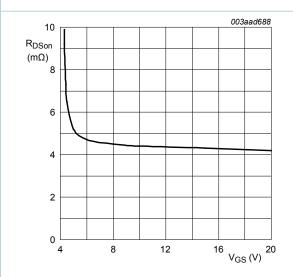


Fig. 11. Drain-source on-state resistance as a function of gate-source voltage; typical values

$$T_j = 25$$
 °C; $I_D = 25$ A

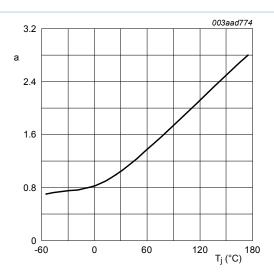


Fig. 10. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25 \, ^{\circ}\text{C})}}$$

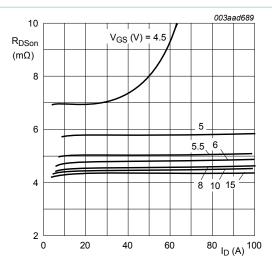


Fig. 12. Drain-source on-state resistance as a function of drain current; typical values

$$T_j = 25 \,^{\circ}C$$

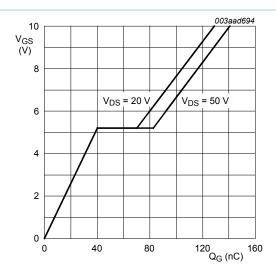


Fig. 13. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25$$
 °C; $I_D = 25$ A

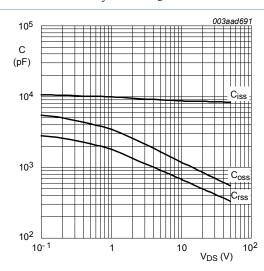


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0 \text{ V; } f = 1 \text{MHz}$$

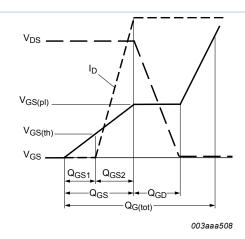


Fig. 14. Gate charge waveform definitions

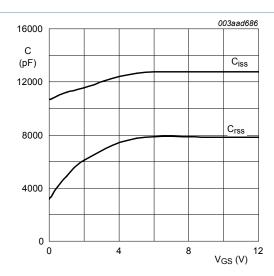


Fig. 16. Input and reverse transfer capacitances as a function of gate-source voltage, typical values

$$f = 1 \text{ MHz}; V_{DS} = 0 \text{ V};$$

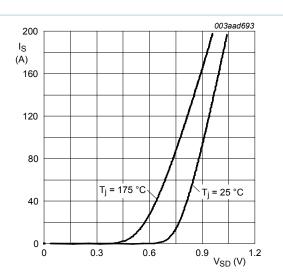
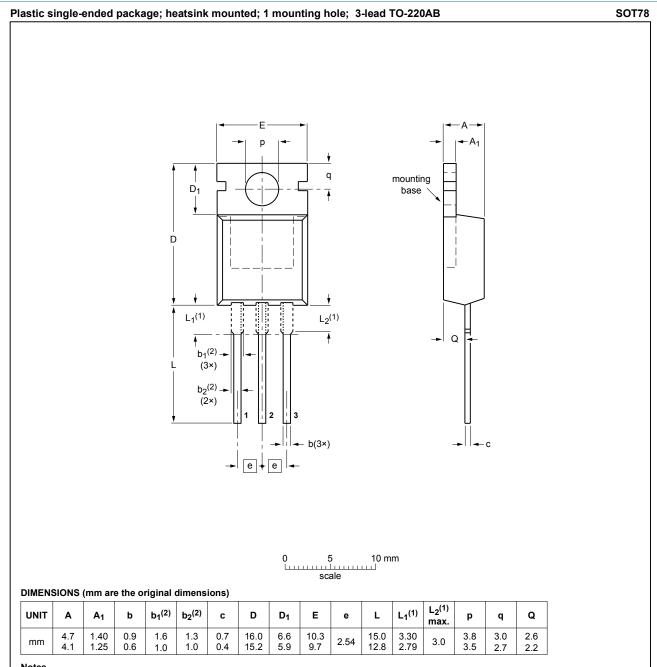


Fig. 17. Source current as a function of source-drain voltage; typical values

$$V_{GS} = 0 \text{ V}$$

Package outline



- 1. Lead shoulder designs may vary.
- 2. Dimension includes excess dambar.

OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE
SOT78		3-lead TO-220AB	SC-46			08-04-23 08-06-13

Fig. 18. Package outline TO-220AB (SOT78)

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