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February 2016



# KSP2907A PNP General-Purpose Amplifier

# Features

- Collector-Emitter Voltage: V<sub>CEO</sub> = -60 V
- Suffix "-C" means a Center Collector (1.Emitter 2.Collector 3.Base)



Straight Lead Bulk Packing

Tape & Reel Ammo Packing

KSP2907A: 1. Emitter 2. Base 3. Collector KSP2907AC: 1. Emitter 2. Collector 3. Base

# Ordering Information<sup>(1)</sup>

Part Number	Marking	Package	Packing Method
KSP2907ABU	KSP2907A	TO-92 3L	Bulk
KSP2907ATA	KSP2907A	TO-92 3L	Ammo
KSP2907ATF	KSP2907A	TO-92 3L	Tape and Reel
KSP2907ACTA	KSP2907AC	TO-92 3L	Ammo

# Note:

1. Affix "-C-" means center collector pin. Suffix "-BU" means bulk packing, and straight lead form. Suffix "-TF" means tape & reel packing, and 0.200 in-line spacing lead form. Suffix "-TA" means ammo packing, and 0.200 in-line spacing lead form.

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-60	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
۱ <sub>C</sub>	Collector Current	-600	mA
ТJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to +150	°C

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# **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
Р	Power Dissipation by R <sub>0JA</sub>	625	mW
PD	Derate Above 25°C	5	mW/°C
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case <sup>(2)</sup>	83.3	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient <sup>(3)</sup>	200	°C/W

## Notes:

2. Infinite heat sink.

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

# **Electrical Characteristics**<sup>(4)</sup>

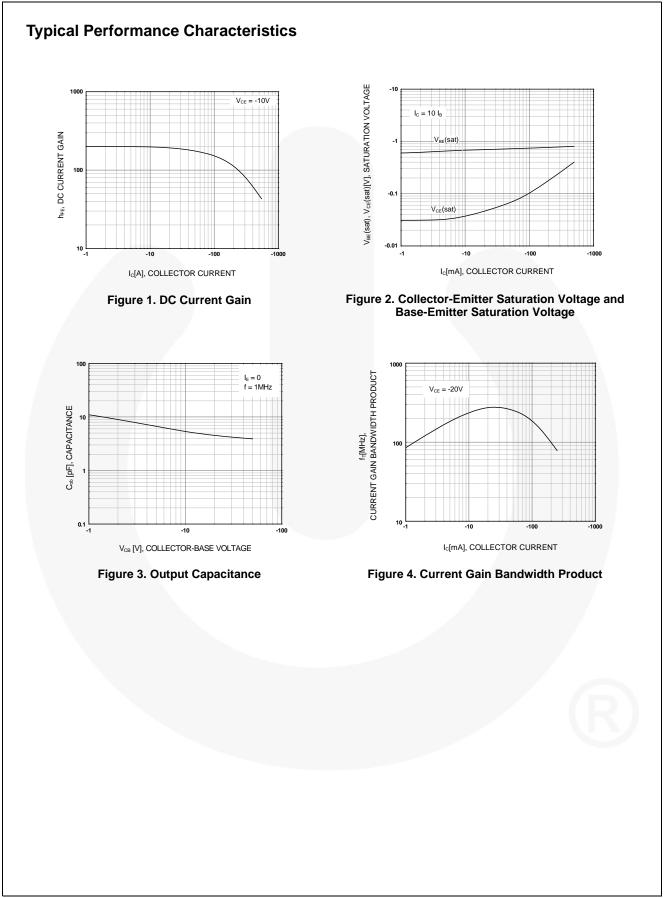
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = -10 \ \mu {\rm A}, \ I_{\rm E} = 0$	-60		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-60		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_{E} = -10 \ \mu A, I_{C} = 0$	-5.0		V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -50 \text{ V}, \text{ I}_{E} = 0$		-10	nA
	DC Current Gain	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	75		
h <sub>FE</sub> [		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -1 mA	100		
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -10 mA	100		
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -150 mA	100	300	
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -500 mA	50		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-0.4	V
	Collector-Emilier Saturation voltage	I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-1.6	
V (aat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-1.3	v
V <sub>BE</sub> (sat) Bas	base-Emilier Saturation voltage	I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-2.6	
C <sub>obo</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1.0 MHz		8	pF
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -20 V, f = 100 MHz	200		MHz
t <sub>ON</sub>	Turn-On Time	$V_{CC} = -30 \text{ V}, \text{ I}_{C} = -150 \text{ mA},$ $\text{I}_{B1} = -15 \text{ mA}$		45	ns
t <sub>OFF</sub>	Turn-Off Time	$V_{CC} = -6 V$ , $I_C = -150 mA$ , $I_{B1} = I_{B2} = -15 mA$		100	ns

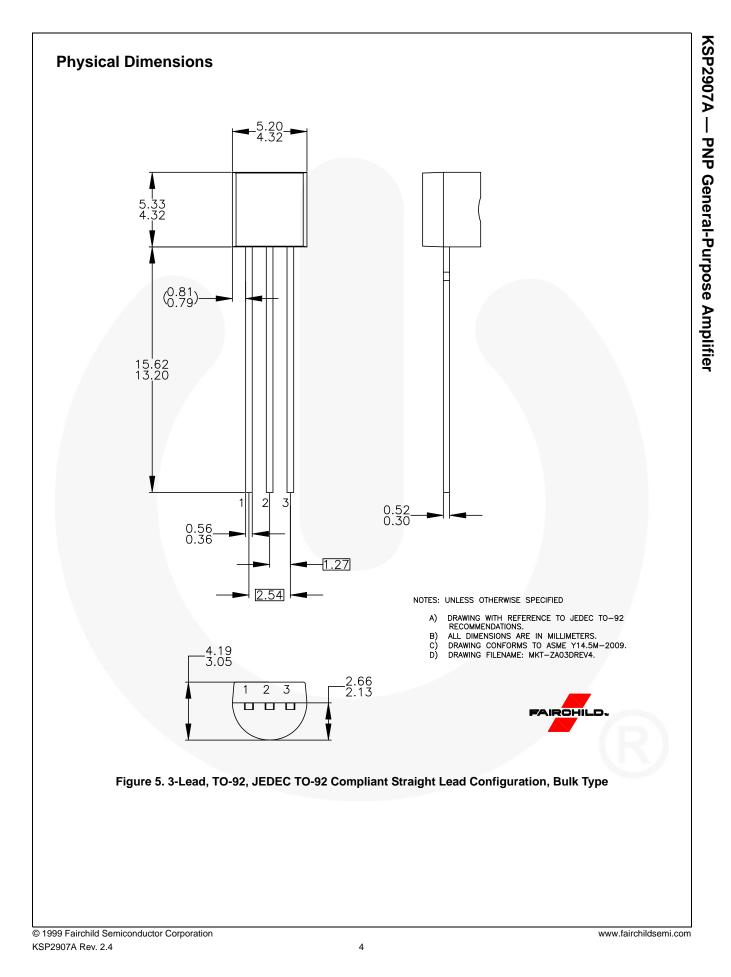
## Note:

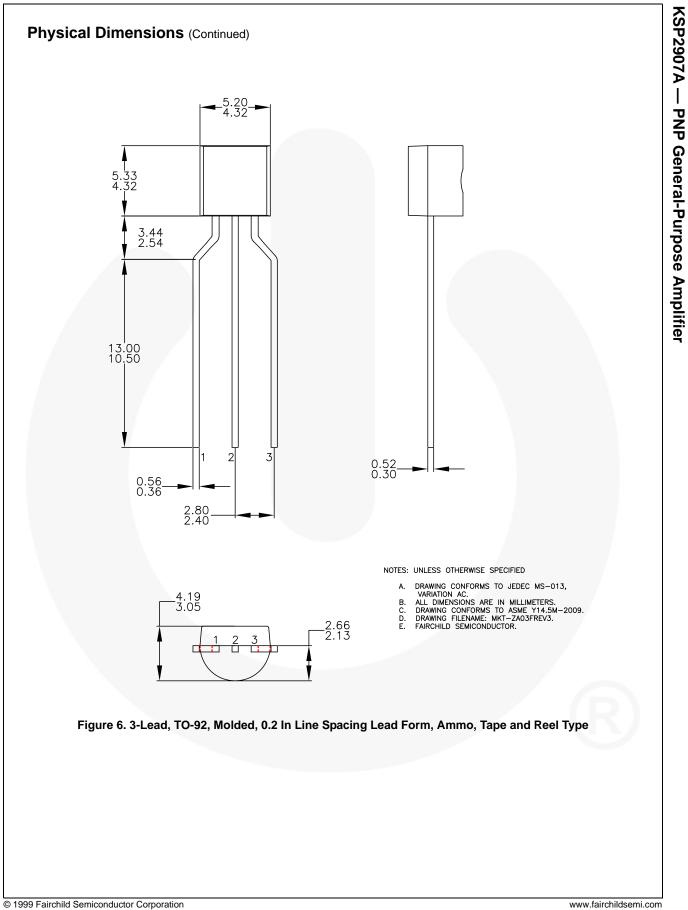
4. DC items are tested by pulse test: pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2\%$ 

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KSP2907A — PNP General-Purpose Amplifier





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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