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#### November 2013



## FCP380N60 / FCPF380N60 N-Channel SuperFET<sup>®</sup> II MOSFET 600 V, 10.2 A, 380 mΩ

#### **Features**

- 650 V @ T<sub>1</sub> = 150°C
- Typ. R<sub>DS(on)</sub> = 330 mΩ
- Ultra Low Gate Charge (Typ. Q<sub>q</sub> = 30 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 95 pF)
- 100% Avalanche Tested
- RoHS Compliant

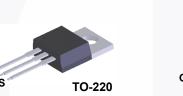
### Applications

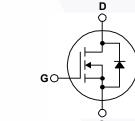
- LCD / LED / PDP TV Lighting
- · Solar Inverter
- AC-DC Power Supply

## Description

TO-220F

SuperFET<sup>®</sup> II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.





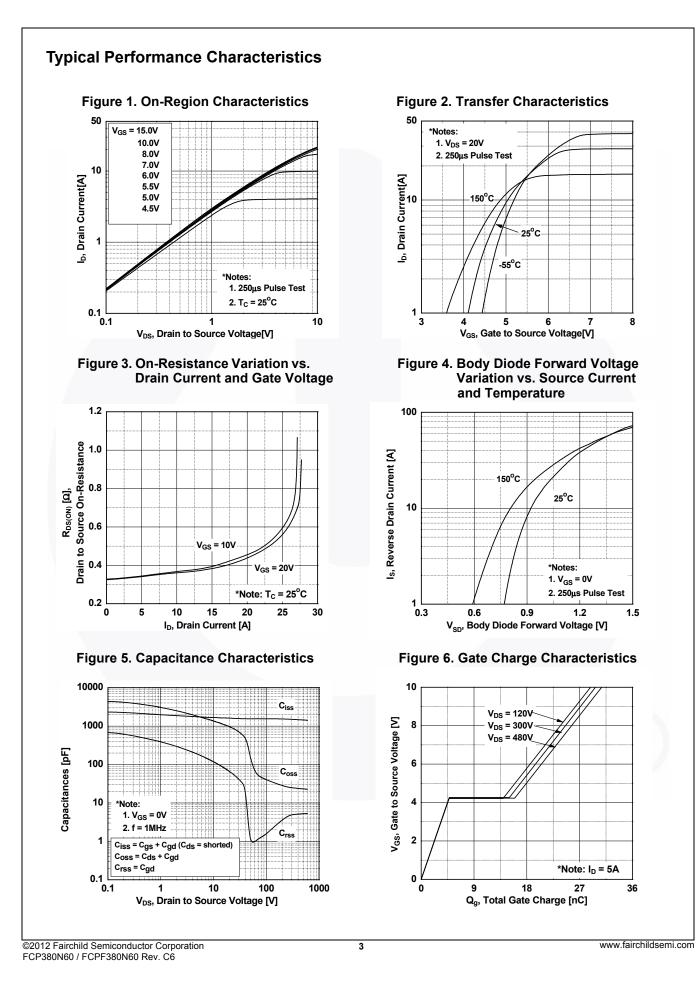
#### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

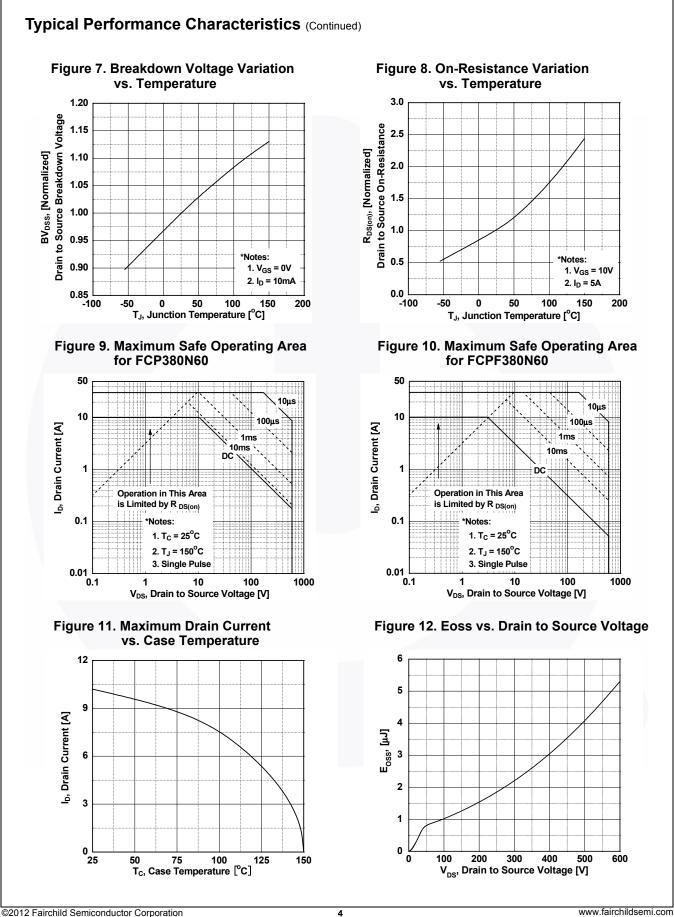
Symbol		FCP380N60	FCPF380N60	Unit		
V <sub>DSS</sub>	Drain to Source Voltage			6	V	
V <sub>GSS</sub>	Cata ta Cauraa Maltana	- DC	- DC - AC (f > 1 Hz)			V
	Gate to Source Voltage	- AC				
ID	Duain Currant	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)			•
	Drain Current	- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)		6.4	6.4*	A
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	30.6	30.6*	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)			21	mJ	
I <sub>AR</sub>	Avalanche Current	(Note 1)	2.3		А	
E <sub>AR</sub>	Repetitive Avalanche Energy			1.	mJ	
dv/dt	MOSFET dv/dt	1	V/ns			
	Peak Diode Recovery dv/d	(Note 3)	20		v/ns	
P <sub>D</sub>	Dower Dissinction	(T <sub>C</sub> = 25 <sup>o</sup> C)	(T <sub>C</sub> = 25°C)		31	W
	Power Dissipation	- Derate Above 25°C	- Derate Above 25°C			W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to	°C	
Γ <sub>L</sub>	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			3	°C	
Drain current	limited by maximum junction ter	mperature.	1			

#### **Thermal Characteristics**

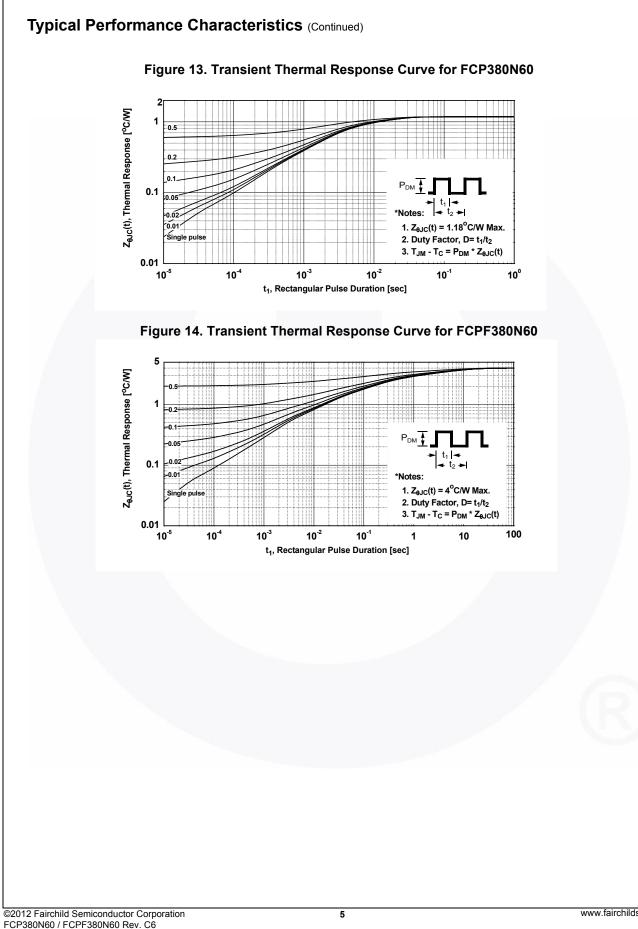
Symbol	Parameter	FCP380N60	FCPF380N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.18	4	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	0/11

Part Number FCP380N60 FCPF380N60		Top Mark	Packa	age Packing Method Reel Size		Tape Width		Qua	ntity	
		FCP380N60	TO-22	20	Tube	N/A		N/A	50 units	
		FCPF380N60	TO-220	220F Tube N/A		N/A		50 units		
Electrica	I Char	acteristics T <sub>c</sub> = 2	25°C unless	s othe	rwise noted.					
Symbol		Parameter			Test Conditio	ons	Min.	Тур.	Max.	Unit
Off Charac	teristic	s						<u> </u>		
		•	-	V.	-0.1/1 - 10 m	T. = 25°C	600	-		
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage		tage	$V_{GS} = 0 V, I_D = 10 mA, T_J = 25^{\circ}C$ $V_{GS} = 0 V, I_D = 10 mA, T_J = 150^{\circ}C$			-	- 650	-	V
∆BV <sub>DSS</sub>	Breakdown Voltage Temperature		e							
$/\Delta T_J$	Coefficient			$I_D = 10 \text{ mA}$ , Referenced to $25^{\circ}C$			-	0.6	-	V/ºC
BV <sub>DS</sub>	Drain to Source Avalanche Breakdown Voltage		akdown	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 A			÷	700	-	V
	Zero Gate Voltage Drain Current			V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V		-	-	1	μA	
DSS	200 08			V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125°C			-	-	10	μΑ
GSS	Gate to	Body Leakage Current		$V_{GS} = \pm 20 V, V_{DS} = 0 V$			-	-	±100	nA
On Charac	torictic	-								
		-	_				0.5		0.5	
V <sub>GS(th)</sub>		reshold Voltage rain to Source On Resis	tanaa	$V_{GS} = V_{DS}, I_D = 250 \mu A$			2.5	-	3.5	V
R <sub>DS(on)</sub>		Transconductance	stance		<sub>S</sub> = 10 V, I <sub>D</sub> = 5 A <sub>S</sub> = 20 V, I <sub>D</sub> = 5 A		-	0.33	0.38	Ω S
9FS	FOIWAR	Transconductance	-	۷D	s - 20 v, i <u>D</u> - 5 A		-	11	-	3
Dynamic C	haracte	eristics								
C <sub>iss</sub>	Input Ca	apacitance					-	1250	1665	pF
C <sub>oss</sub>		Capacitance			$_{\rm S}$ = 25 V, V <sub>GS</sub> = 0 V,		-	905	1205	pF
Crss	Reverse Transfer Capacitance			f = 1 MHz		-	45	60	pF	
C <sub>oss</sub>		Output Capacitance		V <sub>DS</sub> = 380 V, V <sub>GS</sub> = 0 V, f = 1 MHz			-	23	-	pF
Coss(eff.)		Effective Output Capacitance		$V_{\rm DS} = 0 \text{ V to } 480 \text{ V}, \text{ V}_{\rm GS} = 0 \text{ V}$			-	95	-	pF
Q <sub>g(tot)</sub>		ate Charge at 10V		$V_{\rm DS} = 380 \text{ V}, I_{\rm D} = 5 \text{ A},$		-	30	40	nC	
Q <sub>gs</sub>		Source Gate Charge			$V_{\rm GS} = 10  \rm V$		-	5	-	nC
Q <sub>gd</sub>	Gate to	ate to Drain "Miller" Charge		(Note 4) f = 1 MHz			-	10	-	nC
ESR	Equivale						-	1	-	Ω
	<u> </u>			I					1	
Switching	Charac	teristics						4	1	
d(on)	Turn-On Delay Time         Turn-On Rise Time         Turn-Off Delay Time			$V_{DD}$ = 380 V, I <sub>D</sub> = 5 A, $V_{GS}$ = 10 V, R <sub>G</sub> = 4.7 Ω			14	38	ns	
r						-	7	24	ns	
d(off)				$V_{GS} = 10 V, R_G = 4.7 \Omega$			-	45	100	ns
f	Turn-Off	Fall Time				(Note 4)	-	6	22	ns
)rain-Sour	ce Dioc	le Characteristics								
				1e Fo	nward Current				10.2	A
-		num Pulsed Drain to Source Diode Fo								A
		to Source Diode Forward Voltage					-			V
			vollago			-			ns	
		erse Recovery Charge		$dI_{\rm F}/dt = 100  {\rm A}/{\mu {\rm s}}$			_		_	μC
$\frac{S}{SM}$ $\frac{V_{SD}}{V_{T}}$ $\frac{V_{T}}{V_{T}}$ Intersection of the second sec	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	m Continuous Drain to S m Pulsed Drain to Sourd Source Diode Forward Recovery Time	Source Dioc ce Diode Fo Voltage mperature.	orwar V <sub>G</sub> V <sub>G</sub>	d Current <sub>S</sub> = 0 V, I <sub>SD</sub> = 5 A <sub>S</sub> = 0 V, I <sub>SD</sub> = 5 A,		-	- - 240 2.7	10.2 30.6 1.2 -	

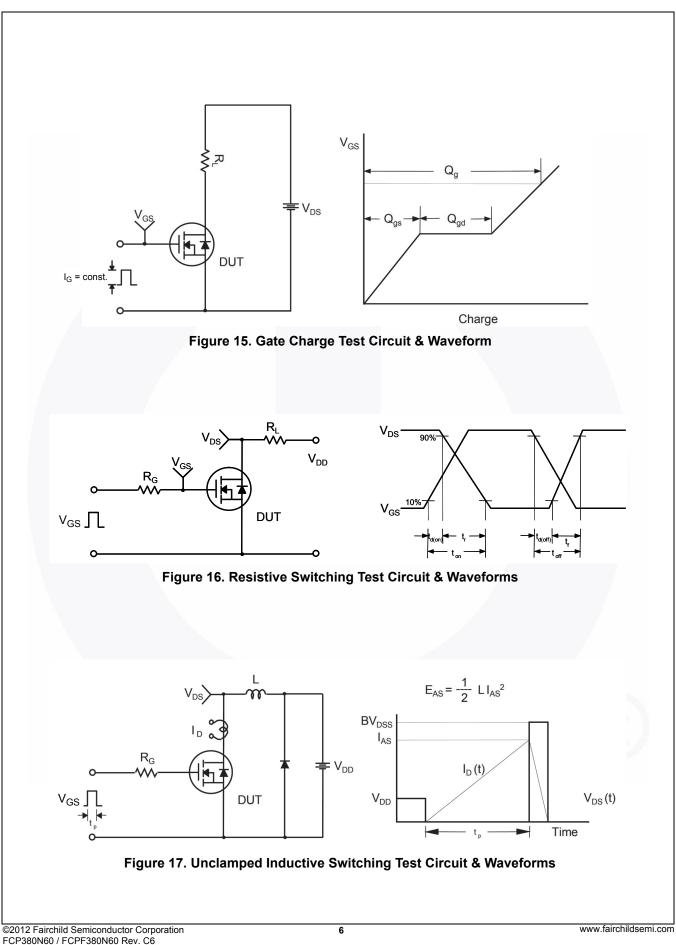


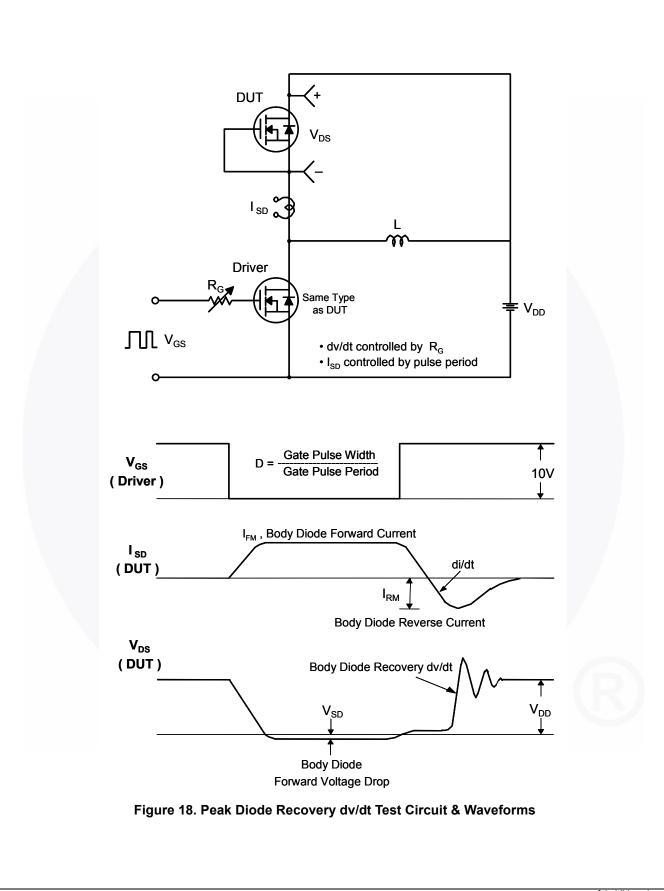


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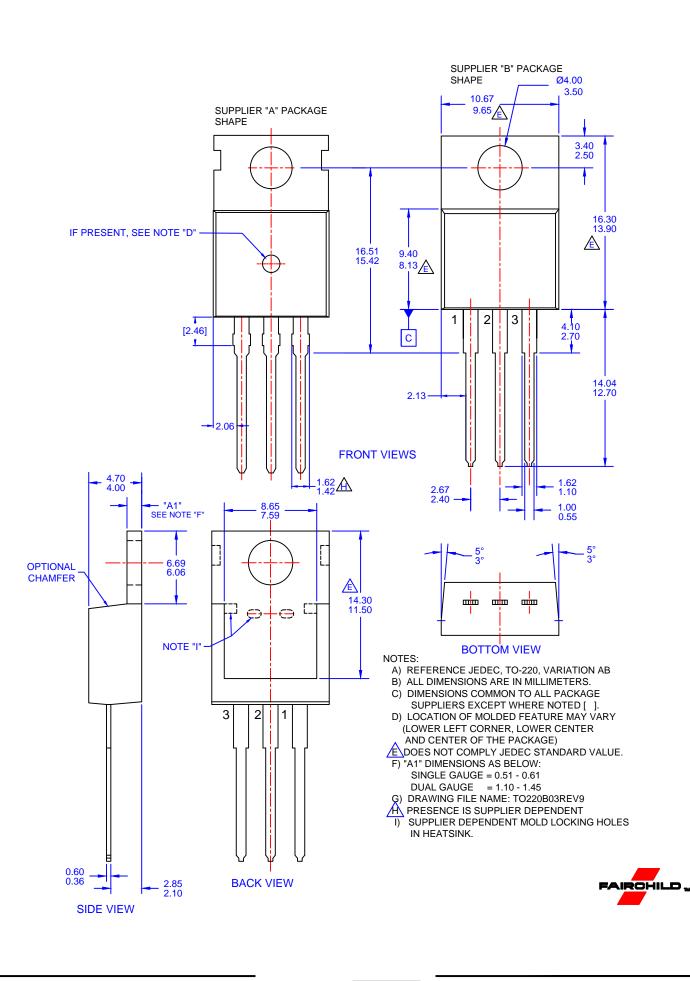


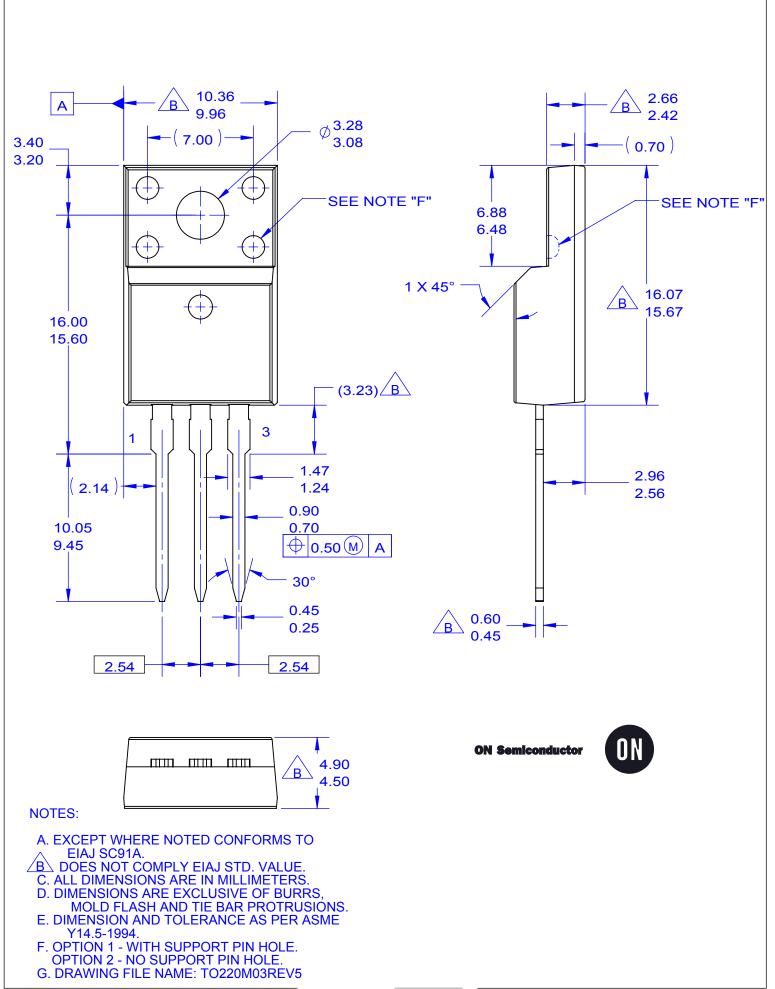
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