Complementary Silicon Plastic Power Transistors

Designed for use in general purpose amplifier and switching applications.

Features

- High Current Gain Bandwidth Product
- Compact TO-220 Package
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit				
Collector – Emitter Voltage TIP31G, TIP32G TIP31AG, TIP32AG TIP31BG, TIP32BG TIP31CG, TIP32CG	V _{CEO}	40 60 80 100	Vdc				
Collector–Base Voltage TIP31G, TIP32G TIP31AG, TIP32AG TIP31BG, TIP32BG TIP31CG, TIP32CG	V _{CB}	40 60 80 100	Vdc				
Emitter-Base Voltage	V _{EB}	5.0	Vdc				
Collector Current – Continuous	Ι _C	3.0	Adc				
Collector Current – Peak	I _{CM}	5.0	Adc				
Base Current	I _B	1.0	Adc				
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	40 0.32	W/∘C				
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	2.0 0.016	W/°C				
Unclamped Inductive Load Energy (Note 1)	E	32	mJ				
Operating and Storage Junction Tem- perature Range	T _J , T _{stg}	-65 to +150	°C				

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. $I_C = 1.8 \text{ A}, L = 20 \text{ mH}, P.R.F. = 10 \text{ Hz}, V_{CC} = 10 \text{ V}, R_{BE} = 100 \Omega$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	62.5	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.125	°C/W

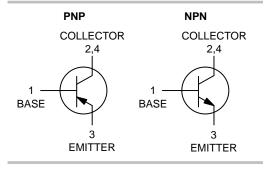
*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

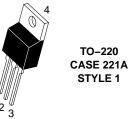


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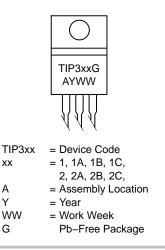
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3 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 40–60–80–100 VOLTS, 40 WATTS





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

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Characteristic	Symbol	Min	Max	Unit
DFF CHARACTERISTICS				1
Collector–Emitter Sustaining Voltage (Note 2) ($I_C = 30 \text{ mAdc}, I_B = 0$) TIP31G, TIP32G TIP31AG, TIP32AG TIP31BG, TIP32BG TIP31CG, TIP32CG	V _{CEO(sus)}	40 60 80 100	- - - -	Vdc
Collector Cutoff Current ($V_{CE} = 30 \text{ Vdc}, I_B = 0$) TIP31G, TIP32G, TIP31AG, TIP32AG ($V_{CE} = 60 \text{ Vdc}, I_B = 0$) TIP31BG, TIP31CG, TIP32BG, TIP32CG	I _{CEO}	-	0.3 0.3	mAdc
Collector Cutoff Current $(V_{CE} = 40 \text{ Vdc}, V_{EB} = 0)$ TIP31G, TIP32G $(V_{CE} = 60 \text{ Vdc}, V_{EB} = 0)$ TIP31AG, TIP32AG $(V_{CE} = 80 \text{ Vdc}, V_{EB} = 0)$ TIP31BG, TIP32BG $(V_{CE} = 100 \text{ Vdc}, V_{EB} = 0)$ TIP31CG, TIP32CG	I _{CES}	- - -	200 200 200 200	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	1.0	mAdc
ON CHARACTERISTICS (Note 2)				
DC Current Gain ($I_C = 1.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}$) ($I_C = 3.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc}$)	h _{FE}	25 10	_ 50	_
Collector–Emitter Saturation Voltage $(I_C = 3.0 \text{ Adc}, I_B = 375 \text{ mAdc})$	V _{CE(sat)}	-	1.2	Vdc
Base–Emitter On Voltage (I _C = 3.0 Adc, V _{CE} = 4.0 Vdc)	V _{BE(on)}	_	1.8	Vdc
DYNAMIC CHARACTERISTICS			•	
Current–Gain – Bandwidth Product (I _C = 500 mAdc, V _{CE} = 10 Vdc, f _{test} = 1.0 MHz)	f _T	3.0	_	MHz
Small–Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	20	_	-

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG, TIP32CG (PNP)

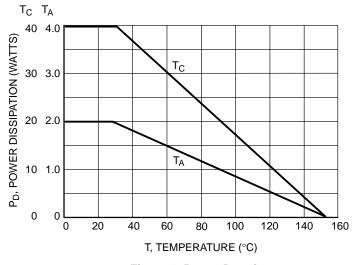
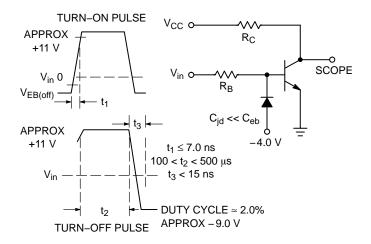
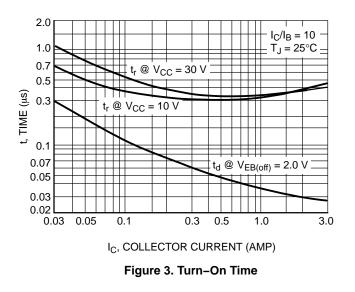


Figure 1. Power Derating



 R_B and R_C VARIED TO OBTAIN DESIRED CURRENT LEVELS.

Figure 2. Switching Time Equivalent Circuit



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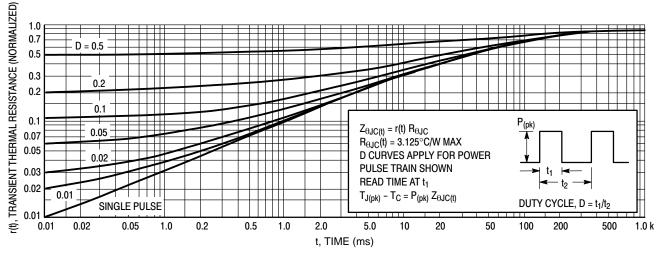


Figure 4. Thermal Response

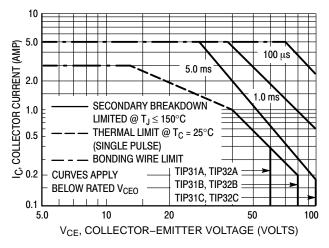
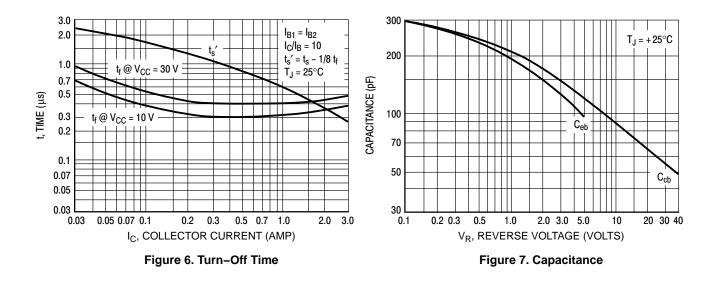
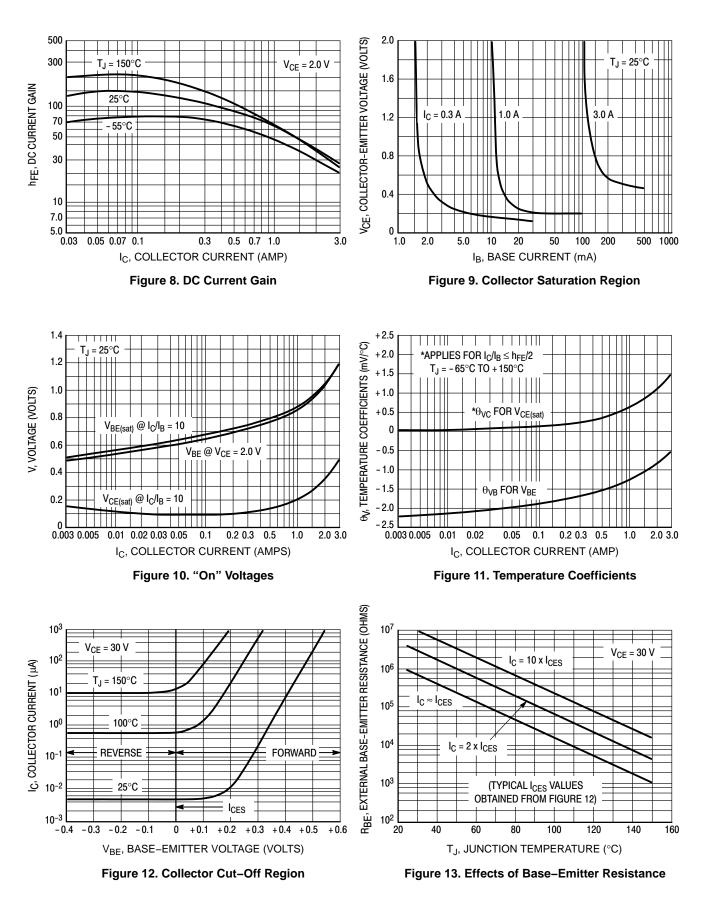


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}$ C; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}$ C. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.





ORDERING INFORMATION

Device	Package	Shipping
TIP31G	TO-220 (Pb-Free)	50 Units / Rail
TIP31AG	TO-220 (Pb-Free)	50 Units / Rail
TIP31BG	TO-220 (Pb-Free)	50 Units / Rail
TIP31CG	TO-220 (Pb-Free)	50 Units / Rail
TIP32G	TO-220 (Pb-Free)	50 Units / Rail
TIP32AG	TO-220 (Pb-Free)	50 Units / Rail
TIP32BG	TO-220 (Pb-Free)	50 Units / Rail
TIP32CG	TO-220 (Pb-Free)	50 Units / Rail

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