

Complementary Power Transistors

DPAK For Surface Mount Applications

MJD31 (NPN), MJD32 (PNP)

Designed for general purpose amplifier and low speed switching applications.

Features

- Lead Formed for Surface Mount Applications in Plastic Sleeves
- Straight Lead Version in Plastic Sleeves ("1" Suffix)
- Lead Formed Version in 16 mm Tape and Reel ("T4" Suffix)
- Electrically Similar to Popular TIP31 and TIP32 Series
- Epoxy Meets UL 94, V-0 @ 0.125 in
- NJV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Collector–Emitter Voltage MJD31, MJD32 MJD31C, MJD32C	V _{CEO}	40 100	Vdc
Collector-Base Voltage MJD31, MJD32 MJD31C, MJD32C	V _{CB}	40 100	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current - Continuous	Ic	3.0	Adc
Collector Current – Peak	I _{CM}	5.0	Adc
Base Current	I _B	1.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	15 0.12	W W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.56 0.012	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C
ESD - Human Body Model	НВМ	3B	V
ESD - Machine Model	MM	МЗ	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

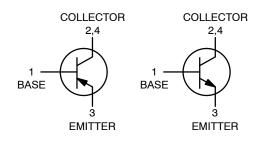
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	8.3	°C/W
Thermal Resistance, Junction-to-Ambient*	$R_{\theta JA}$	80	°C/W
Lead Temperature for Soldering Purposes	TL	260	°C

^{*}These ratings are applicable when surface mounted on the minimum pad sizes recommended.

SILICON POWER TRANSISTORS 3 AMPERES 40 AND 100 VOLTS 15 WATTS

COMPLEMENTARY





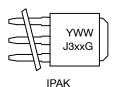


DPAK CASE 369C STYLE 1

IPAK CASE 369D STYLE 1

MARKING DIAGRAMS





A = Site Code
Y = Year
WW = Work Week
xx = 1, 1C, 2, or 2C
G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Sy	mbol	Min	Max	Unit
OFF CHARACTERISTICS	•				
Collector-Emitter Sustaining Voltage (Note 1) (I _C = 30 mAdc, I _B = 0) MJD31, MJD32 MJD31C, MJD32C	V _{CE}	EO(sus)	40 100	- -	Vdc
Collector Cutoff Current $(V_{CE} = 40 \text{ Vdc}, I_B = 0)$ MJD31, MJD32 $(V_{CE} = 60 \text{ Vdc}, I_B = 0)$	Ic	CEO	-	50 50	μAdc
MJD31C, MJD32C Collector Cutoff Current (V _{CE} = Rated V _{CEO} , V _{EB} = 0)	IC	CES	-	20	μAdc
Emitter Cutoff Current (V _{BE} = 5 Vdc, I _C = 0)	Ig	≣BO	-	1	mAdc
ON CHARACTERISTICS (Note 1)	•				
DC Current Gain (I _C = 1 Adc, V _{CE} = 4 Vdc) (I _C = 3 Adc, V _{CE} = 4 Vdc)	ŀ) _{EE}	25 10	- 50	
Collector–Emitter Saturation Voltage (I _C = 3 Adc, I _B = 375 mAdc)	V _C	E(sat)	-	1.2	Vdc
Base-Emitter On Voltage (I _C = 3 Adc, V _{CE} = 4 Vdc)	V _E	BE(on)	-	1.8	Vdc
DYNAMIC CHARACTERISTICS					
Current Gain – Bandwidth Product (Note 2) (I _C = 500 mAdc, V _{CE} = 10 Vdc, f _{test} = 1 MHz)		f _T	3	-	MHz
Small-Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1 kHz)		h _{fe}	20	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2\%$.

2. $f_T = |h_{fe}| \bullet f_{test}$.

TYPICAL CHARACTERISTICS

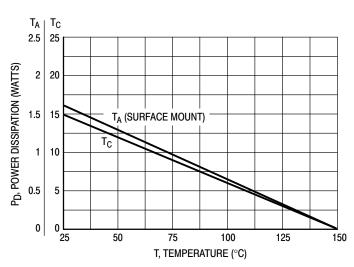
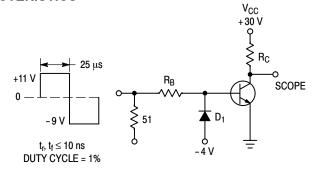


Figure 1. Power Derating



 R_B and R_C Varied to obtain desired current levels D_1 Must be fast recovery type, e.g.: $1 N5825 \ \text{USED ABOVE} \ I_B \approx 100 \ \text{mA} \\ \text{MSD6}100 \ \text{USED BELOW} \ I_B \approx 100 \ \text{mA} \\ \text{REVERSE ALL POLARITIES FOR PNP.}$

Figure 2. Switching Time Test Circuit

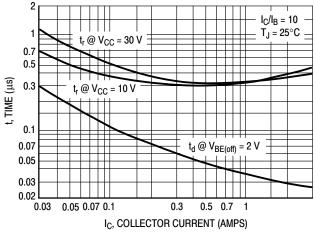


Figure 3. Turn-On Time

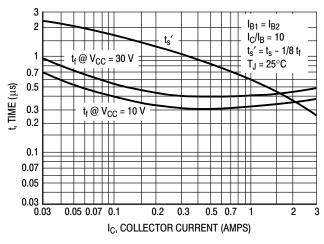


Figure 4. Turn-Off Time

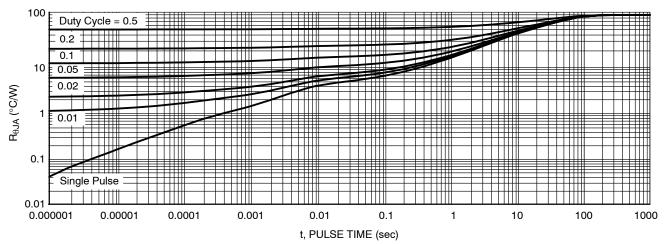
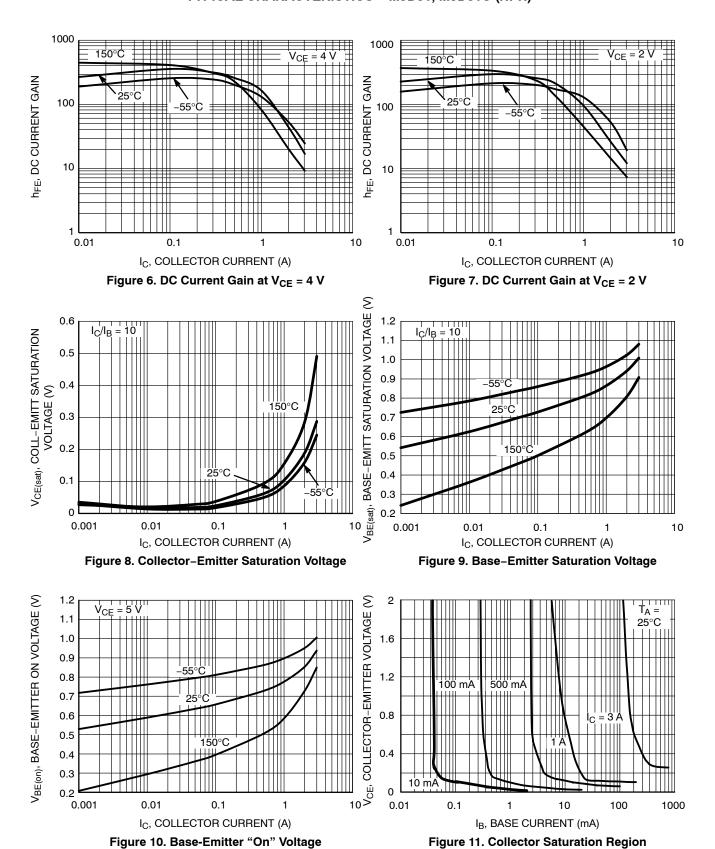


Figure 5. Thermal Response

TYPICAL CHARACTERISTICS - MJD31, MJD31C (NPN)



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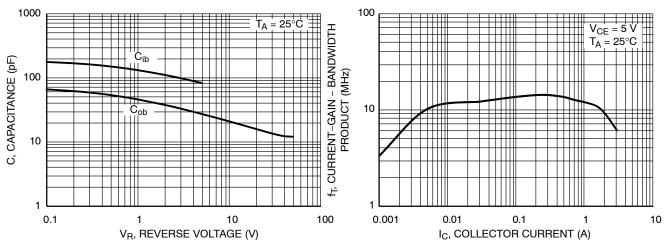


Figure 12. Capacitance

Figure 13. Current-Gain-Bandwidth Product

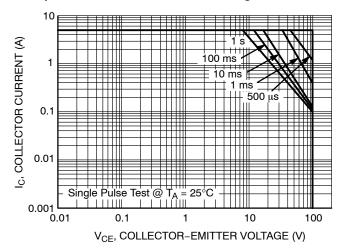
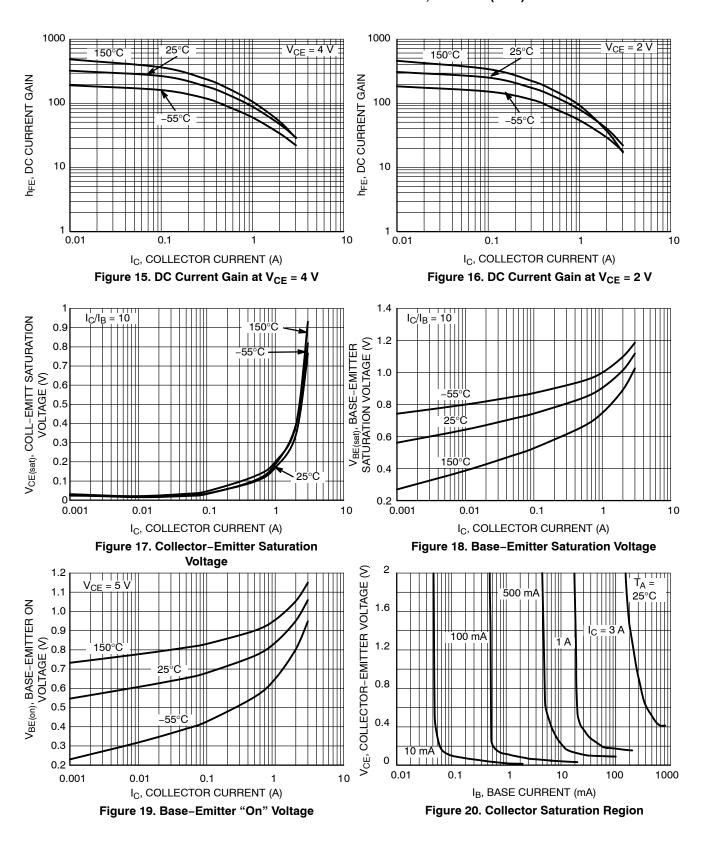


Figure 14. Safe Operating Area

TYPICAL CHARACTERISTICS - MJD32, MJD32C (PNP)



TYPICAL CHARACTERISTICS

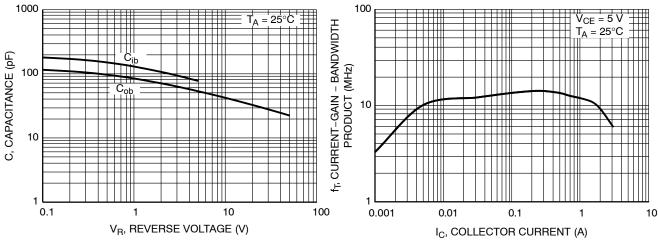


Figure 21. Capacitance

Figure 22. Current-Gain-Bandwidth Product

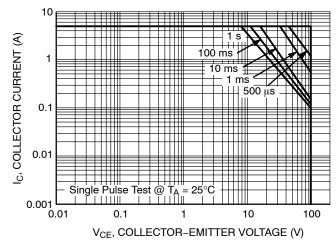


Figure 23. Safe Operating Area

ORDERING INFORMATION

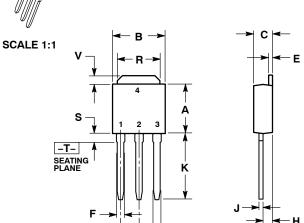
Device	Package Type	Package	Shipping [†]
MJD31CG	DPAK (Pb-Free)	369C	75 Units / Rail
NJVMJD31CG*	DPAK (Pb-Free)	369C	75 Units / Rail
MJD31C1G	IPAK (Pb-Free)	369D	75 Units / Rail
MJD31CRLG	DPAK (Pb-Free)	369C	1,800 / Tape & Reel
NJVMJD31CRLG*	DPAK (Pb-Free)	369C	1,800 / Tape & Reel
MJD31CT4G	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
NJVMJD31CT4G*	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
MJD31T4G	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
NJVMJD31T4G*	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
MJD32CG	DPAK (Pb-Free)	369C	75 Units / Rail
NJVMJD32CG*	DPAK (Pb-Free)	369C	75 Units / Rail
MJD32CRLG	DPAK (Pb-Free)	369C	1,800 / Tape & Reel
MJD32CT4G	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
NJVMJD32CT4G*	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
MJD32RLG	DPAK (Pb-Free)	369C	1,800 / Tape & Reel
MJD32T4G	DPAK (Pb-Free)	369C	2,500 / Tape & Reel
NJVMJD32T4G*	DPAK (Pb-Free)	369C	2,500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NJV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.



DATE 15 DEC 2010



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⊕ 0.13 (0.005) M T

STYLE 3: PIN 1. ANODE

2. CATHODE

4. CATHODE

3 ANODE

STYLE 7: PIN 1. GATE 2. COLLECTOR

3. EMITTER

COLLECTOR

G ←

STYLE 1: PIN 1. BASE

3

STYLE 5: PIN 1. GATE

2. ANODE 3. CATHODE

ANODE

2. COLLECTOR

EMITTER

COLLECTOR

STYLE 2:

PIN 1. GATE

3

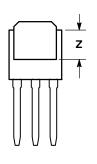
STYLE 6: PIN 1. MT1 2. MT2 3. GATE

2. DRAIN

4. DRAIN

MT2

SOURCE



NOTES:

- DIMENSIONING AND TOLERANCING PER
- ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.35
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.090	BSC	2.29	BSC
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.180	0.215	4.45	5.45
S	0.025	0.040	0.63	1.01
٧	0.035	0.050	0.89	1.27
Z	0.155		3.93	

MARKING DIAGRAMS

STYLE 4: PIN 1. CATHODE

ANODE
 GATE

4. ANODE

Discrete YWW XXXXXXXX



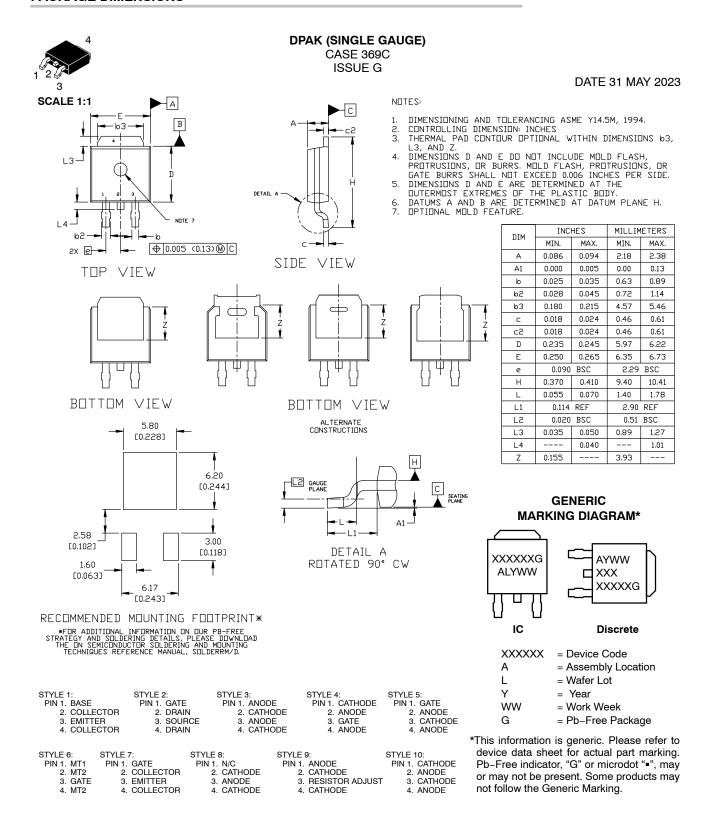
xxxxxxxxx = Device Code Α = Assembly Location IL = Wafer Lot

Υ = Year WW = Work Week

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DESCRIPTION:	IPAK (DPAK INSERTION M	IOUNT)	PAGE 1 OF 1

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