# **Complementary Silicon Power Transistors**

These series of plastic, silicon NPN and PNP power transistors can be used as general purpose power amplification and switching such as output or driver stages in applications such as switching regulators, converters and power amplifiers.

#### **Features**

- Low Collector-Emitter Saturation Voltage
- Fast Switching Speeds
- Complementary Pairs Simplifies Designs
- These Devices are Pb-Free and are RoHS Compliant\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage D44H8, D45H8 D44H11, D45H11	V <sub>CEO</sub>	60 80	Vdc
Emitter Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current – Continuous	I <sub>C</sub>	10	Adc
Collector Current – Peak (Note 1)	I <sub>CM</sub>	20	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>A</sub> = 25°C	P <sub>D</sub>	70 2.0	W
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Pulse Width  $\leq$  6.0 ms, Duty Cycle  $\leq$  50%.

#### THERMAL CHARACTERISTICS

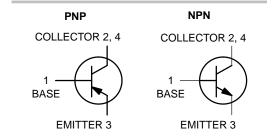
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.8	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	TL	275	°C

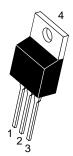


#### ON Semiconductor®

www.onsemi.com

## 10 AMP COMPLEMENTARY SILICON POWER TRANSISTORS 60, 80 VOLTS





## DIAGRAM

**MARKING** 



D4xHyy = Device Code

x = 4 or 5

yy = 8 or 11

A = Assembly Location

TO-220

**CASE 221A** 

STYLE 1

Y = Year WW = Work Week G = Pb-Free Package

#### ORDERING INFORMATION

Device	Package	Shipping
D44H8G	TO-220 (Pb-Free)	50 Units/Rail
D44H11G	TO-220 (Pb-Free)	50 Units/Rail
D45H8G	TO-220 (Pb-Free)	50 Units/Rail
D45H11G	TO-220 (Pb-Free)	50 Units/Rail

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		<del>'</del>		<del>!</del>	<del>!</del>	
Collector–Emitter Sustaining Voltage (I <sub>C</sub> = 30 mAdc, I <sub>B</sub> = 0 Adc)	D44H8, D45H8 D44H11, D45H11	V <sub>CEO(sus)</sub>	60 80	- -	- -	Vdc
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CEO</sub> , V <sub>BE</sub> = 0)		I <sub>CES</sub>	_	-	10	μА
Emitter Cutoff Current (V <sub>EB</sub> = 5.0 Vdc)		I <sub>EBO</sub>	_	-	10	μΑ
ON CHARACTERISTICS						
DC Current Gain $ (V_{CE} = 1.0 \text{ Vdc}, I_{C} = 2.0 \text{ Adc}) $ $ (V_{CE} = 1.0 \text{ Vdc}, I_{C} = 4.0 \text{ Adc}) $		h <sub>FE</sub>	60 40	- -	- -	-
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 8.0 Adc, I <sub>B</sub> = 0.4 Adc)		V <sub>CE(sat)</sub>	_	-	1.0	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 8.0 Adc, I <sub>B</sub> = 0.8 Adc)		V <sub>BE(sat)</sub>	-	-	1.5	Vdc
DYNAMIC CHARACTERISTICS						
Collector Capacitance (V <sub>CB</sub> = 10 Vdc, f <sub>test</sub> = 1.0 MHz)	D44H Series D45H Series	C <sub>cb</sub>	- -	90 160	_ _	pF
Gain Bandwidth Product ( $I_C = 0.5 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f = 20 \text{ MHz}$ )	D44H Series D45H Series	f <sub>T</sub>	- -	50 40	_ _	MHz
SWITCHING TIMES						
Delay and Rise Times (I <sub>C</sub> = 5.0 Adc, I <sub>B1</sub> = 0.5 Adc)	D44H Series D45H Series	t <sub>d</sub> + t <sub>r</sub>	- -	300 135	_ _	ns
Storage Time ( $I_C = 5.0 \text{ Adc}$ , $I_{B1} = I_{B2} = 0.5 \text{ Adc}$ )	D44H Series D45H Series	t <sub>s</sub>	- -	500 500	_ _	ns
Fall Time $(I_C = 5.0 \text{ Adc}, I_{B1} = 102 = 0.5 \text{ Adc})$	D44H Series D45H Series	t <sub>f</sub>	- -	140 100	_ _	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

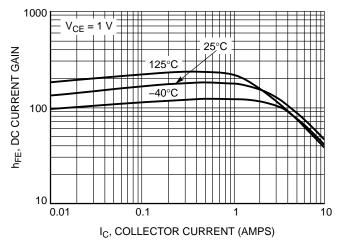


Figure 1. D44H11 DC Current Gain

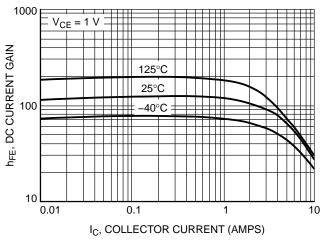


Figure 2. D45H11 DC Current Gain

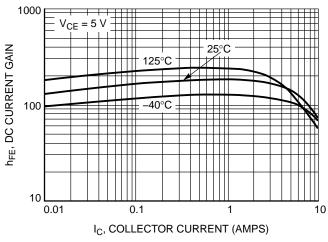


Figure 3. D44H11 DC Current Gain

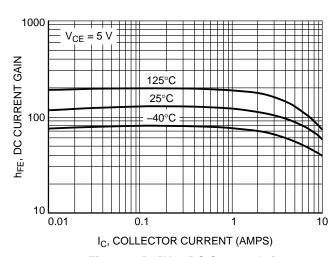


Figure 4. D45H11 DC Current Gain

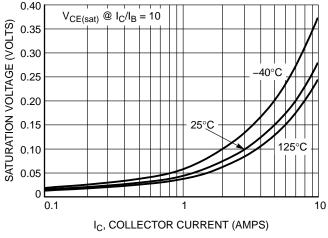


Figure 5. D44H11 ON-Voltage

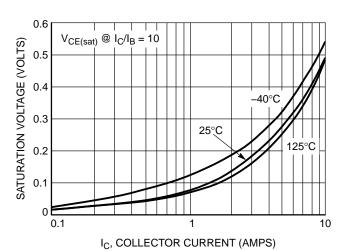


Figure 6. D45H11 ON-Voltage

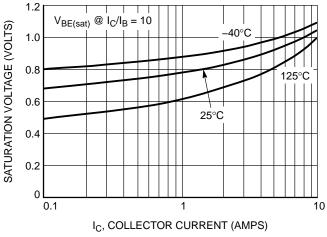


Figure 7. D44H11 ON-Voltage

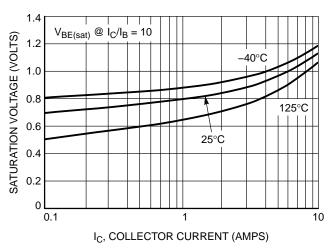


Figure 8. D45H11 ON-Voltage

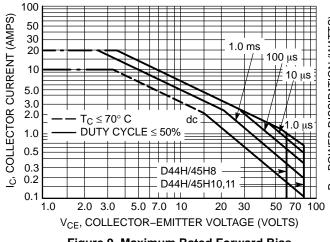


Figure 9. Maximum Rated Forward Bias Safe Operating Area

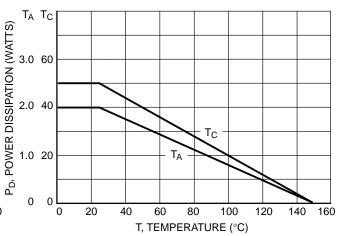


Figure 10. Power Derating

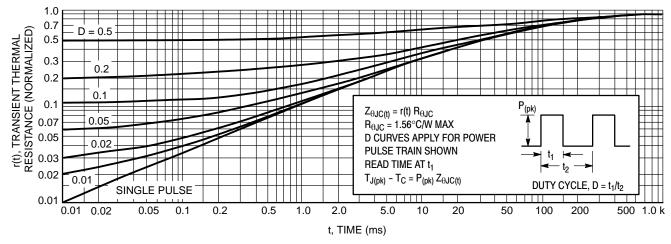
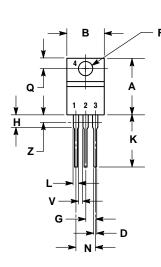
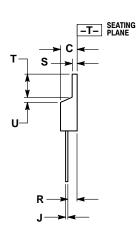


Figure 11. Thermal Response

#### PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH** 





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.415	9.66	10.53	
С	0.160	0.190	4.07	4.83	
D	0.025	0.038	0.64	0.96	
F	0.142	0.161	3.61	4.09	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.161	2.80	4.10	
J	0.014	0.024	0.36	0.61	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

STYLE 1:

BASE PIN 1.

- COLLECTOR
- **EMITTER** 3
- COLLECTOR

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