

TinyLogic ULP-A 2-Input AND Gate

NC7SV08

The NC7SV08 is a single 2-Input AND Gate in tiny footprint packages. The device is designed to operate for $V_{CC} = 0.9 \text{ V}$ to 3.6 V.

Features

- Designed for 0.9 V to 3.6 V V_{CC} Operation
- 1.6 ns t_{PD} at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I_{OFF} Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.3 V
- Available in SC−88A and MicroPak[™] Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

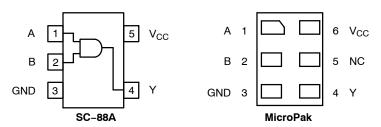


Figure 1. Pinout Diagrams (Top Views)



Figure 2. Logic Symbol

MARKING DIAGRAMS



SIP6 1.45X1.0 MicroPak CASE 127EB





UDFN6 MicroPak2 CASE 517DP



CC = Specific Device Code

KK = 2-Digit Lot Run Traceability Code

XY = 2-Digit Date Code Z = Assembly Plant Code



SC-88A CASE 419AC



XXX = Specific Devic Code

M = Date Code

= Pb-Free Package

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 6 of this data sheet.

PIN ASSIGNMENT

Pin	SC-88A	MicroPak
1	А	А
2	В	В
3	GND	GND
4	Υ	Υ
5	V _{CC}	N.C.
6	-	V _{CC}

N.C. = No Connect

FUNCTION TABLE

Inp	Output Y = AB	
Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

MAXIMUM RATINGS

Symbol	Characteristics		Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to +4.3	V
V _{IN}	DC Input Voltage		-0.5 to +4.3	٧
V _{OUT}		Mode (High or Low State) Tri-State Mode (Note 1) -Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +4.3 -0.5 to +4.3	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-50	mA
I _{OK}	DC Output Diode Current	V _{OUT} < GND	-50	mA
lout	DC Output Source/Sink Current		±50	mA
I _{CC} or I _{GND}	DC Supply Current per Supply Pin or Ground Pin		±50	mA
T _{STG}	Storage Temperature Range		-65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 Seconds		260	°C
TJ	Junction Temperature Under Bias		+150	°C
θЈА	Thermal Resistance (Note 2)	SC-88A MicroPak	377 154	°C/W
P _D	Power Dissipation in Still Air	SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	-
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V _{ESD}	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	4000 2000	V
I _{Latchup}	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality

- should not be assumed, damage may occur and reliability may be affected.

 1. Applicable to devices with outputs that may be tri-stated.

 2. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.

 3. HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
- 4. Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter			Max	Unit
V _{CC}	Positive DC Supply Voltage		0.9	3.6	V
V _{IN}	DC Input Voltage		0	3.6	V
V _{OUT}	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	0 0 0	V _{CC} 3.6 3.6	
T _A	Operating Temperature Range		-40	+85	°C
t _r , t _f	Input Transition Rise and Fall Time	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

				T	T _A = 25°C		$T_{A} = -40^{\circ}$	C to +85°C						
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit					
V _{IH}	High-Level Input		0.9	-	0.5	-	-	-	V					
	Voltage		1.1 to 1.3	0.65 x V _{CC}	-	-	0.65 x V _{CC}	-						
			1.4 to 1.6	0.65 x V _{CC}	-	-	0.65 x V _{CC}	-						
			1.65 to 1.95	0.65 x V _{CC}	-	-	0.65 x V _{CC}	-						
			2.3 to <2.7	1.6	-	-	1.6	-						
			2.7 to 3.6	2.0	-	-	2.0	-						
V _{IL}	Low-Level Input		0.9	-	0.5	-	-	-	V					
	Voltage		1.1 to 1.3	_	-	0.35 x V _{CC}	-	0.35 x V _{CC}						
			1.4 to 1.6	-	-	0.35 x V _{CC}	-	0.35 x V _{CC}						
			1.65 to 1.95	_	-	0.35 x V _{CC}	-	0.35 x V _{CC}						
			2.3 to <2.7	-	-	0.7	-	0.7						
			2.7 to 3.6	-	-	0.8	-	0.8						
V _{OH}	High-Level Output	$V_{IN} = V_{IH}$ or V_{IL}							V					
	Voltage	I _{OH} = -100 μA	0.9	-	V _{CC} – 0.1	_	-	-						
				1.1 to 1.3	V _{CC} – 0.1	_	-	V _{CC} – 0.1	-					
			1.4 to 1.6	V _{CC} – 0.1	_	-	V _{CC} - 0.1	-						
								1.65 to 1.95	V _{CC} – 0.2	-	-	V _{CC} - 0.2	-	
			2.3 to <2.7	V _{CC} – 0.2	_	-	V _{CC} - 0.2	-						
			2.7 to 3.6	V _{CC} - 0.2	-	-	V _{CC} - 0.2	-						
		$I_{OH} = -2 \text{ mA}$	1.1 o 1.3	0.75 x V _{CC}	-	-	0.75 x V _{CC}	-						
			$I_{OH} = -4 \text{ mA}$	1.4 to 1.6	$0.75 \times V_{CC}$	-	-	0.75 x V _{CC}	-	ı				
		$I_{OH} = -6 \text{ mA}$	1.65 to 1.95	1.25	-	-	1.25	-						
			2.3 to <2.7	2.0	-	-	2.0	-						
		I _{OH} = -12 mA 2.3 to <2.7 2.7 to 3.6	1.8	-	-	1.8	-							
			2.7 to 3.6	2.2	-	-	2.2	-						
		I _{OH} = −18 mA	2.3 to <2.7	1.7	-	-	1.7	-						
			2.7 to 3.6	2.4	-	-	2.4	-						
		$I_{OH} = -24 \text{ mA}$	2.7 to 3.6	2.2	_	-	2.2	_						

DC ELECTRICAL CHARACTERISTICS (continued)

					T _A = 25°	С	$T_A = -40^{\circ}$	C to +85°C	
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
V _{OL}	Low-Level	$V_{IN} = V_{IH}$ or V_{IL}							V
	Output Voltage	I _{OL} = 100 μA	0.9	-	0.1	-	-	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	-	0.1	-	0.1	
			1.65 to 1.95	-	-	0.2	-	0.2	
			2.3 to < 2.7	-	-	0.2	-	0.2	
			2.7 to 3.6	-	-	0.2	-	0.2	
		I _{OL} = 2 mA	1.1 o 1.3	-	-	0.25 x V _{CC}	1	0.25 x V _{CC}	
		I _{OL} = 4 mA	1.4 to 1.6	-	_	0.25 x V _{CC}	-	0.25 x V _{CC}	
		I _{OL} = 6 mA	1.65 to 1.95	-	-	0.3	1	0.3	
		I _{OL} = 12 mA	2.3 to <2.7	-	_	0.4	-	0.4	
			2.7 to 3.6	-	-	0.4	-	0.4	
		I _{OL} = 18 mA	2.3 to <2.7	-	-	0.6	1	0.6	
			2.7 to 3.6	-	_	0.4	-	0.4	
		I _{OL} = 24 mA	2.7 to 3.6	-	-	0.55	1	0.55	
I _{IN}	Input Leakage Current	V _{IN} = 0 V to 3.6 V	0.9 to 3.6	-	-	±0.1	-	±0.5	μΑ
I _{OFF}	Power Off Leakage Current	V _{IN} = 0 V to 3.6 V or V _{OUT} = 0 V to 3.6 V	0	_	-	0.5	ı	0.5	μΑ
Icc	Quiescent Supply Current	V _{IN} = V _{CC} or GND	0.9 to 3.6	_	-	0.9	_	0.9	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

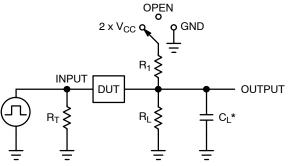
AC ELECTRICAL CHARACTERISTICS

				T _A = 25°C		T _A = -40°C to +85°C			
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
t _{PLH} ,	Propagation Delay,	$R_L = 1 \text{ M}\Omega$, $C_L = 15 \text{ pF}$	0.9	_	15.9	_	-	-	ns
t _{PHL}	(A or B) to Y (Figures 3 and 4)	$R_L = 2 \text{ k}\Omega$, $C_L = 15 \text{ pF}$	1.1 to 1.3	_	6.8	11.6	-	14.6	
	,		1.4 to 1.6	_	3.6	6.0	-	7.2	
		$R_L = 500 \ \Omega, \ C_L = 30 \ pF$	1.65 to 1.95	-	2.6	4.5	-	5.3	
			2.3 to 2.7	-	1.9	2.6	-	3.7	
			2.7 to 3.6	-	1.6	2.3	-	3.0	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typical (T _A = 25°C)	Unit
C _{IN}	Input Capacitance	V _{CC} = 0 V	2.0	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	$f = 10 \text{ MHz}, V_{CC} = 0.9 \text{ to } 3.6 \text{ V}, V_{IN} = 0 \text{ V or } V_{CC}$	8.0	pF

^{5.} C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no–load dynamic power consumption: P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.



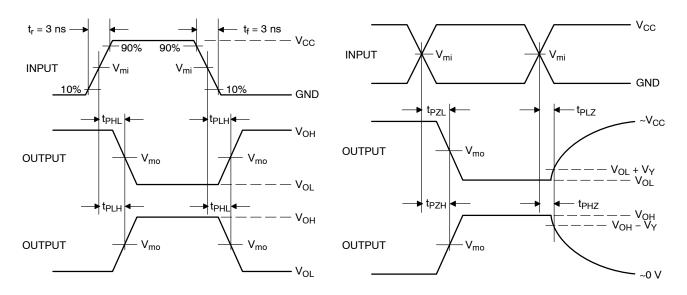
Test	Switch Position
t _{PLH} / t _{PHL}	Open
t _{PLZ} / t _{PZL}	2 x V _{CC}
t _{PHZ} / t _{PZH}	GND

 C_L includes probe and jig capacitance

 R_{T} is Z_{OUT} of pulse generator (typically 50 $\Omega)$

f = 1 MHz

Figure 3. Test Circuit



V _{CC} , V	V _{mi} , V	V_{mo}, V	V _Y , V
0.9	V _{CC} /2	V _{CC} / 2	0.1
1.1 to 1.3	V _{CC} /2	V _{CC} / 2	0.1
1.4 to 1.6	V _{CC} / 2	V _{CC} / 2	0.1
1.65 to 1.95	V _{CC} / 2	V _{CC} /2	0.15
2.3 to 2.7	V _{CC} / 2	V _{CC} /2	0.15
3.0 to 3.6	1.5	1.5	0.3

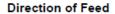
Figure 4. Switching Waveforms

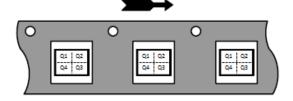
ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping [†]
NC7SV08P5X	SC-88A	V08	Q4	3000 / Tape & Reel
NC7SV08P5X-L22735	SC-88A	V08	Q4	3000 / Tape & Reel
NC7SV08L6X	MicroPak	G3	Q4	5000 / Tape & Reel
NC7SV08L6X-L22780	MicroPak	G3	Q4	5000 / Tape & Reel
NC7SV08FHX	MicroPak2	G3	Q4	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

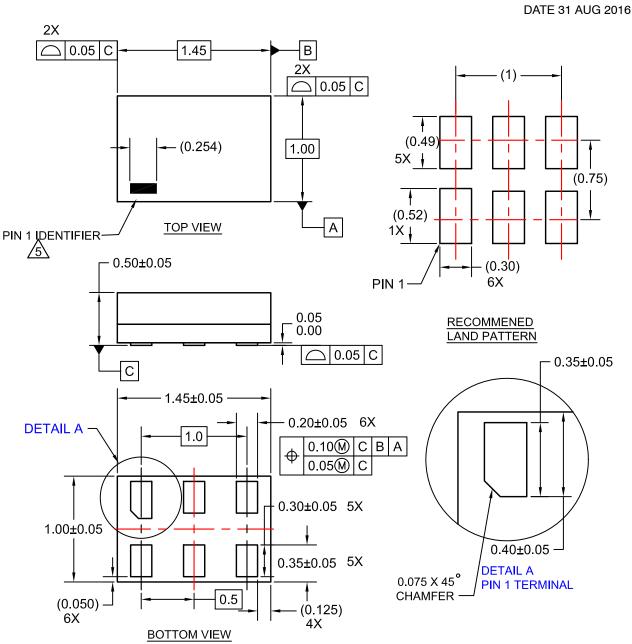
Pin 1 Orientation in Tape and Reel





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NOTES:

- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
- 4.PIN ONE IDENTIFIER IS 2X LENGTH OF ANY
 - OTHER LINE IN THE MARK CODE LAYOUT.

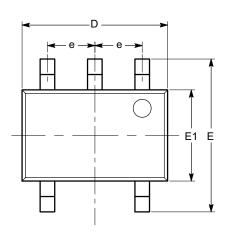
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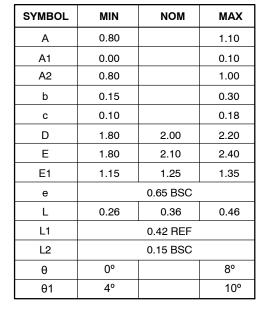


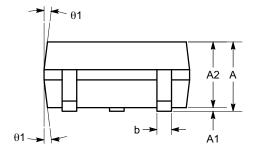
SC-88A (SC-70 5 Lead), 1.25x2 CASE 419AC-01 ISSUE A

DATE 29 JUN 2010

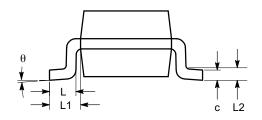


TOP VIEW





SIDE VIEW



END VIEW

Notes

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-203.

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UDFN6 1.0X1.0, 0.35P CASE 517DP ISSUE O **DATE 31 AUG 2016** 0.89 0.05 C В 1.00±0.050 Α 2X 5X 0.40 PIN 1 MIN 250uM 0.66 1 00±0 050 1X 0.45 0.05 C **TOP VIEW** 6X 0.19 2X RECOMMENDED LAND PATTERN FOR SPACE CONSTRAINED PCB 0.05 C - 0.90 -0.50±0.05 5X 0.52 SIDE VIEW 6X 0.14±0.05 (0.08)4X -0.73 **DETAIL A** 3 1X 0.57 - 0.20 6X ALTERNATIVE LAND PATTERN FOR UNIVERSAL APPLICATION (0.05)6X5X 0.30±0.05 0.60 0.10M|C|B|A(0.08).05 C 4X 0.35±0.050 **BOTTOM VIEW** NOTES: A. COMPLIES TO JEDEC MO-252 STANDARD B. DIMENSIONS ARE IN MILLIMETERS. C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009

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0.075X45°

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DETAIL A

PIN 1 LEAD SCALE: 2X

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