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August 2018

ISL9R860P2, ISL9R860S3ST 8 A, 600 V, STEALTH™ Diode

Features

- Stealth Recovery trr = 28 ns (@ IF = 8 A)
- Max Forward Voltage, VF = 2.4 V (@ TC = 25°C)
- 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

Applications

- SMPS FWD
- · Hard Switched PFC Boost Diode
- · UPS Free Wheeling Diode
- Motor Drive FWD
- · Snubber Diode

Description

The ISL9R860P2, ISL9R860S3ST is a STEALTH™ diode optimized for low loss performance in high frequency hard switched applications. The STEALTH™ family exhibits low reverse recovery current (I_{RR}) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{RR} and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH™ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package JEDEC TO-220AC-2L JEDEC TO-263AB(D²-PAK) CATHODE (FLANGE) N/C ANODE N/C ANODE

Device Maximum Ratings T_C= 25°C unless otherwise noted

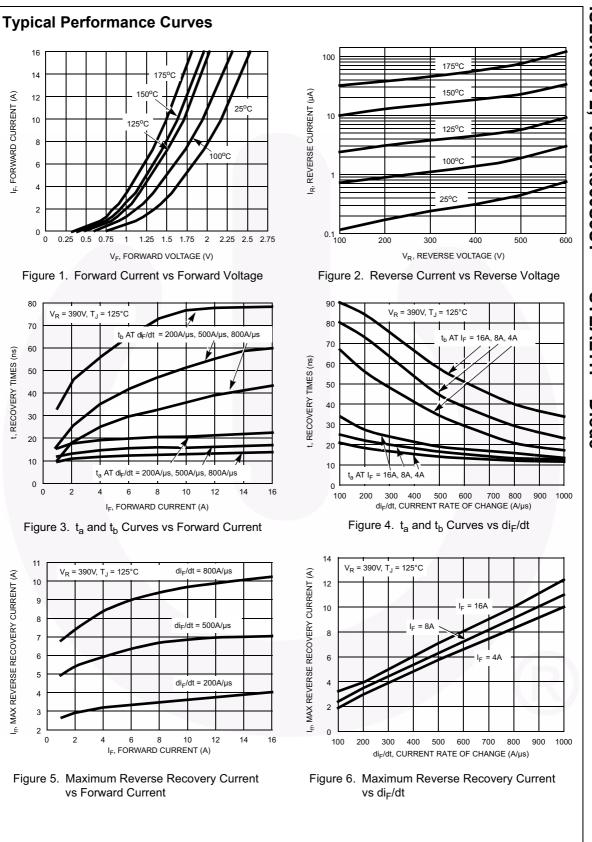
Symbol	Parameter	Ratings	Unit
V _{RRM}	Peak Repetitive Reverse Voltage	600	V
V _{RWM}	Working Peak Reverse Voltage	600	V
V _R	DC Blocking Voltage	600	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 147°C)	8	Α
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	16	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	100	Α
P _D	Power Dissipation	85	W
E _{AVL}	Avalanche Energy (1 A, 40 mH)	20	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C
T _L T _{PKG}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Techbrief TB334	300 260	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

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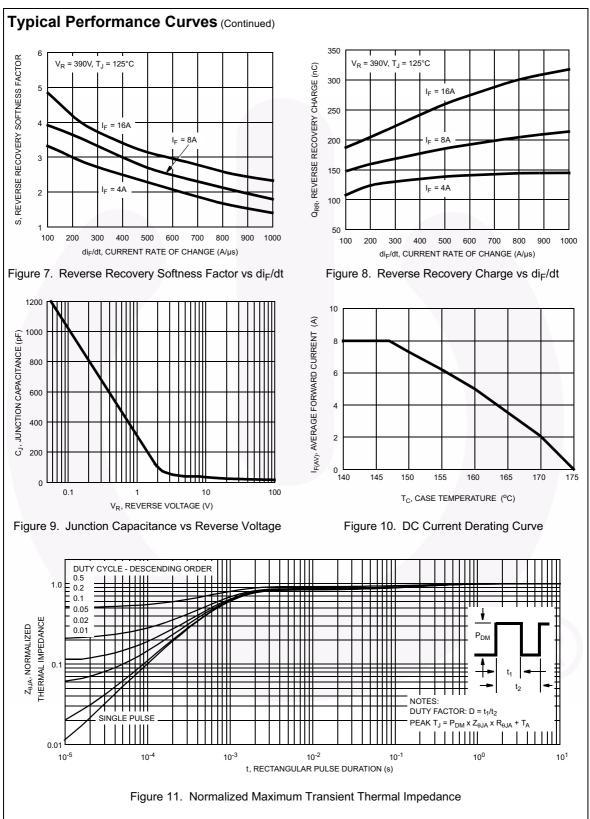
art Num	nber	Top Mark	Package	Packing Method Reel Size		Tap	e Wic	lth G	Quanti	
ISL9R860P2		R860P2	TO-220AC-2L	Tube		N/A		N/A		50
ISL9R860S3ST R860S3S TO-263A		TO-263AB(D ² -PAK) Reel 13" [13" Dia	24mm			800	
Electric	cal (Characteri	stics T _C = 25°C u	nless otherwise	noted					
Symbol		Parar	Test Conditions			Min	Тур	Max	Unit	
Off State	e Cha	racteristics								
I _R		Instantaneous Reverse Current		V _R = 600 V	To =	= 25°C	_	-	100	μА
'K	linote					= 125°C	_	-	1.0	mΑ
	<u> </u>				1 0					1
	Cha	racteristics								
V _F	Insta	intaneous Forwa	ard Voltage	I _F = 8 A	T _C =	= 25°C	-	2.0	2.4	V
					T _C =	= 125°C	-	1.6	2.0	V
Dynamic	Cha	racteristics								
C,I	Junc	tion Capacitanc	e	V _R = 10 V, I _F = 0 A		-	30	-	pF	
0	01									
	-	naracteristic								
t _{rr} Reverse Recov		erse Recovery T	îme	F, a.p,			-	18	25	ns
		Reverse Recovery Time Reverse Recovery Current Reverse Recovery Charge		$I_F = 8 \text{ A}, \text{ di}_F/\text{dt} = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$ $I_F = 8 \text{ A},$ $\text{di}_F/\text{dt} = 200 \text{ A}/\mu\text{s},$ $\text{V}_R = 390 \text{ V}, \text{ T}_C = 25^{\circ}\text{C}$ $I_F = 8 \text{ A},$ $\text{di}_F/\text{dt} = 200 \text{ A}/\mu\text{s},$			-	21	30	ns
t _{rr}							-	28	-	ns
I _{rr}	_						-	3.2	-	A
Q _{rr}							-	50	-	nC
t _{rr}		everse Recovery Time					-	77	-	ns
S		coftness Factor (t _b /t _a) Reverse Recovery Current		$V_{R} = 390 \text{ V},$			3.7	-		
I _{rr}				T _C = 125°C		-		3.4	-	A
Q _{rr}	_	erse Recovery C				-	150	-	nC	
t _{rr}	-	erse Recovery T		$I_F = 8 \text{ A},$ $di_F/dt = 600 \text{ A/}\mu\text{s},$ $V_R = 390 \text{ V},$ $T_C = 125^{\circ}\text{C}$		-	53	-	ns	
S		ness Factor (t _b /t _a	и,			-	2.5	-		
Irr	_	erse Recovery C				-	6.5	-	A	
Q _{rr}		erse Recovery C		ļ ~				195	-	nC
dI _M /dt	Max	mum di/dt durin	g t _b			A		500	-	A/µs
Γhermal	Cha	racteristics								
$R_{\theta JC}$	Ther	mal Resistance				-	-	1.75	°C/W	
	_		ance Junction to Ambient TO-220			-	-	62	°C/W	
RAIA										
$R_{\theta JA}$ $R_{\theta JA}$	Ther	mal Resistance	Junction to Ambient	TO-263					62	°C/W



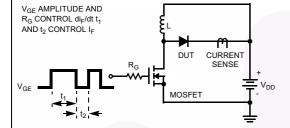
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Test Circuits and Waveforms



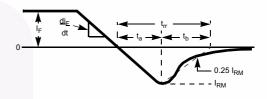
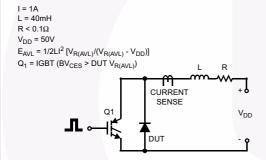


Figure 12. t_{rr} Test Circuit

Figure 13. t_{rr} Waveforms and Definitions



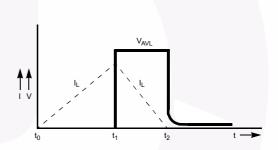


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

Mechanical Dimensions

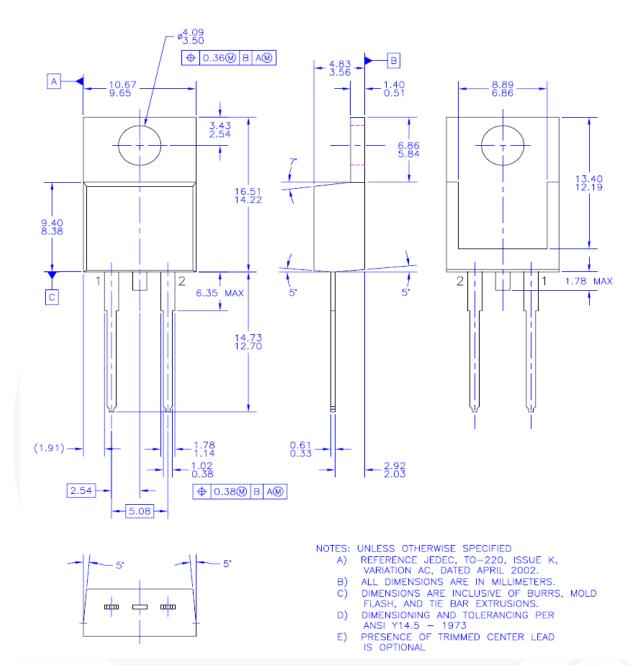


Figure 16. TO-220 2L - 2LD,TO220,JEDEC TO-220 VARIATION AC

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9.45 10.00 (6.40)1.78 MAX 3.80 (2.12)5.08 LAND PATTERN RECOMMENDATION UNLESS NOTED, ALL DIMS TYPICAL 5.08 → 0.25 M B AM 6.22 MIN 6.86 MIN 15.88 14.61 SEE DETA|L A NOTES: UNLESS OTHERWISE SPECIFIED A) ALL DIMENSIONS ARE IN MILLIMETERS. B) REFERENCE JEDEC, TO-263, VARIATION AB. C) DIMENSIONING AND TOLERANCING PER ANSI Y14,5M - 1994. D) LOCATION OF THE PIN HOLE MAY VARY GAGE PLANE (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE). E) LANDPATTERN RECOMMENDATION PER IPC 0.25 TO254P1524X482-3N F) FILENAME: TO263A02REV6 O.10 B 0.25 MAX (5.38) DETAIL A, ROTATED 90°

Figure 17. TO-263 2L (D2PAK) - 2LD, TO263, SURFACE MOUNT

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