

ORIENT

Photocoupler

Product Data Sheet

Name:	ORPC-814
Customer:_	
Date:	

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1. Features

- (1) AC input response.
- (2) Current transfer ratio (CTR: MIN. 20% at IF = ±1mA, VCE = 5V)
- (3) Wide Operating temperature range -55~110°C
- (4) High input-output isolation voltage (Viso = 5,000Vrms)
- (5) Response time (tr : TYP. 4us at $V_{CE} = 2V$, $I_C = 2mA$, $R_L = 100$)
- (6) High collector-emitter voltage (V_{CE} ≥ 80V)
- (7) Safety approval

UL approved (No.E323844) VDE approved (No.40029733)

CQC approved (No.CQC09001029446 CQC13001086898)

CE approved (No.AC/0431008)

State Grid approved (No.SGCM013420170152)

2. Description

- (1) The ORPC-814 series of devices each consist of two infrared emitting diodes, connected in inverse parallel, optically coupled to a phototransistor detector.
- (2) They are packaged in a 4-pin DIP package and available in side-lead spacing and SMD option.



- (1) AC line monitor
- (3)Telephone line interface

(2)Programmable controllers (4)Unknown polarity DC sensor

4. Absolute Maximum Ratings at Ta=25℃

	Parameter	Symbol	Rated Value	Unit
	Forward Current	I _F	±50	mA
Input	Peak forward current (100µs pulse, 100Hz frequency)	I _{FP}	1	Α
1	Reverse Voltage	V_R	6	V
	Consume Power	Р	70	mW
	Collector and emitter Voltage	V _{CEO}	80	V
Output	Emitter and collector Voltage	V _{ECO}	6] V
Catput	Collector Current	Ic	50	mA
	Consume Power	Pc	150	mW
	Total Power Dissipation	Ptot	200	mW
	*1 Isolation Voltage	Viso	5,000	Vrms
	Operating Temperature	Topr	-50 to + 110	
Storage Temperature		Tstg	-55 to + 125	\mathbb{C}
,	² Soldering Temperature	Tsol	260	

1. AC For 1 Minute, R.H. = $40 \sim 60\%$

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector and emitter on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
- 2. For 10 Seconds











5. Electro-Optical Characteristics (Ta=25℃ unless specified otherwise)

Pa	nrameter	Symbol	Condition	Min	Тур.*	Max	Unit
Innut	Forward Current	V_{F}	$I_F=\pm 20 mA$		1.2	1.4	V
Input	Collector capacitance	C_{t}	V=0, f=1KHz		30	250	pF
	Collector to emitter Current	I_{CEO}	V_{CE} =20V, I_{F} =0mA			100	nA
Output	Collector and Emitter attenuation Voltage	$\mathrm{BV}_{\mathrm{CEO}}$	I _C =0.1mA I _F =0mA	80			V
	Emitter and Collector attenuation Voltage	$\mathrm{BV}_{\mathrm{ECO}}$	I _E =10uA I _F =0mA	6			V
	*1 Current conversion ratio	CTR		20		300	%
	Collector Current	$ m I_{C}$	IF=±1mA VCE=5V	0.2		3	mA
	Collector and Emitter Saturation Voltage	$V_{\text{CE(sat)}}$	$I_F=\pm 20 \text{mA}$ $I_C=1 \text{mA}$		0.1	0.2	V
Transforming Characteristics	Insulation Impedance	R_{iso}	DC500V 40~60%R.H.		1×10 ¹²		Ω
	Floating Capacitance	C_{f}	V=0, f=1MHz		0.6	1.0	pF
	Cut-off Frequency	$ m f_c$	V_{CE} =5V, I_{C} =2mA R_{L} =100 Ω , -3dB		80		kHz
	Rise Time	t _r	$V_{CE}=2V$, $I_{C}=2mA$		4	18	μs
	Descend Time	t_{f}	$R_L=100\Omega$		3	18	μs

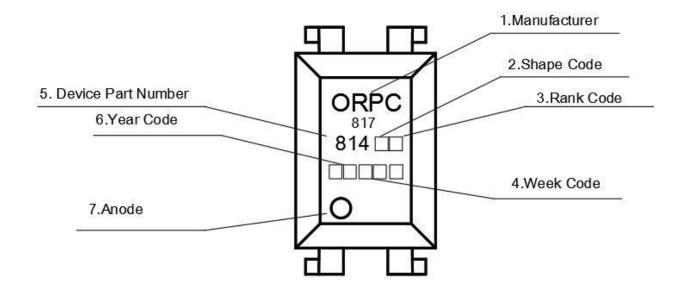
^{*1} Current Conversion Ratio = I_C / I_F × 100%, CTR Tolerance: $\pm 3\%$.



6. Rank Table of Current Transfer Ratio

	CTR Rank	Min	Max	Condition
	A	50	150	I _F =± 1mA
ORPC-814	В	100	300	V _{CE} =5V
	A or B or No mark	20	300	Ta=25 ℃

7. Naming Rule

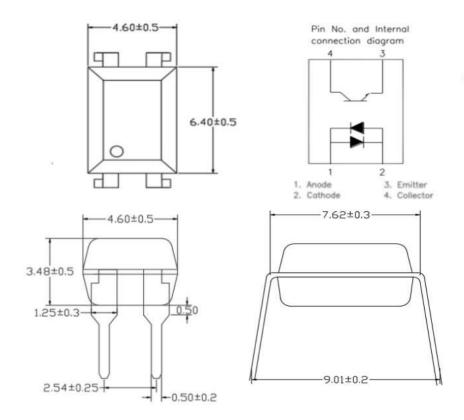


- (1)ORPC denotes Shenzhen Orient Tech Ltd . Co ., Ltd.
- (2) denotes Shape Code.
- (3) denotes Rank code.
- (4) denotes Week code.
- (5) denotes Device Part Number.
- (6) denotes Year Code
- (7) Anode.
- (8) Unit:mm

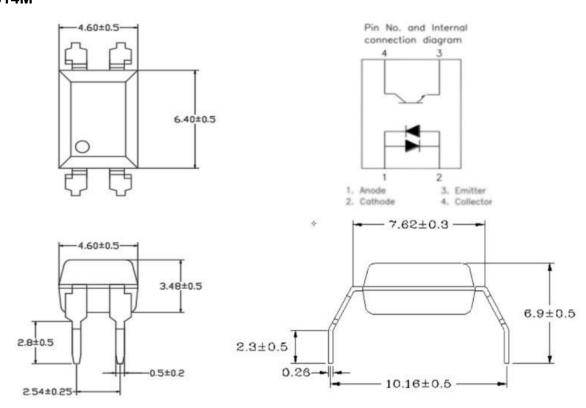


8. Package Dimension (Unit: mm)

1.ORPC-814

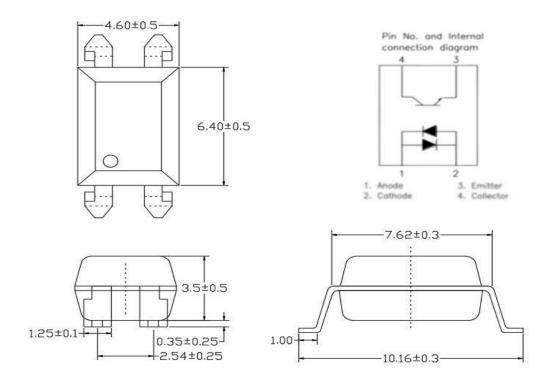


2.ORPC-814M



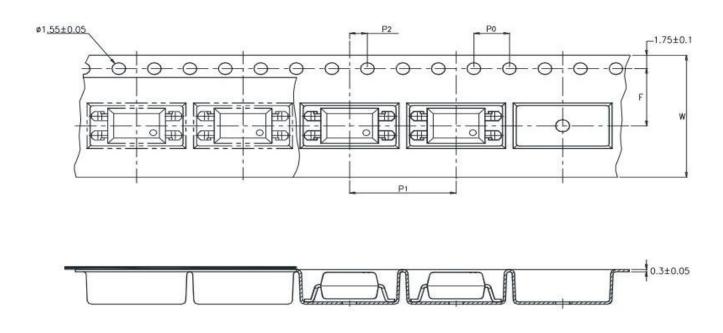


3.ORPC-814S



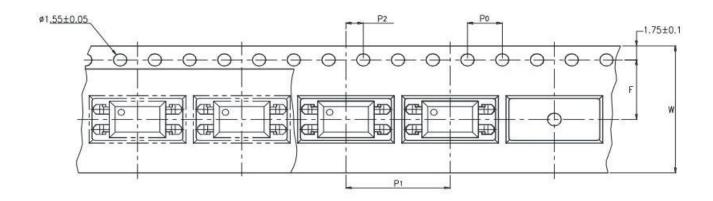
9. Taping Dimensions

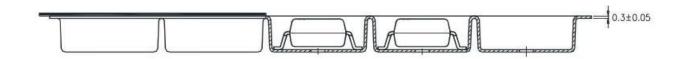
(1) ORPC-814S-TA





(2) ORPC-814S-TA1



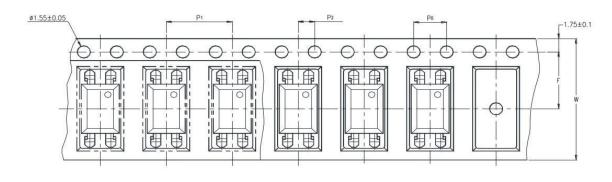


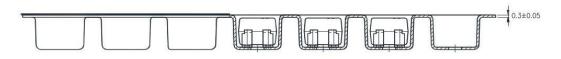
Description	Symbol	Dimension in mm (inch)
Tape wide	W	16±0.3 (.63)
Pitch of sprocket holes	P ₀	4±0.1 (.15)
Distance of commontment	F	7.5±0.1 (.295)
Distance of compartment	P2	2±0.1 (.0079)
Distance of compartment to compartment	P1	12±0.1 (.472)

Package Type	TA/TA1
Quantities(pcs)	1000



(3) ORPC-814S-TP

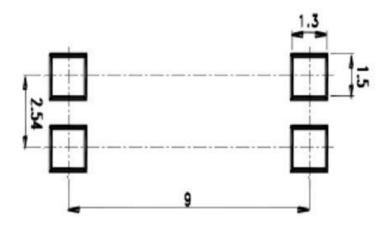




Description	Symbol	Dimension in mm (inch)
Tape wide	W	16±0.3 (.63)
Pitch of sprocket holes	P ₀	4±0.1 (.15)
Distance of compartment	F	7.5±0.1 (.295)
Distance of compartment	P2	2±0.1 (.0079)
Distance of compartment to compartment	P1	8±0.1 (.472)

Package Type	TP
Quantities(pcs)	2000

10. Recommended Foot Print Patterns (Mount Pad) (Unit: mm)



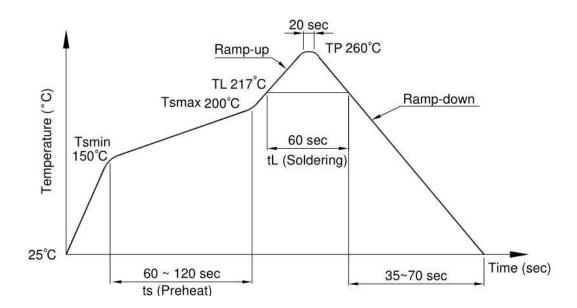


11. Temperature Profile Of Soldering

(1).IR Reflow soldering (JEDEC-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

Profile item	Conditions
Preheat	
- Temperature Min (T _{Smin})	150°C
- Temperature Max (T _{Smax})	200°C
- Time (min to max) (ts)	90±30 sec
Soldering zone	
- Temperature (TL)	217°C
- Time (t _L)	60 sec
Peak Temperature(T _P)	260°C
Ramp-up rate	3°C / sec max.
Ramp-down rate	3~6°C / sec

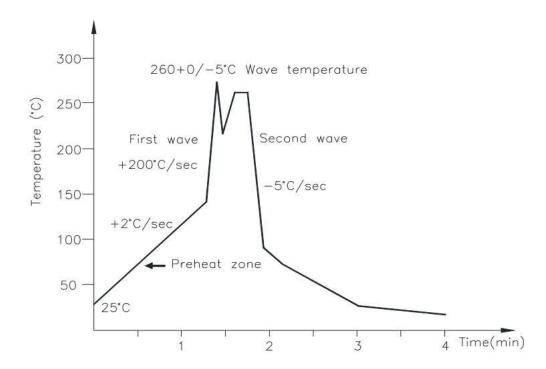




(2). Wave soldering (JEDEC22A111 compliant)

One time soldering is recommended within the condition of temperature.

Temperature	260+0/-5°C
Time	10 sec
Preheat temperature	25 to 140°C
Preheat time	30 to 80 sec



(3). Hand soldering by soldering iron

Allow single lead soldering in every single process. One time soldering is recommended.

Temperature	380+0/-5°C
Time	3 sec max



12. Characteristics Curves

Fig.1 Forword Current vs. Ambient Temperatute

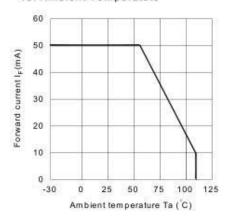


Fig.3 Collector-emitter Saturation Voltage vs. Forward Current

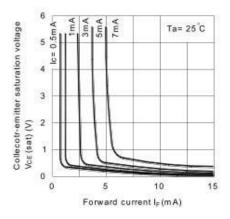


Fig.5 Current Transfer Ratio vs. Forward Current

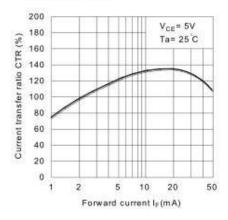


Fig.2 Collector Power Dissiption vs. Ambient Temperature

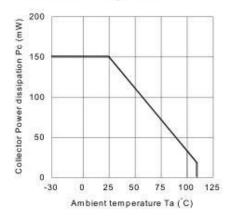


Fig.4 Forward Current vs. Forward Voltage

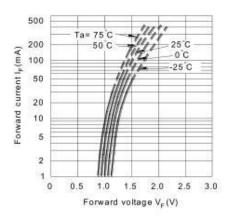


Fig.6 Collector Current vs.

Collector-emitter Voltage

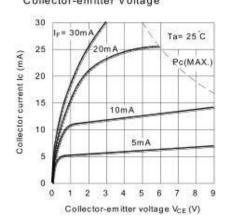




Fig.7 Relative Current Transfer Ratio vs. Ambient Temperature

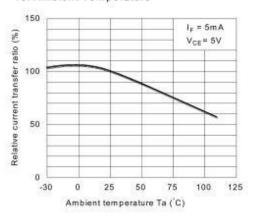


Fig.9 Collector Dark Current vs. Ambient Temperature

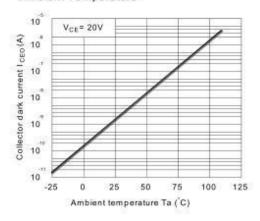


Fig.11 Frequency Response

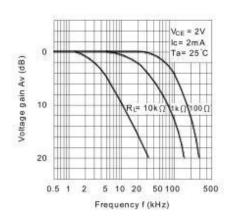


Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature

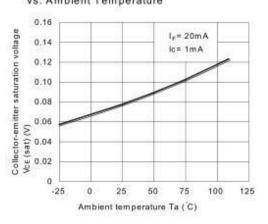
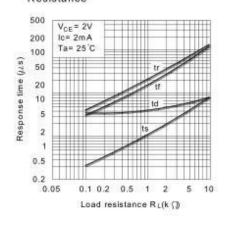
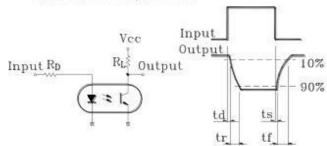


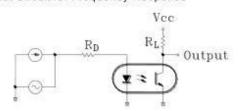
Fig.10 Response Time vs. Load Resistance



Test Circuit for Response Time



Test Circuit for Frequency Response





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