2SB0940 (2SB940), 2SB0940A (2SB940A)

Silicon PNP epitaxial planar type

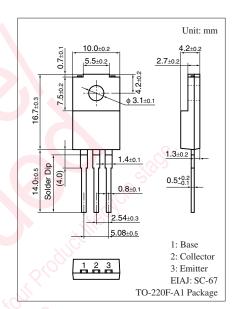
For power amplification
For TV vertical deflection output
Complementary to 2SD1264, 2SD1264A

■ Features

- ullet High collector-emitter voltage (Base open) V_{CEO}
- Large collector power dissipation P_C
- Full-pack package which can be installed to the heat sink with one screw

■ Absolute Maximum Ratings $T_C = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Er	V_{CBO}	-200	V	
Collector-emitter voltage	2SB0940	V _{CEO}	-150	V
(Base open)	2SB0940A		-180	
Emitter-base voltage (Coll	V _{EBO}	-6	V	
Collector current		I _C	-2	A
Peak collector current	I_{CP}	-3	A	
Collector power	P _C	30	W	
dissipation	$T_a = 25^{\circ}C$		2	
Junction temperature	T _j	150	°C	
Storage temperature	T_{stg}	-55 to +150	°C	



■ Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

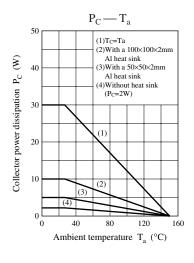
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Em	itter open)	V_{CBO}	$I_C = -50 \mu A, I_E = 0$	-200			V
Collector-emitter voltage	2SB0940	V _{CEO}	$I_{\rm C} = -5 \text{ mA}, I_{\rm B} = 0$	-150			V
(Base open)	2SB0940A		, Chill William	-180			
Emitter-base voltage (Colle	ctor open)	V _{EBO}	$I_E = -500 \mu\text{A}, I_C = 0$	-6			V
Base-emitter voltage		V_{BE}	$V_{CE} = -10 \text{ V}, I_{C} = -400 \text{ mA}$			-1	V
Collector-base cutoff current (Emitter open)		I_{CBO}	$V_{CB} = -200 \text{ V}, I_E = 0$			-50	μΑ
Emitter-base cutoff current (Col	llector open)	I_{EBO}	$V_{EB} = -4 \text{ V}, I_C = 0$			-50	μΑ
Forward current transfer ratio		h _{FE1} *	$V_{CE} = -10 \text{ V}, I_{C} = -150 \text{ mA}$	60		240	_
		h _{FE2}	$V_{CE} = -10 \text{ V}, I_{C} = -400 \text{ mA}$	50			
Collector-emitter saturation voltage		V _{CE(sat)}	$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$			-1	V
Transition frequency		f_T	$V_{CE} = -10 \text{ V}, I_{C} = -0.5 \text{ A}, f = 10 \text{ MHz}$		30		MHz

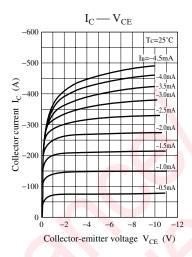
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

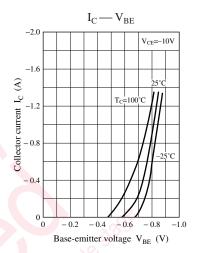
2. *: Rank classification

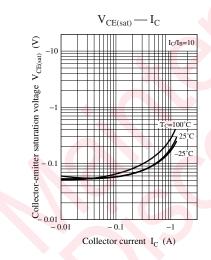
Rank	Q	Р		
h _{FE1}	60 to 140	100 to 240		

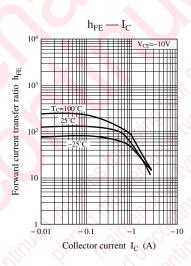
Note) The part numbers in the parenthesis show conventional part number.

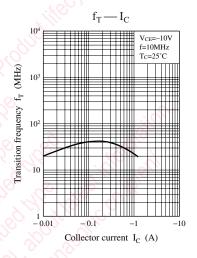


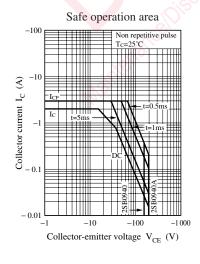


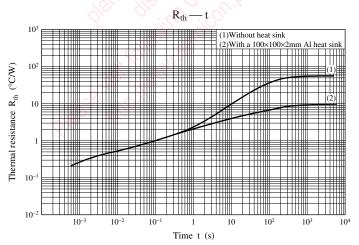












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