2SB1722J

Silicon PNP epitaxial planar type

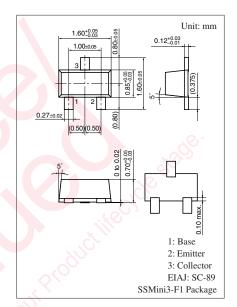
For high breakdown voltage low-frequency amplification

■ Features

- ullet High collector-emitter voltage (Base open) V_{CEO}
- SS-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V_{CBO}	-100	V	
Collector-emitter voltage (Base open)	V_{CEO}	-100	V	
Emitter-base voltage (Collector open)	V _{EBO}	-5	V	
Collector current	I_{C}	-20	mA	
Peak collector current	I_{CP}	-50	mA	
Collector power dissipation	P _C	125	mW	
Junction temperature	T_{j}	125	°C	
Storage temperature	T _{stg}	-55 to +125	°CO	

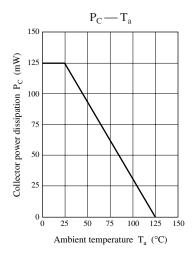


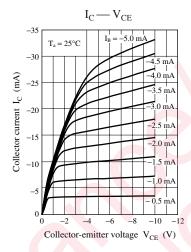
Marking Symbol: 4R

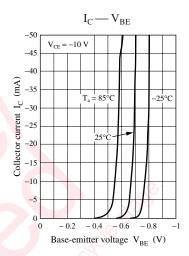
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

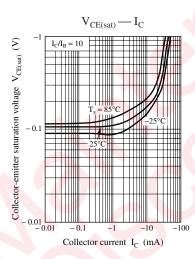
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = -10 \mu\text{A}, I_E = 0$	-100			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_{\rm C} = -1 \text{ mA}, I_{\rm B} = 0$	-100			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = -10 \ \mu A, I_C = 0$	-5			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$			-100	nA
Collector-emitter cut-off current (Base open)	I_{CEO}	$V_{CE} = -50 \text{ V}, I_{B} = 0$			-1	μΑ
Forward current transfer ratio	h_{FE}	$V_{CE} = -10 \text{ V}, I_C = -2 \text{ mA}$	200		700	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -1 \text{ mA}$			- 0.3	V
Transition frequency	f_T	$V_{CB} = -5 \text{ V}, I_E = 2 \text{ mA}, f = 200 \text{ MHz}$		200		MHz

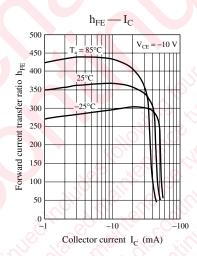
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

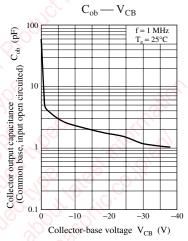












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