# 2SC3944, 2SC3944A

### Silicon NPN epitaxial planar type

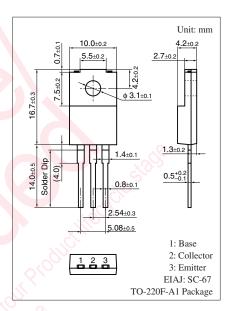
For low-frequency driver and high power amplification Complementary to 2SA1535 and 2SA1535A

#### ■ Features

- ullet Excellent collector current  $I_C$  characteristics of forward current transfer ratio  $h_{FE}$
- High transition frequency f<sub>T</sub>
- A complementary pair with 2SA1535 and 2SA1535A, is optimum for the driver stage of a 60 W to 100 W output amplifier
- Full-pack package which can be installed to the heat sink with one screw

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter			
2SC3944	$V_{CBO}$	150	V
2SC3944A		180	
2SC3944	V <sub>CEO</sub>	150	V
2SC3944A		180	
Emitter-base voltage (Collector open)			V .
Collector current			
Peak collector current			
$T_C = 25^{\circ}C$	$P_{C}$	15	S W
		2.0	
	$T_{j}$	150	°C
Storage temperature			
	2SC3944A 2SC3944A 2SC3944A lector open)	2SC3944A	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$



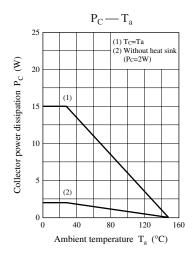
#### ■ Electrical Characteristics T<sub>a</sub> = 25°C ± 3°C

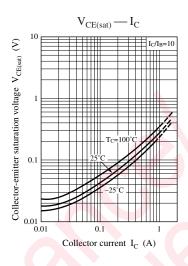
Parameter		Symbol Conditions		Min	Тур	Max	Unit
Collector-emitter voltage	2SC3944	$V_{CEO}$ $I_C = 1 \text{ mA}, I_B = 0$ 150				V	
(Base open)	2SC3944A		· Mi Mis	180			
Emitter-base voltage (Collector open)		$V_{EBO}$	$I_E = 10 \mu\text{A},  I_C = 0$	5			V
Collector-base cutoff current	2SC3944	$I_{CBO}$	$V_{CB} = 150 \text{ V}, I_E = 0$			10	μΑ
(Emitter open)	2SC3944A		$V_{CB} = 180 \text{ V}, I_{E} = 0$			10	
Forward current transfer ratio		h <sub>FE1</sub> *	$V_{CE} = 10 \text{ V}, I_{C} = 150 \text{ mA}$	65	160	330	_
		h <sub>FE2</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 500 \text{ mA}$	50	100		
Collector-emitter saturation	voltage	V <sub>CE(sat)</sub>	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		0.5	2.0	V
Base-emitter saturation volt	age	V <sub>BE(sat)</sub>	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		1.0	2.0	V
Transition frequency		$f_T$	$V_{CE} = 10 \text{ V}, I_{C} = 50 \text{ mA}, f = 10 \text{ MHz}$		200		MHz
Collector output capacitance		C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30	50	pF
(Common base, input open	circuited)						

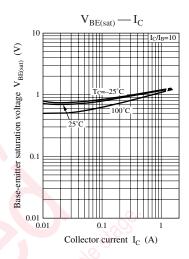
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

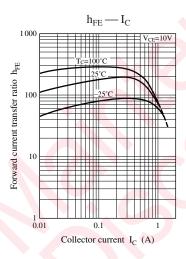
#### 2. \*: Rank classification

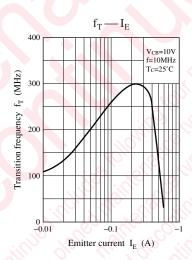
Rank	Р	Q	R	S
$h_{\rm FE1}$	65 to 110	90 to 155	130 to 220	185 to 330

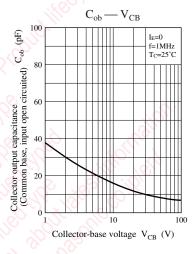


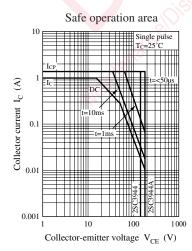












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