2SD1450

Silicon NPN epitaxial planar type

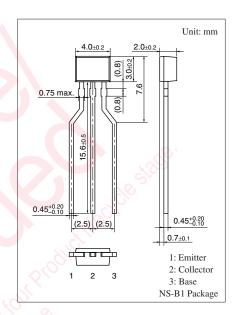
For low-frequency amplification

■ Features

- Optimum for high-density mounting
- Allowing supply with the radial taping
- ullet Low collector-emitter saturation voltage $V_{CE(sat)}$

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Symbol Rating	
Collector-base voltage (Emitter open)	V _{CBO}	25	V
Collector-emitter voltage (Base open)	V _{CEO}	20	V
Emitter-base voltage (Collector open)	V _{EBO}	12	V
Collector current	$I_{\mathbb{C}}$	0.5	A
Peak collector current	I_{CP}	1	A
Collector power dissipation	P _C	300	mW
Junction temperature	T_{j}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C



■ Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	25		10,	V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	20	4		V
Emitter-base voltage (Collector open)	$V_{\rm EBO}$	$I_E = 10 \mu A, I_C = 0$	12	767	0.5	V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 25 \text{ V}, I_{E} = 0$		10	100	nA
Forward current transfer ratio *1	h _{FE1} *2	$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}$	200	c0//	800	_
	h _{FE2}	$V_{CE} = 2 \text{ V}, I_{C} = 1 \text{ A}$	60	0.		
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_C = 500 \text{ mA}, I_B = 20 \text{ mA}$	7.0°	0.13	0.40	V
Base-emitter saturation voltage *1	V _{BE(sat)}	$I_C = 500 \text{ mA}, I_B = 20 \text{ mA}$			1.2	V
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		10		pF
(Common base, input open circuited)		The Time				
ON resistance *3	R _{on}	113 114		0.6		Ω

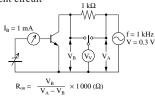
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

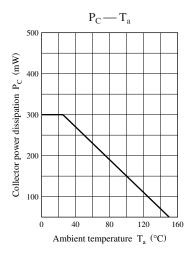
2. *1: Pulse measurement

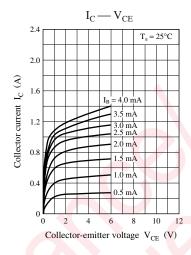
*2: Rank classification

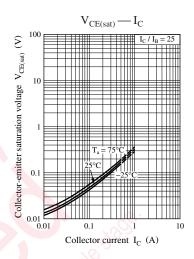
Rank	R	S	Т	No rank
h _{FE1}	200 to 350	300 to 500	400 to 800	200 to 800

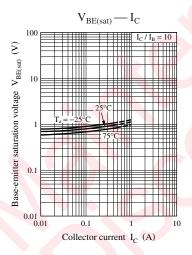
*3: R_{on} Measurement circuit

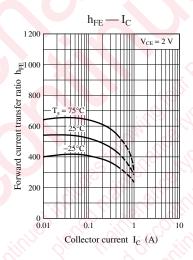


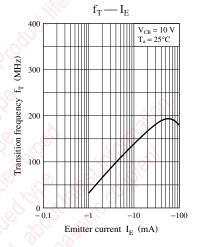


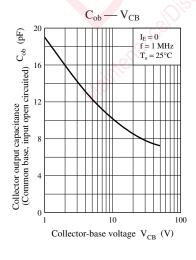


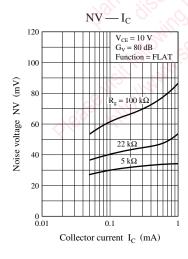












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