Transistors

## Panasonic

# 2SD1979

### Silicon NPN epitaxial planar type

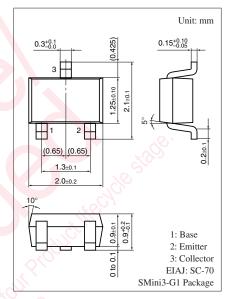
For low frequency amplification For muting For DC-DC converter

#### Features

- Low ON resistance R<sub>on</sub>
- $\bullet$  High forward current transfer ratio  $h_{FE}$
- S-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing and the magazine packing.

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit			
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	50	v			
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	20	V			
Emitter-base voltage (Collector open)	V <sub>EBO</sub>	25	V			
Collector current	I <sub>C</sub>	300	mA			
Peak collector current	I <sub>CP</sub>	500	mA			
Collector power dissipation	P <sub>C</sub>	150	mW			
Junction temperature	Tj	150	°C ∕o			
Storage temperature	T <sub>stg</sub>	-55 to +150	°C			



#### Marking symbol: 3W

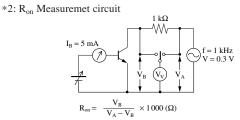
#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	20	p-		V
Base-emitter voltage	V <sub>BE</sub>	$V_{CE} = 2 V, I_C = 4 mA$	$\sim 2^{\circ}$	0.6		V
Collector-base cutoff current (Emitter open)	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_E = 0$			1	μΑ
Emitter-base cutoff current (Collector open)	I <sub>EBO</sub>	$V_{EB} = 25 \text{ V}, I_C = 0$			1	μΑ
Forward current transfer ratio *1	h <sub>FE</sub>	$V_{CE} = 2 V, I_C = 4 mA$	500		2 5 0 0	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 30 \text{ mA}, I_B = 3 \text{ mA}$			0.1	V
Transition frequency	f <sub>T</sub>	$V_{CB} = 6 V, I_E = -4 mA, f = 200 MHz$		80		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		4.5		pF
(Common base, input open circuited)						
ON resistance *2	R <sub>on</sub>			1		Ω

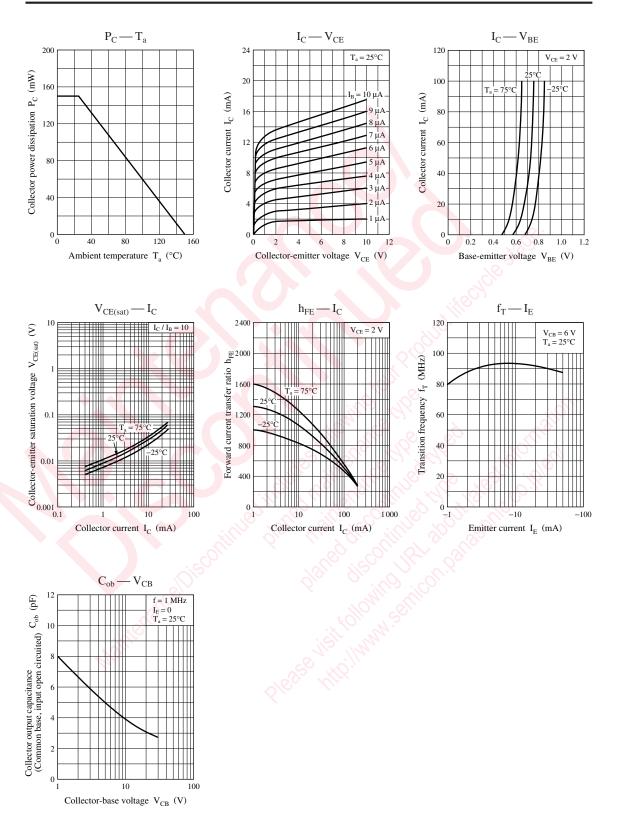
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. \*1: Rank classification

Rank	S	Т
h <sub>FE</sub>	500 to 1 500	800 to 2500



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