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Product Specifications AN7161N

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Doc No. SD

Structure	Silicon Monolithic Bipolar IC
Appearance	SIL-12 Pin Plastic Package (Power Type with Fin attached)
Application	Hi-Fi and Car Stereo
Function	BTL 23W Audio Power Amplifier

A	Absolute Maximum Ratings					
No.	Item Symbol Ratings Unit Note					
1	Storage Temperature	Tstg	-55 ~ +150	° C	1	
2	Operating Ambient Temperature	Topr	-30 ~ +75	° C	1	
3	Supply Voltage	Vcc	26	V		
4	Supply Current	Icc	4.0	A		
5	Power Dissipation	P_{D}	62.5	W		
6	Surge Voltage	V _{surge}	50	V		

Note: 1) The temperature of all items shall be Ta=25°C except storage temperature and operating ambient temperature.



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В	B Electrical Characteristics (Unless otherwise specified, the ambient temperature is 25°C ± 2°C)								
No	Item	Symbol	Test Cir-	Conditions	Limits		Unit	Note	
110	Item	Symbol	cuit	Conditions	min	typ	max	Omt	Note
1	Quiescent Circuit Current	I _{CQ}	1	V _{CC} =15V, V _{in} =0V	-	45	75	mA	
	Power Amplifier (VCC:	=15V, RL	=4Ω	, freq.=1kHz)					
2	Output Noise Voltage	V_{N}	1	$\begin{array}{l} f{=}15Hz\sim30kHz,\\ 12dB/oct,R_g{=}10k\Omega \end{array}$	-	0.6	1.0	mV	
3	Voltage Gain	Gv	1	V _{in} =5mV	48.5	50.5	52.5	dB	
4	Total Harmonic Distortion	THD	1	V _{in} =5mV	_	0.15	0.5	%	
5	Maximum Power Output	PO	1	THD=10%	20	23		W	
6	Output Offset Voltage	V _{OS}	1	$R_g=0\Omega$	A000	-	150	mV	
	Headphone Amplifier (V_{CC} =15V, R_L =33 Ω , freq.=1kHz)								
7	Output Noise Voltage	V _{N-H}	1	f=15Hz ~ 30kHz, 12dB/oct, R_g =10k Ω	-	0.1	0.7	mV	
8	Voltage Gain	G _{V-H}	1	V _{in} =10mV Power Amplifier mute	17.5	19.5	21.5	dB	
9	Maximum Output Power	P _{O-H}	1	THD=1% Power Amplifier mute	10		-	mW	

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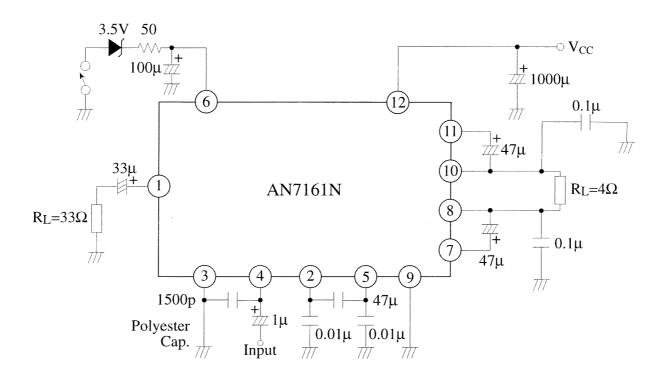
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(Description of test circuit and test method)

Test Circuit 1



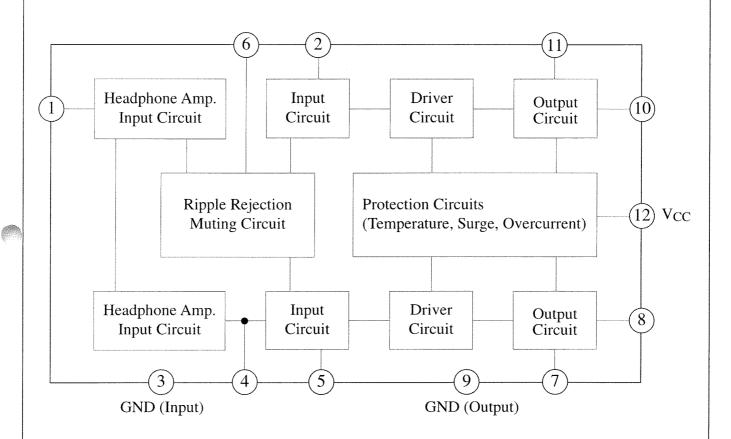
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Circuit Function Block Diagram



Pin Descriptions

Pin No.	Description	Pin No.	Description
1	Output (Headphone)	7	Bootstrap Channel 1
2	Negative Feedback Channel 2	8	Output Channel 1
3	GND (Input)	9	GND (Output)
4	Input	10	Output Channel 2
5	Negative Feedback Channel 1	11	Bootstrap Channel 2
6	Ripple Filter	12	Vcc

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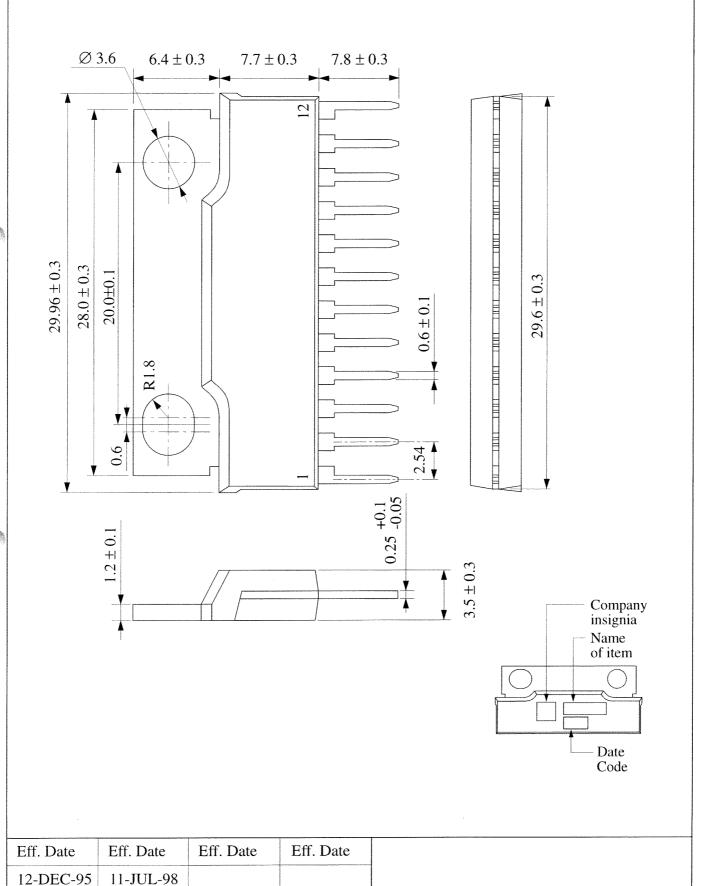
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Package Name

FP-12S

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Unit: mm



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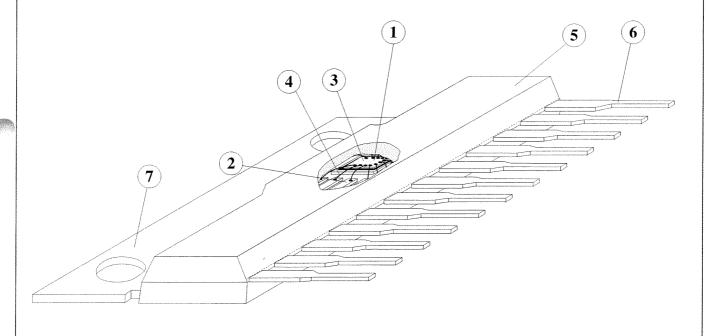
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(Structure Description)

Chip surface passivation	SiN,	PSG,	Others ()	1
Lead frame material	Fe group,	Cu group,	Others ()	2,6
Inner lead surface process	(Ag plating,	Au plating,	Others ()	2
Outer lead surface process	Solder plating,	Solder dip,	Others ()	6
Chip mounting method	Ag paste,	Au-Si alloy, Solder,	Others ()	3
Wire bonding method	Thermalsonic be	onding,	Others ()	4
Wire material, Diameter	(Au,	Diameter <u>50</u> μm	Others ()	4
Mold material	Epoxy,		Others ()	<u>(5)</u>
Molding method	Transfer mold,	Multiplunger mold,	Others ()	<u>(5)</u>
Fin material	Cu Group		Others ()	7

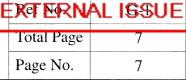
Package FP-12S



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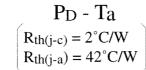
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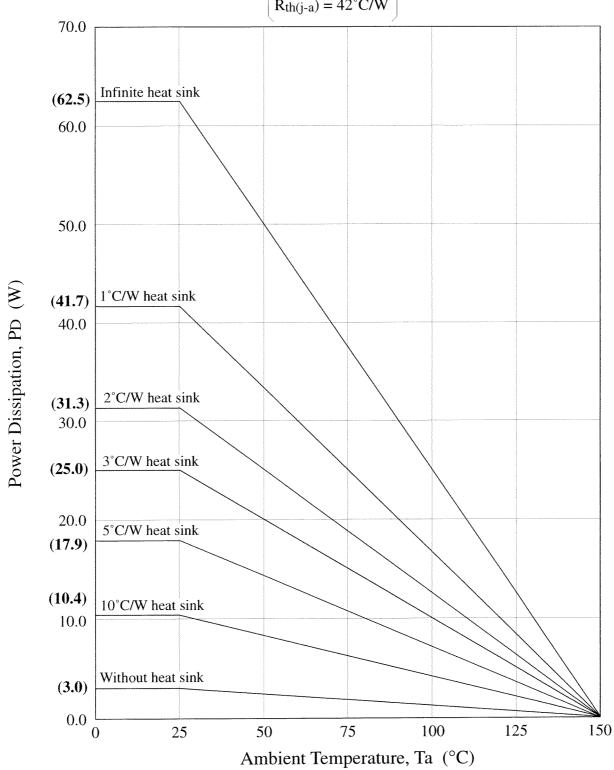
Product Specifications (Technical Data) AN7161N



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单击下面可查看定价,库存,交付和生命周期等信息

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