

Transistors with Built-in Resistor **DRA3144W0L**

DRA3144W0L

Silicon PNP epitaxial planar type

For digital circuits Complementary to DRC3144W DRA9144W in SSSMini3 type package

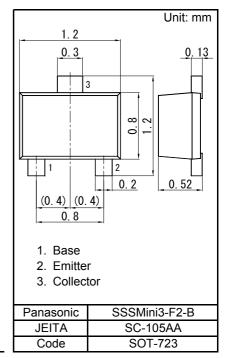
Features

- Low collector-emitter saturation voltage Vce(sat) ٠
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: LK

Packaging

1

Embossed type (Thermo-compression sealing) : 10 000 pcs / reel (standard)



Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	VCBO	-50	V	
Collector-emitter voltage (Base open)	VCEO	-50	V	Internal (
Collector current	IC	-100	mA	
Total power dissipation	PT	100	mW	R ₁
Junction temperature	Tj	150	°C	B O-
Operating ambient temperature	Topr	-40 to +85	°C	R ₂
Storage temperature	Tstg	-55 to +150	°C	

Our marks and

Internal Connection					
B O-C					
Resistance	R1	47	kΩ		
value	R2	22	kΩ		

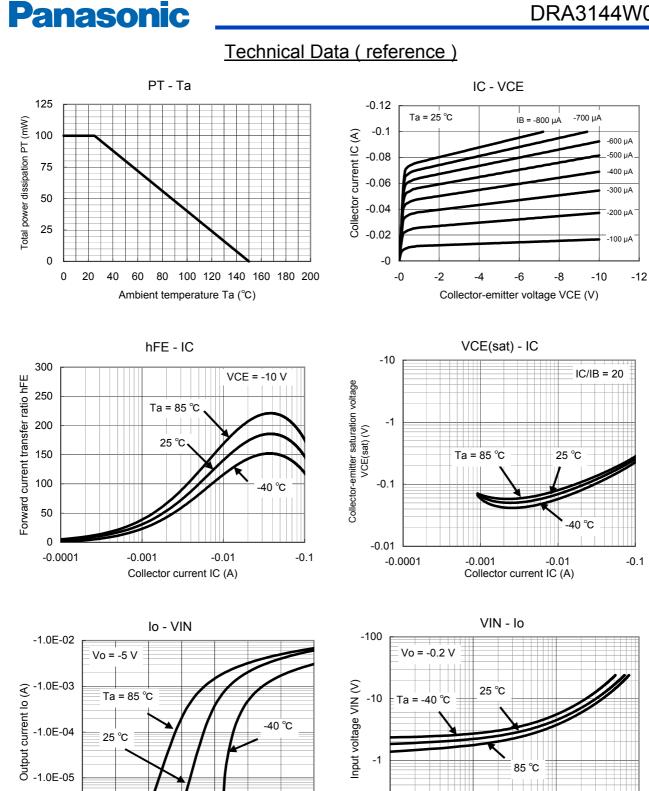
Electrical Characteristics	Ta = 25 °C ± 3 °C
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Absolute Maximum Ratings Ta = 25 °C

	<u>1</u> 00					
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = -10 μA, IE = 0	-50			V
Collector-emitter voltage (Base open)	VCEO	IC = -2 mA, IB = 0	-50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = -50 V, IE = 0			-0.1	μA
Collector-emitter cutoff current (Base open)	ICEO	VCE = -50 V, IB = 0			-0.5	μA
Emitter-base cutoff current (Collector open)	IEBO	VEB = -6 V, IC = 0			-0.2	mA
Forward current transfer ratio	hFE	VCE = -10 V, IC = -5 mA	60			-
Collector-emitter saturation voltage	VCE(sat)	IC = -10 mA, IB = -0.5 mA			-0.25	V
Input voltage	Vi(on)	VCE = -0.2 V, IC = -5 mA	-4.4			V
	Vi(off)	VCE = -5 V, IC = -100 μA			-1.2	V
Input resistance	R1		-30%	47	+30%	kΩ
Resistance ratio	R1/R2		1.70	2.14	2.60	-

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

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Established : 2009-10-23 Revised : 2014-02-20

-1.0E-06

-0

-0.5

-1

-1.5

Input voltage VIN (V)

-2

-2.5

-3

-3.5

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-0.1

-0.0001

-0.001

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-0.1

-0.01

Output current Io (A)



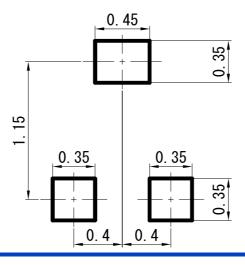
SSSMini3-F2-B

Transistors with Built-in Resistor DRA3144W0L

Unit: mm

1.20 ± 0.05 0.13-0.02 **0. 30**^{+0. 05} 0. 02 3 0.80±0.05 1.20 ± 0.05 20 2 1 **0. 20**+0. 05 -0. 02 0.20 ± 0.05 (0.4) (0.4) 0.80 ± 0.05 (5°) 27) 52 ± 0.03 ġ o' 0 to 0.05

Land Pattern (Reference) (Unit: mm)



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