DSA3G01

Silicon PNP epitaxial planar type

For high-frequency amplification DSA9G01 in SSSMini3 type package

Features

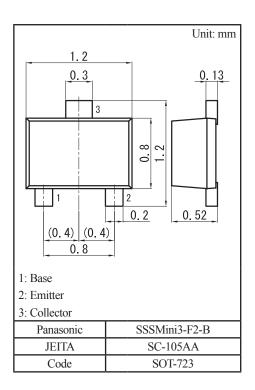
- \bullet High transition frequency f_{T}
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)
- Marking Symbol: A4

Packaging

DSA3G0100L Embossed type (Thermo-compression sealing): 10000 pcs / reel (standard)

ADSUME MAXIMUM Ralings $T_a = 23$ C								
Parameter	Symbol	Rating	Unit					
Collector-base voltage (Emitter open)	V _{CBO}	-30	V					
Collector-emitter voltage (Base open)	V _{CEO}	-20	V					
Emitter-base voltage (Collector open)	V _{EBO}	-5	V					
Collector current	I _C	-30	mA					
Collector power dissipation	P _C	100	mW					
Junction temperature	Tj	150	°C					
Operating ambient temperature	T _{opr}	-40 to +85	°C					
Storage temperature	T _{stg}	-55 to +150	°C					





Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Base-emitter voltage	V _{BE}	$V_{CE} = -10 \text{ V}, I_C = -1 \text{ mA}$		- 0.7		V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = -20$ V, $I_{\rm B} = 0$			-100	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = -5 \text{ V}, I_{\rm C} = 0$			-10	μΑ
Forward current transfer ratio	h _{FE}	$V_{\rm CE} = -10 \text{ V}, I_{\rm C} = -1 \text{ mA}$	70		220	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -1 \text{ mA}$		- 0.1		V
Transition frequency	f _T	$V_{\rm CE} = -10 \text{ V}, I_{\rm C} = -1 \text{ mA}$	150	300		MHz
Reverse transfer capacitance (Common emitter)	C _{re}	$V_{CE} = -10 \text{ V}, I_C = -1 \text{ mA}, f = 10.7 \text{ MHz}$		1.0		pF
Noise figure	NF	$V_{CE} = -10 \text{ V}, I_C = -1 \text{ mA}, f = 5 \text{ MHz}$		2.8		dB
Reverse transfer impedance	Z _{rb}	$V_{CE} = -10 \text{ V}, I_C = -1 \text{ mA}, f = 2 \text{ MHz}$		22		Ω

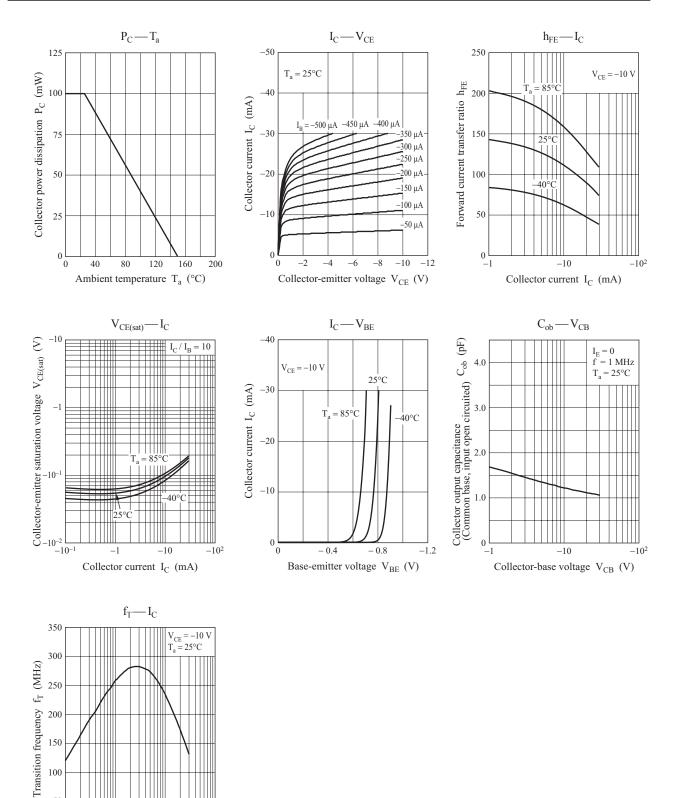
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

150

100

50

0



 -10^{2}

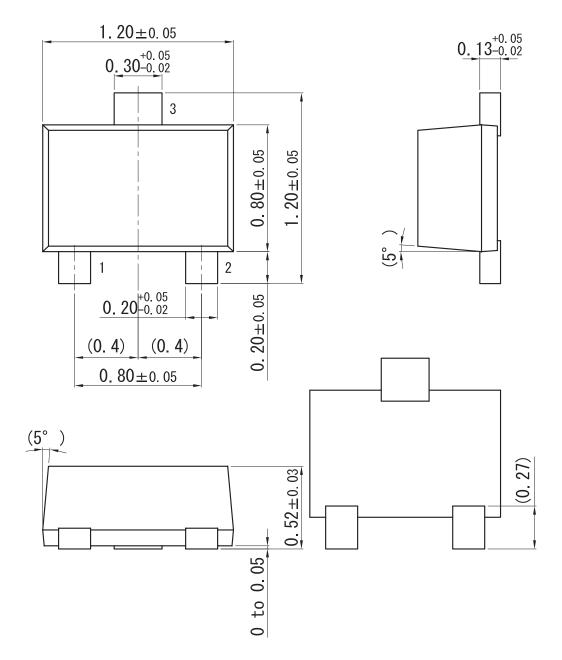
-10

 $^{-1}$

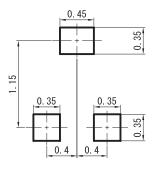
Collector current I_C (mA)

SSSMini3-F2-B

Unit: mm



Land Pattern (Reference) (Unit: mm)



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