# DSC5001

### Silicon NPN epitaxial planar type

#### For general amplification Complementary to DSA5001

#### Features

- $\bullet$  High forward current transfer ratio  $h_{FE}$  with excellent linearity
- Eco-friendly Halogen-free package

#### Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit				
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	60	V				
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V				
Emitter-base voltage (Collector open)	V <sub>EBO</sub>	7	V				
Collector current	I <sub>C</sub>	100	mA				
Peak collector current	I <sub>CP</sub>	200	mA				
Collector power dissipation	P <sub>C</sub>	150	mW				
Junction temperature	Tj	150	°C				
Storage temperature	T <sub>stg</sub>	-55 to +150	°C				

#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	60			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 2  {\rm mA},  I_{\rm B} = 0$	50			V
Emitter-base voltage (Collector open)	V <sub>EBO</sub>	$I_{\rm E} = 10 \ \mu {\rm A}, I_{\rm C} = 0$	7			V
Collector-base cutoff current (Emitter open)	I <sub>CBO</sub>	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 10 \text{ V}, I_{B} = 0$			100	μΑ
Forward current transfer ratio *	h <sub>FE</sub>	$V_{CE} = 10 \text{ V}, I_C = 2 \text{ mA}$	210		460	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = 100 \text{ mA}, I_{\rm B} = 10 \text{ mA}$		0.13	0.3	V
Transition frequency	$\mathbf{f}_{\mathrm{T}}$	$V_{CE} = 10 \text{ V}, I_C = 2 \text{ mA}$		150		MHz
Collector output capacitance (Common base, input open circuited)	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		1.5		pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. \*: Rank classification

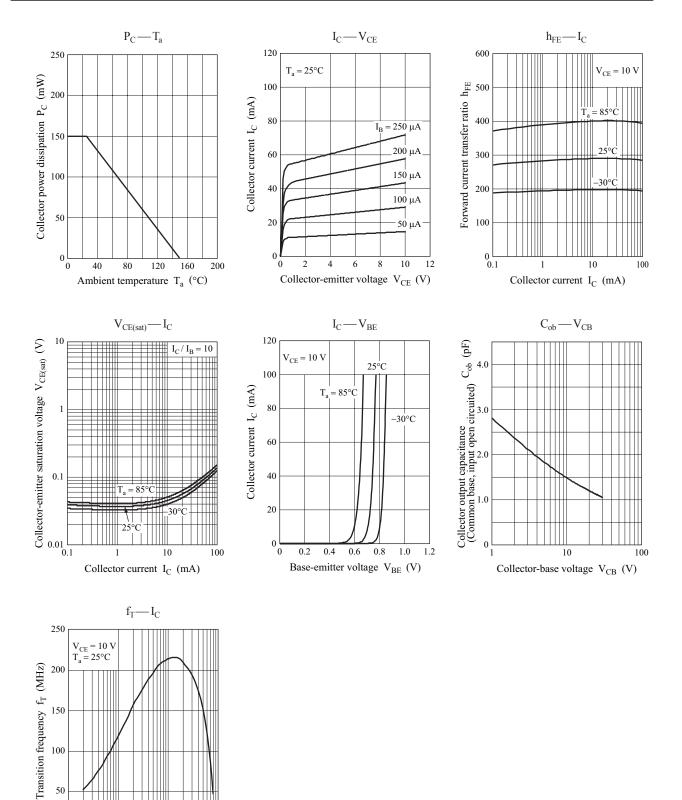
Code	R	S	0	
Rank	R	S	No-rank	
$h_{\rm FE}$	210 to 340	290 to 460	210 to 460	
Marking Symbol	C1R	C1S	C1	

Product of no-rank is not classified and have no marking symbol for rank.

- Package
- Code
- SMini3-F2-B • Pin Name
  - 1. Base
  - 1. Base
  - 2. Emitter
    3. Collector
- Marking Symbol: C1

#### DSC5001

### **Panasonic**



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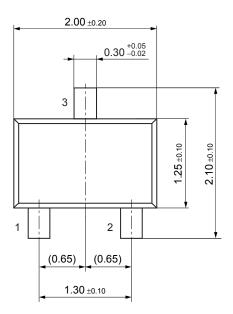
Collector current I<sub>C</sub> (mA)

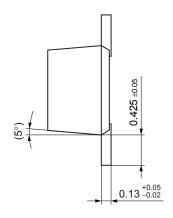
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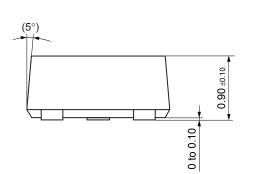
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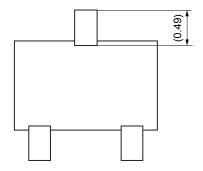
### SMini3-F2-B

Unit: mm









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