

DY2L4A0C0L1

TVS Diode DY2L4A0C0L1

Silicon epitaxial planar type

## For bidirectional ESD protection and transient voltage suppressor

#### Features

- IEC 61000-4-2 (ESD) ±15kV (air and contact)
- Low clamping voltage
- · Low capacitance
- Low leak current
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: F3

#### Packaging

Embossed type (Thermo-compression sealing): 1 000 pcs / reel (standard)

Absolute Maximum Ratings  $Ta = 25 \ ^{\circ}C$ ParameterSymbolRatingTotal power dissipation \*1PT100Electrostatic discharge \*2ESD±15

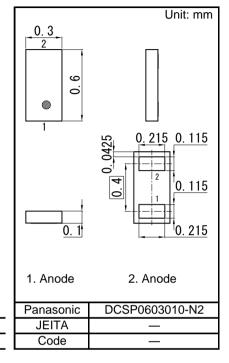
Peak pulse power *3	Ррр	21	W
Peak pulse current *3	lpp	2.1	Α
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C

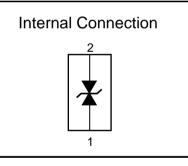
Note: \*1 Mounted on FR4 board. (25.4 mm x 25.4 mm x 1.0 mm)

\*2 Test method:IEC61000-4-2

(C = 150 pF, R = 330  $\Omega$ , Contact and Air discharge:10 times)

\*3 Test method:IEC61000-4-5 (tp = 8/20µs, Unrepeated)





### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Reverse stand-off voltage	VRWM	—			4.0	V	
Reverse breakdown voltage *1, *2	VBR	IR = 5 mA	6.42	6.90	7.38	V	
Reverse current	IR	VR = 4 V			50	nA	
Clamping voltage *3	Vc	lpp = 2.1 A, tp = 8/20 μs			12	V	
Terminal capacitance	Ct	VR = 0 V, f = 1 MHz		6.5		pF	

Unit

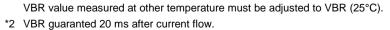
mW

kV

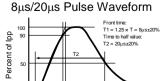
Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. Absolute frequency of input and output is 5 MHz.

3. \*1 The temperature must be controlled 25°C for VBR mesurement.

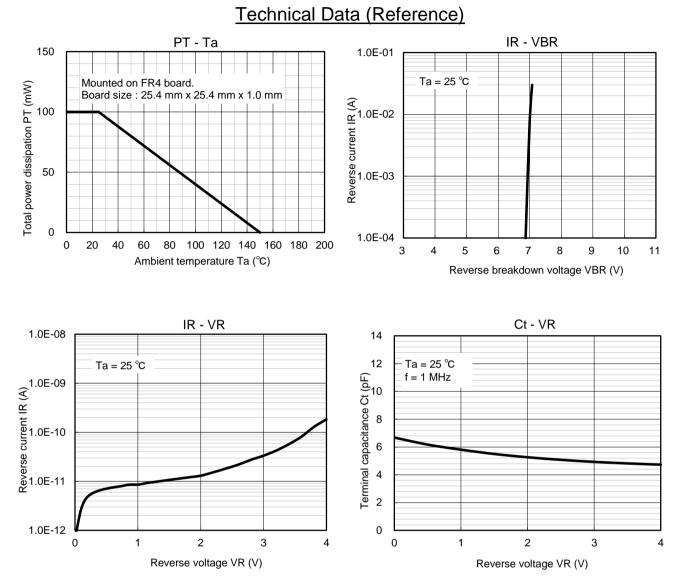


\*3 8µs/20µs Pulse Waveform

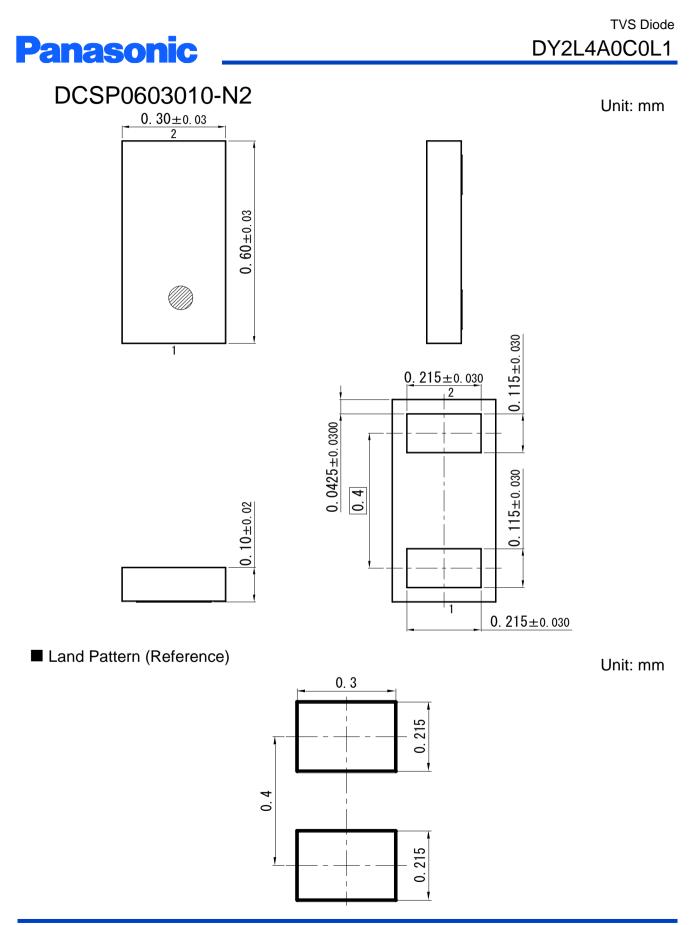




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