ubiect			Spec. No.	
Chip Resistor	esistor PRODUCT SPECIFICATION FOR IMFORMATION			
art No.	ERJ1TYF			
. Dimension			14 - 1	
	>			
		(1) Substrate : Alumina	Desin	
	W	(2) Protective Coating :(3) Resistive Element :		
		(4) Inner Termination :	•	
		(5) Middle Termination :		
(1) (2) (3)	(4) (5) (6)	(6) Outer Termination :	Sn Plating	
		* Under running changi	ng form Sn/Pb plating	
	t	to Sn plating.	5 1 5	
< <mark>b</mark> >	< <mark>⊳</mark> >			
	L W		t I	
Dimension (mm)	6.40+/-0.20 3.20+/-0.20	0.65+/-0.20 0.60+/-0.20	0.60+/-0.10 5.10+/-0.20	
. Power Derating Curve	3			
120				
× 100	70			
			ing temperature	
60 L		range	to 105 day C	
08 E			to 125 deg.C	
20				
	-40 -20 0 20 40 60 80	100 120 140 160		
-00	Ambient Temperature (
	Figu	ure 1		
	Ũ			
. Ratings				
3-1 Resistor				
-	Rated value	Explar		
3-1 Resistor Item		When used at ambient	temperature above 70	
3-1 Resistor Item	Rated value		temperature above 70 all be determined in	

Chip Resistor Part No.

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	2.10.1.1				
		-			
ltem	Rated value	Explanation			
Rated voltage &	The rated voltage of each resistance should be calculated from the equation below. And when the rated voltage exceeds the maximum RCWV,				
Rated Continuous	the maximum RCWV should	be the rated voltage.			
Working Voltage	Rated voltage = (Power rati	ng x Resistance Value) ^{1/2}			
	The maximum RCWV : 200	V			
	The overload voltage should	be 2.5 times the rated voltage. And when			
Overload Voltage	the voltage exceeds the max voltage should be the overloa	imum overload voltage, the maximum overload ad voltage.			
	The maximum overload volta	age : 400V			
Resistance Tolerance	F : +/- 1%				
Resistance	10 ohm – 1.0 M ohm (E-24	1 series)			
Circuit board of	You should use the aluminum	n or the ceramic substrate when the added			
use	wttage exceeds 0.5W.	wttage exceeds 0.5W.			
3-2 Jumper					
ltem	Rated value	Explanation			
		When used at ambient temperature above 70			
Ratied current	2 A	deg.C, power rating shall be determined in			
		accordance with Figure 1.			
Overload current	4 A				
Resistance value	Less than 50 m ohm				
I. Explanation of Par	t Number				
4-1 Resistor					
ER	JITY	F 1 0 2 U			
(1)	(2) (3)	(4) (5) (6)			
(1) Product Code :	Thick Film Chip Resistor				
(2) Size and Rated	Power: 6.4 mm x 3.2mm, 1.	0 W			
(3) Marking : Marki	ing on Black Side				
()					
(4) <u>Resistance Tole</u>	ance				
(4) Resistance Tole	erance Resistance Tolerance				

Subject		Spec. No.
Chip Resistor	PRODUCT SPECIFICATION FOR IMFORMATION	151-SRJ-E7049B
Part No.		
	ERJ1TYF	14 - 3
(5) Resistance	Value	
The first	two digits are the significant figures of resistance value, and the	last figure shows
the numbe	r of zero following in ohm.	
(6) Packaging	Configuration	
Code	Packaging Configuration	
U	Taping (4000pcs/reel)	
4-2 Jumper		
ER] J 1 T Y 0 R 0 0 U	
(1)	(2) (3) (4) (5)	_
(1) Product Co	de : Thick Film Chip Resistor	
(2) Size and R	ated Power : 6.4mm x 3.2 mm	
(3) Resistance	value	
Code	Resistance value	
0R00	Jumper (Less than 50 m ohm)	
(4) Packaging	Configuration	
Code	Packaging Configuration	
U	Taping (4000pcs/reel)	

5. Appearance & Construction

Item	Rated value	Explanation			
Appearance & Construction	 fade easily. The surface of and discoloration. 2. The electrode should be projection and discoloration. 2. The plating should not fact pinhole, projection and discoloration. 3. The electrode should be of element. 4. Dimensions of the substrational discoloration. 	connected electrically, mechanically to resistive te should be as in the list and it should not and crack. Details of appearance criteria shall			
As far as there s	As far as there shall not designation especially, the following tests and measurement shall be				
operated under t	operated under the following conditions.				
Normal temper	Normal temperature : 5 deg.C to 35 deg.C				
Normal humidity : 45 % to 85 %					

Normal atmospheric pressure : 86 k Pa to 106 k Pa

Chip Resistor Part No.

PRODUCT SPECIFICATION FOR IMFORMATION

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5. Performance Sp	ecification			
ltem	Specifica	tion	Teet Method (US C	E201 1)
item	Resistor	Jumper	Test Method (JIS-C	5201-1)
	DC resistance value shall			
DC resistance	be within the spe	cified	At 20 deg.C, 65%RH	
	tolerance.			
			Natural resistance change pe	r temperature
			degree centigrade.	
Temperature			TCR= $(R_2 - R_1) \times 10^6 / R_1 (t_2 - t_1)$	
coefficient	10 ohm – 1 M o		R ₁ : Resistance value at	reference
of resistance	: +/- 200 x 10	%deg.C	temperature (t ₁)	
(TCR)			R ₂ : Resistance value at	test
			temperature (t_2)	dog C
			t_1 : 25 deg.C , t_2 : 125	
Short time		Less than	Resistors shall be applied 2. voltage for 5 seconds.	
overload		50m ohm	Max. overload voltage shall k	ae 400\/
			Resistors shall be subjected	
			of 2.5 times the rated voltag	-
Intermittent	ΔR :	Less than	second with pause of 25 sec	
overload	+/-(5%+0.1ohm)	50m ohm	tests.	
			Max. overload voltage shall b	be 400V.
			AC 400V between substrate	and termination
	No evidence of fl	ashover,	for 1 minute.	
Dielectric	mechanical dama			AC power
Withstanding	or insulation brea			supply
				or
				Insulation
				resistance
Insulation	Min. 1000 M ohm	,		meter
resistance		I	Resistors shall be facing dow	
			After applying DC 400V to th	
			insulation resistance shall be	measured.

Chip Resistor Part No. PRODUCT SPECIFICATION FOR IMFORMATION

Spec. No.

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lteres	Specificatio	n	Test Mathed (US OF201 1)
ltem	Resistor	Jumper	Test Method (JIS-C5201-1)
	10 ohm – 1k ohm		
	: 0 dB (1	.0 μV/V)	
Noise	1.1k ohm – 9.1k ol	nm	Noise shall be measured by RESISTOR
	: 10 dB (3	.2 μV/V)	NOISE TEST SET MODEL 315C by
	10k ohm – 91k ohr	m	Quan-Tech Div.
	: 15 dB (5	.6 μV/V)	Max. V_R shall be 35 dB.
	100k ohm – 910k o	ohm	
	: 20 dB (1	0 μV/V)	

7. Machinery characteristic

Item	Specification		Test Method (US C5201.1)
nem	Resistor	Jumper	Test Method (JIS-C5201-1)
			Cupper plate : t=0.4mm Pull speed : 10mm/s
Terminal strength	Min. 4.9 N (500g))	Fixed Solder Sample → P Copper plate
Bending	No mechanical da	amage.	Substrate : Glass epoxy (t=1.6 mm) Span : 90mm Bending distance : 2mm (10 seconds) <test pattern=""> 1.85 5.2 1.85 4.0 (mm)</test>
strength	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	
			100

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Item	Specifica	tion	Toot Mathed (US C5201.1)
liem	Resistor	Jumper	Test Method (JIS-C5201-1)
Solderability	Termination shoul covered uniformly (Min. 95% covera	with solder	Resistors shall be dipped in the melted solder bath at 235 deg.C +/- 5 deg.C for 2 seconds +/- 0.5 second. Flux shall be removed from the surface of termination with clean organic solvent.
Resistance to soldering heat	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be dipped in the melted solder bath at 270 deg.C +/- 3 deg.C for 10 seconds +/- 1 second.
Resistance to vibration (Low frequency)	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be subjected to a single vibration having as double amplitude of 1.5 mm in 3 directions perpendicular one another for 2 hours each. (6 hours in total) The vibration frequency shall be varied uniformly from 10 Hz to 55 Hz, and return to 10 Hz traversing for 1 min.
Resistance to solvent	Without distinct deformation in ap ∆R : +/-(0.5%	pearance Less than 50m ohm	Solvent solution : Isopropyl alcohol (1)Dipping 10 +/- 1 hours, dry in room condition for 30 +/- 10 minutes. (2)Ultrasonic wave washing : 5 +/- 1 min. (0.3W/cm,28kHz)
solvent			

Subiect Chip Resistor

Chip Resist

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8. Environmental test

ltem	Specifica	tion	Test Method (JIS-C5201-1)
lleni	Resistor	Jumper	
Low temperature exposure	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be exposed at -55 deg.C +/- 3 deg.C with no load for 1000 hours +48/-0 hours.
Low temperature Operating	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be placed at -65 deg.C +/- 5 deg.C for 1.5hours. After applying RCWV for 45 minutes, resistors shall be exposed in room condition for 8 hours +/- 1 hour.
High temperature exposure	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be exposed at 125 deg.C +/- 3 deg.C with no load for 1000 hours +48/-0 hours.
Temperature cycling	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be tested for 5 cycles continuously in accordance with the following duty cycle.StepTemperature (deg.C)Time (min.)1-55 +/-3302Room temperatureMax. 33+125 +/-3304Room temperatureMax.3
Humidity (Steady state)	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Resistors shall be exposed at 60 deg.C +/- 2 deg.C and 90% to 95% relative hummidity in a humidity test chamber for 1000 hours +48/-0 hours.
Salt spray	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Spray 5 +/- 1 wt% salt water for 96 +/- 4 hours at 35 +/-2 deg.C.
Load life	∆R: +/-(3%+0.1ohm)	Less than 50m ohm	Resistors shall be operated at DC rated powe (1.5 hours "ON", 0.5 hours "OFF") for 1000 hours +48/-0 hours in a test chamber controlled at 70 deg.C +/-2 deg.C.
Load life in humidity	∆R : +/-(3%+0.1 ohm)	Less than 50m ohm	Resistors shall be operated at DC rated powe (1.5 hours "ON", 0.5 hours "OFF") for 1000 hours +48/-0 hours in a test chamber controlled at 60 deg.C +/- 2 deg.C and at 90 % to 95% in relative hummidity.

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ltom	Specification		Test Mathed (US CE201.1)
Item	Resistor	tor Jumper	Test Method (JIS-C5201-1)
Boiling test	∆R : +/-(1%+0.05ohm)	Less than 50m ohm	Soak in boiling water for 1 +/- 0.1 hour. Resistors shall be exposed at room condition for 30 minuites.

9. Other characteristics

Item	Specification	Test method
Surface Temperature rise	Less than 50 deg.C	Resistors shall be mouted on the Ceramic substrate (t=1.0mm). And the hot spot temperarure rise of chip resistor shall be measured when applied the rated power. Applied voltage must not exceed maximun RCWV.

10. Resistance value Marking

Express resistance value on resin side with three digits.



<Example>

<u>123 = 12k ohm</u>

000 = Chip jumper

The first two digits should be significant figures of resistance for E-24 series and the third one denotes number of zeros.

Subiect Chip Resistor

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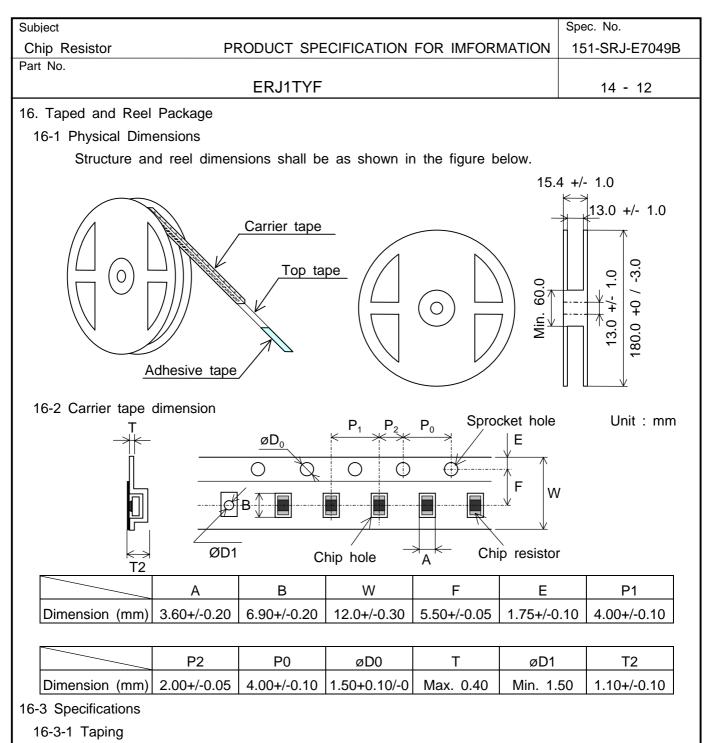
151-SRJ-E7049B

11. Common precautions in handling resistors

Ϊ	. 00	
-		<u>/!</u> Notice for use
	(1)	This specification shows the quality and performance of a unit component. Before adoption
		be sure to evaluate and verify the product mounting it in your product.
	(2)	We take no responsibility for troubles caused by the product usage that is not specified in
		this catalog. Be sure to exchange the delivery specification with us.
	(3)	Use fail-safe design and ensure safety by carrying out the following items in cases where it is forecast that the failure of the product gives serious damage to something important like human life, for instant in traffic transportation equipment (trains, cars, traffic signal equipment, etc.), medical equipment, aerospace equipment, electric heating appliances, combustion and gas equipment, rotating equipment, disaster and crime preventive equipment.
		*Ensure safety as the system by setting protective circuits and protective equipment.
		*Ensure safety as the system by setting such redundant circuits as do not cause danger by a single failure.
	(4)	When a dogma shall be occurred about safety for this product, be sure to inform us rapidly, operate your technical examination.
	(5)	The product is designed to use in general standard applications of general electric
		equipment (AV products, household electric appliances, office equipment, information and
		communication equipment, etc.); hence, it do not take the use under the following special
		environments into consideration.
		Accordingly, the use in the following special environments, and such environmental
		conditions may affect the performance of the product; prior to use, verify the performance,
		reliability, etc. thoroughly.
		1) Use in liquids such as water, oil, chemical, and organic solvent.
		2) Use under direct sunlight, in outdoor or in dusty atmospheres.
		3) Use in places full of corrosive gases such as sea breeze, CI_2 , H_2S , NH_3 , SO_2 , and NO_X .
		4) Use in environment with large static electricity or strong electromagnetic waves.
		5) Where the product is close to a heating component, and where an inflammable such as
		a polyvinyl chloride wire is arranged close to the product.
		6) Where the resistor is sealed or coated with resin, etc.
		7) Where water or a water-soluble detergent is used in cleaning free soldering and in flux
		cleaning after soldering (Pay particular attention to soluble flux.)
	(6)	If transient load (heavy load in a short time) like pulse is expected to be applied, carry out
		evaluation and confirmation test with resistors actually mounted on your own board. When
		the load of more than rated power is applied under the load condition at steady state, it
		may impair performance and/or reliability of resistor. Never exceed the rated power. When

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 the product shall be used under special condition, be sure to ask us in advance. (7) Halogen type (Chlorine type, Bromine type, etc.) or other high-activity flux is not recommended as the residue may affect performance or reliability of resistors. (8) When soldering with soldering iron, never touch the body of the chip resistor with a tip of the soldering iron. When using a soldering iron with a tip at high temperature, solder for a time as short as possible. (three seconds or less up to 350 deg.C) (9) Avoid physical shock to the resistor and nipping of the resistor with hard tool (a pair of pliers or tweezers) as it may damage protective film or the body of resistor and may affect resistor's performance. (10) Avoid immersion of chip resistor in solvent for long time. Use solvent after the effect of 				
firmed.				
adly affected, avoid the storage in the following end full of corrosive gases such as sea breeze, CI_2 , H exposed to direct sunlight. Dutside the temperature range of 5 deg.C to 35 c to 85 %RH. antee for performance such as solderability is 1 y	nvironm I₂S, NH leg.C a vear aft	nents. H ₃ , SO ₂ , and NO _X . and humidity er our delivery; and		
col. in this part are registered material under the Law Regulation of Manufacturs, etc. of Chemical substa sed in this part contain no brominated materials c	Conce ances. of PBB0	erning the D _s or PBB _s as the		
	ERJ1TYF be used under special condition, be sure to ask of prine type, Bromine type, etc.) or other high-activit the residue may affect performance or reliability of the soldering iron, never touch the body of the chi When using a soldering iron with a tip at high to possible. (three seconds or less up to 350 deg.C) ock to the resistor and nipping of the resistor with a as it may damage protective film or the body of nce. If chip resistor in solvent for long time. Use solver firmed. If in the following environments and conditions, the addy affected, avoid the storage in the following en- ull of corrosive gases such as sea breeze, Cl ₂ . H exposed to direct sunlight. Dutside the temperature range of 5 deg.C to 35 of to 85 %RH. antee for performance such as solderability is 1 y es only to the case where the storage method sp ot been manufactured with any ozone-depleting c col. in this part are registered material under the Law Regulation of Manufacturs, etc. of Chemical substates and in this part contain no brominated materials c	be used under special condition, be sure to ask us in a brine type, Bromine type, etc.) or other high-activity flux the residue may affect performance or reliability of resist th soldering iron, never touch the body of the chip resis When using a soldering iron with a tip at high tempera possible. (three seconds or less up to 350 deg.C) ck to the resistor and nipping of the resistor with hard t as it may damage protective film or the body of resist nce. f chip resistor in solvent for long time. Use solvent after firmed. If in the following environments and conditions, the perfor adly affected, avoid the storage in the following environm ull of corrosive gases such as sea breeze, Cl ₂ , H ₂ S, NH exposed to direct sunlight. butside the temperature range of 5 deg.C to 35 deg.C at to 85 %RH. antee for performance such as solderability is 1 year aft es only to the case where the storage method specified ot been manufactured with any ozone-depleting chemica col. in this part are registered material under the Law Conce Regulation of Manufacturs, etc. of Chemical substances. sed in this part contain no brominated materials of PBBC tice by letter of "A preliminary judgement on the Laws of		

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Chip Resistor	PRODUCT SPECIFICATION FOR IMFORMATION	151-SRJ-E7049B
Part No.		
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14. Receipt and Valid tir	ne limit for Product Specification for Information	
(1) Be sure to return	a copy to our company after stamping your company ac	cceptance.
If you shall not re	eturn a copy by 3 months which a valid time limit of listing	ng shall expire, we
judge that you sh	all receive this specification.	
(2) By 3 months that	a valid time limit of listing shall expire, if there shall not	offering it by letter,
a valid time limit s	shall continue every 1 year.	
If an alteration sha	all be done on the way futher more, a former specificatio	n shall become
invalid.		
15. Production site		
* Country : Japan		
Plant : Fukui Matsu	ushita Electric Co.,Ltd.	
* Country : Malaysia		
Plant : Matsushita I	Electronic Device (M) Sdn. Bhd. <medem></medem>	
* Country : China		
Plant : Tianjin Mats	sushita Electronic Components Co.,Ltd. <tmcom></tmcom>	
-		



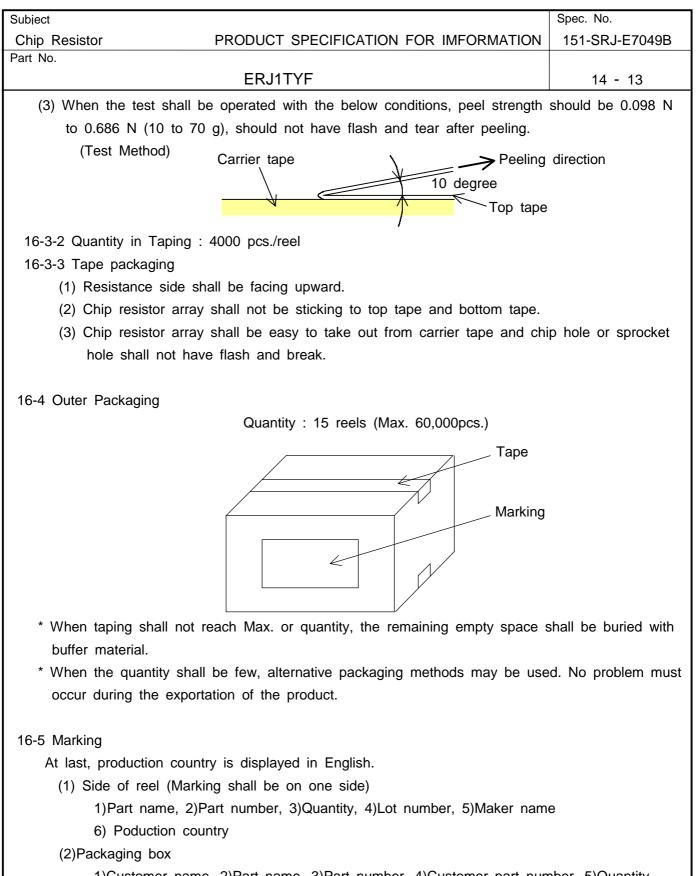
(1) Minimum Bending Radius

When Carrier tape shall be bent by Minimum Bending Radius (15mm), no defection of chip and no break of carrier tape. However minimum bending radius shall be tested for 1 time.

(2) Resistance to climate

When resistors shall be exposed at 60 deg.C, 90 to 95 %RH for 120 hours, no defection of chip and no break of carrier tape.

When the top tape shall be peeled tape should not have flash and tear.



1)Customer name, 2)Part name, 3)Part number, 4)Customer part number, 5)Quantity 6)Maker name, 7)Poduction country

Part No.

Chip Resistor

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ERJ1TYF
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7 4			
7. Appearance Quality			
Item	Figure	Appearance quality criteria	Remark
Protective coating Chipping	W A A	A≤W/4 B≤C/2	Chipping on both sides shall be considered defective
Terminal Chipping		A≤W/4 B≤Terminal width	
Pin hole	-+	One pin hole / chip resistor ∳≤0.2 mm	Pin hole penetrates The resistive material.
Flash		A≤0.1 mm	
Top terminal Lacking	w A	A≤W/4	
Side terminal Lacking		A≤W/4	
Protective coating and terminal aberration		Protective coating and terminal aberration shall be within the terminal width dimension	
Marking		Marking must be readable.	

单击下面可查看定价,库存,交付和生命周期等信息

>>Panasonic(松下)