Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

Panasonic

FC8V36060L

Dual N-channel MOS FET

For lithium-ion secondary battery protection circuits For load switching

■ Features

- Low drain-source ON resistance:RDS(on) typ. = 70 m Ω (VGS = 4.5 V)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)
- Marking Symbol: M2
- Packaging

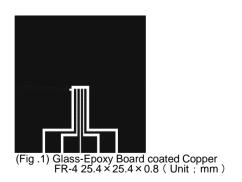
Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

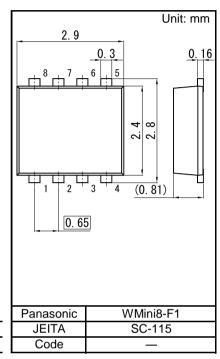
■ Absolute Maximum Ratings Ta = 25 °C

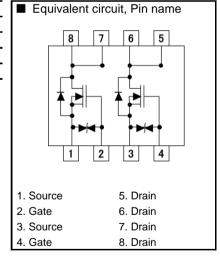
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	VDS	60	V
Gate-source Voltage	VGS	±20	V
Drain Current (DC) *1	ID	3	Α
Drain Current (Pulsed) *1,*2	IDp	9	Α
Total Power Dissipation *1 (Steady State)	PD	1.3	W
Channel Temperature	Tch	150	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Note '	'1	Device mounted on a	glass-epoxy	board coated	Copper	see Fig . 1	1)
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^{*2} Ensure that the channel temperature does not exceed 150°C







MOS FET

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■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	60			V	
Zero Gate Voltage Source Current	IDSS	VDS = 60 V, VGS = 0 V			10	μΑ	
Gate-source Leakage Current	IGSS	$VGS = \pm 16 \text{ V}, VDS = 0 \text{ V}$			±10	μΑ	
Gate-source Threshold Voltage	Vth	ID = 480 μA, VDS = 10 V	1.0		3.0	V	
Drain-source On-state Resistance	RDS(on)1	ID = 1.5 A, VGS = 10 V	ı	60	85	mΩ	
	RDS(on)2	ID = 1.5 A, VGS = 4.5 V	ı	70	100		
Input Capacitance *1	Ciss			360			
Output Capacitance *1	Coss	VDS = 10 V, VGS = 0 V, f = 1 MHz		50		pF	
Reverse Transfer Capacitance *1	Crss			20			
Turn-on delay Time *1,*2	td(on)	VDD = 30 V, VGS = 0 to 10 V		9		ns	
Rise Time *1,*2	tr	ID = 3.0 A		7		115	
Turn-off delay Time *1,*2	td(off)	VDD = 30 V, VGS = 10 to 0 V		38		ns	
Fall Time *1,*2	tf	ID = 3.0 A		6		115	
Total Gate Charge *1	Qg	VDD = 30 V		7.0			
Gate-source Charge *1	Qgs	VGS = 0 to 10 V,		1.4		nC	
Gate-drain Charge *1	Qgd	ID = 3.0 A		1.2			
Body Diode Forward Voltage	VSD	IF = 1.5 A, VGS = 0 V		8.0	1.2	V	

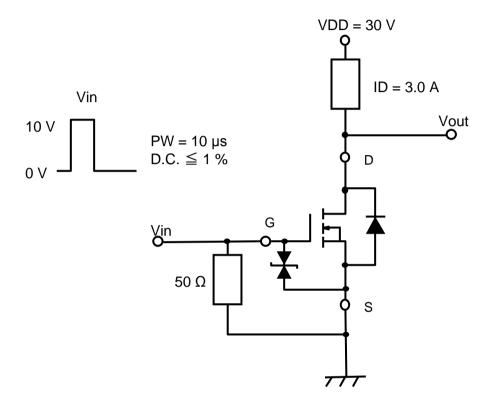
Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

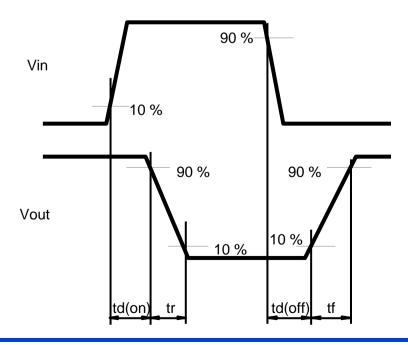
^{*1} Assured by design

^{*2} Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time

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*2 Measurement circuit for Turn-on delay time / Rise time / Turn-off delay time / Fall time





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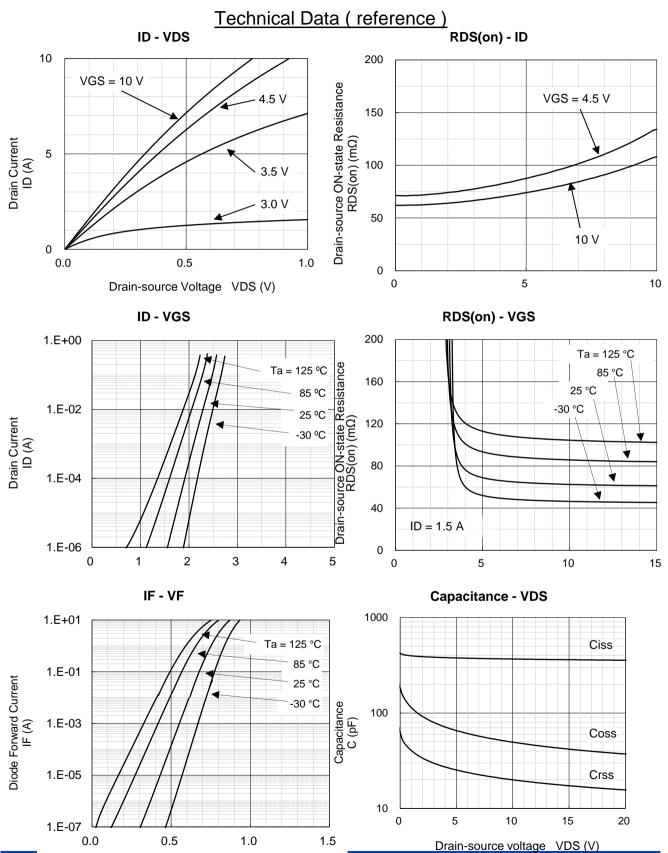
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100 ms

100

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Technical Data (reference)

Drain Current ID (A)

0.1

0.01

0.001

0.1

Operation in this area is limited by R_{DS}(on) (4.5 V) I_{DP} = 9 A

10

1 Ims
10 ms

Mounted on FR4 board (25.4 × 25.4 × t0.8

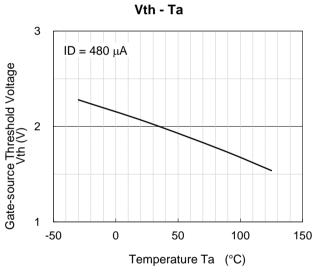
coated with copper foil

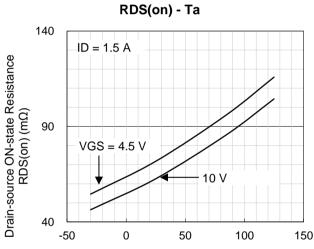
Safe Operating Area

Drain-source Voltage VDS (V)

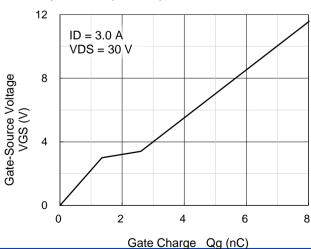
Temperature Ta (°C)

10





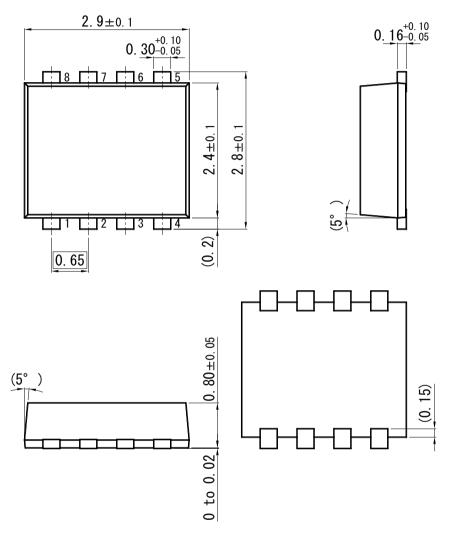
Dynamic Input/Output Characteristics



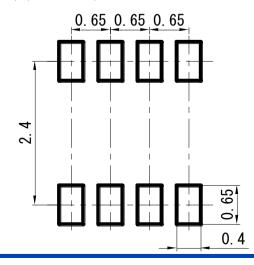
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WMini8-F1
Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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