MA2SV01

Silicon epitaxial planar type

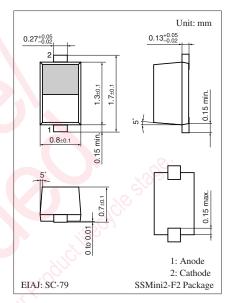
For VCO

■ Features

- \bullet Good linearity and large capacitance-ratio in $C_D V_R$ relation
- Small series resistance r_D
- SS-Mini type package, allowing downsizing of equipment and automatic insertion through the taping package

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Reverse voltage	V _R	6	V	
Junction temperature	Tj	150	°C	
Storage temperature	T_{stg}	-55 to +150	°C	



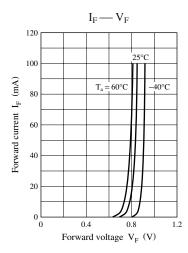
Marking Symbol: U

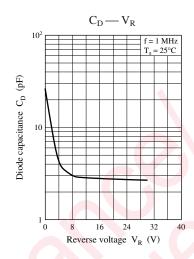
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

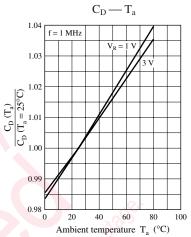
Symbol	Conditions	Min	Тур	Max	Unit
I_R	$V_R = 6 \text{ V}$	100	0,	10	nA
C _{D(1V)}	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$	15.0)-	17.0	pF
$C_{D(3V)}$	$V_R = 3 V, f = 1 MHz$	5.0		7.0	
$C_{D(1V)} / C_{D(3V)}$	612 P. 1100 100	2.2			_
r_{D}	$C_D = 9 \text{ pF, f} = 470 \text{ MHz}$			1.0	Ω
	$\begin{array}{c} I_{R} \\ C_{D(1V)} \\ C_{D(3V)} \\ C_{D(1V)} / C_{D(3V)} \end{array}$	$\begin{split} & I_{R} & V_{R} = 6 \ V \\ & C_{D(1V)} & V_{R} = 1 \ V, \ f = 1 \ MHz \\ & C_{D(3V)} & V_{R} = 3 \ V, \ f = 1 \ MHz \\ & C_{D(1V)} / C_{D(3V)} \end{split}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

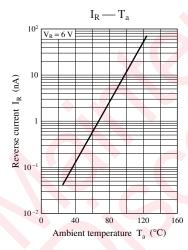
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

- 2. Absolute frequency of input and output is 470 MHz.
- 3. *: Measuring instrument; YHP MODEL 4191A RF IMPEDANCE ANALYZER









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