# **MAS3132E**

## Silicon epitaxial planar type

For high-speed switching circuits

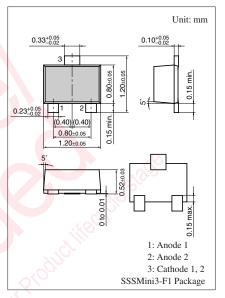
#### ■ Features

- Two elements are contained in one package, allowing highdensity mounting
- Short reverse recovery time t<sub>rr</sub>
- Small terminal capacitance C<sub>t</sub>

### ■ Absolute Maximum Ratings $T_a = 25$ °C

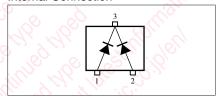
Parameter		Symbol	Rating	Unit	
Reverse voltage		$V_R$	80	V	
Maximum peak reverse voltage		$V_{RM}$	80	V	
Forward current	Single	$I_{\mathrm{F}}$	100	mA	
	Double		150		
Peak forward current	Single	$I_{FM}$	225	mA	
	Double		340		
Non-repetitive peak	Single	$I_{FSM}$	500	mA	
forward surge current *	Double		750		
Junction temperature		$T_{j}$	150	°C	
Storage temperature		T <sub>stg</sub>	-55 to +150	°C .o	

Note) \*: t = 1 s



Marking Symbol: MU

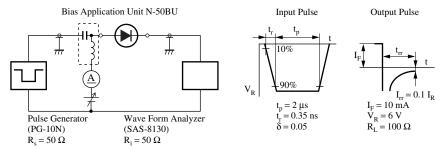
#### Internal Connection

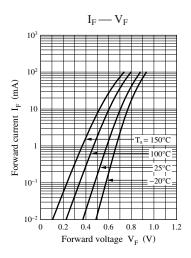


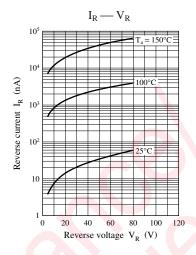
## ■ Electrical Characteristics T<sub>a</sub> = 25°C ± 3°C

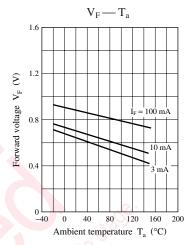
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	$V_{\rm F}$	I <sub>F</sub> = 100 mA	1.90		1.2	V
Reverse voltage	$V_R$	$I_R = 100 \mu A$	80			V
Reverse current	$I_R$	V <sub>R</sub> = 75 V			100	nA
Terminal capacitance	C <sub>t</sub>	$V_R = 0 \text{ V, f} = 1 \text{ MHz}$			2	pF
Reverse recovery time *	t <sub>rr</sub>	$I_F = 10 \text{ mA}, V_R = 6 \text{ V}$			3	ns
"VSI		$I_{rr} = 0.1 I_R$ , $R_L = 100 \Omega$				

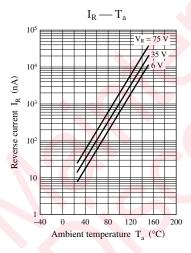
- Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring method for diodes.
  - 2. Absolute frequency of input and output is 100 MHz.
  - 3. \*: t<sub>rr</sub> measurement circuit

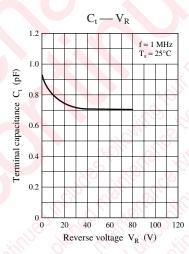


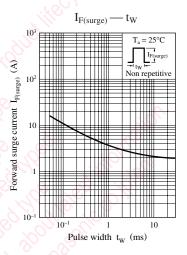












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