Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

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MIP2E4DMTSCF

種別/Type シリコン M		シリコン MOS形集利	責回路/Sill	icon MOSFET type l	ntegrated Circu	iit				
用途/Application スイッチング電源制行		卸用/For a Switching Power Supply Control								
構造。	構造/Structure CMOS形/CMOS t									
外形/Out Line DIP7-A1-B			マーク記号/マーキング / Markin				ng MIP2E4D			
A. 約	色 対最大定格/	ABSOLUTE MAXIMU	JM RATING	GS (Ta=25℃±3	°C)					
NO.	項目/Item		記号/ Symbol	定格/Ratings		単位/ Unit	備考/Note			
1	ドレイン電圧 DRAIN Voltage		VD	700		V				
2	コントロール電圧 CONTROL Voltage		vc	10		V				
3	出力電流 Output Current		ID	1. 72		А				
4	・ 出力ピーク電流 Output Peak Current		IDP	2. 4		А				
5	コントロール電流 CONTROL Current		IC	0.1		А				
6	チャネル部温度 Channel Temperature		Tch	150		°C				
7	保存温度 Storage Temperature		Tstg	-55 ~ +	150	°C				
В. Т	B. 電気的特性 / ELECTRICAL CHARACTERISTICS 測定条件 / Measure condition (Tc=25℃±3℃)									
No.	項目/Item		記号/ Symbol	測定条件/Measure Condition (別紙測定図-1 参照/ See Figure 1)		typ.	Lim Min	it Max	Unit	
【コント										
1	出力周波数 Output Freque	ncy	fOSC	Vc=Vc(CNT)-0.2V		100	90	110	kHz	
2	最大デューティーサ Maximum Duty		MAXDC	Vc=Vc(CNT)-0.2V		69	66	72	%	
* 3	PWMゲイン PWM Gain		GPWM			11			dB	
* 4	スロープ 補償値 Slope Compensate Value		m			60			mA/us	
[1	電源/SUPPLY	:*は設計保証項目/Des	sign Guarar	ntee Item]		•			•	
5	起動前動作電流 Before Auto-restart Current		IC(SB)	VC < VC(ON)	(2)	0.30	0.05	0.6	mA	
6	動作時電流 Operating Cur	rent	IC(OP)	VC=VC(CNT)-0.2V	(2)	1.8	0.7	2.7	mA	
7	起動時コントロール Auto-restart T	端子電圧 hreshold Voltage	VC(ON)	S1=OPEN	(2)	6.0	5.1	6.6	٧	
8	停止時コントロール UV Lockout TI	端子電圧 hreshold Voltage	VC(OFF)	S1=OPEN	(2)	5.0	4.1	5.5	٧	
9	起動/停止ヒステ Auto-restart h	リシス電圧 ysteresis voltage	⊿vc	S1=OPEN	(2)	1.0	0.5	1.5	٧	
10	間欠動作時間は Auto-restart d		TSW/TTIM	S1=OPEN		2			%	

Page 1of 6

Established : 2013-08-01 Revised : ###-##-##

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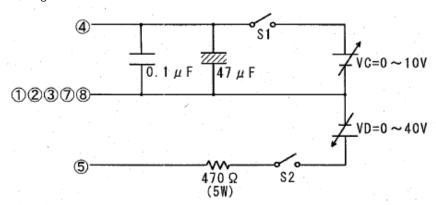
NI -	万 口 (1)	記号/ Symbol	測定条件/Measure Condition	Тур.	Limit		
No.	項目/Item		(別紙測定図-1 参照/ See Figure 1)		Min	Max	Unit
11	間欠動作周波数 Auto-restart frequency	fTIM	S1=OPEN	0.5			Hz
12	コントロール端子充電電流 CONTROL Pin	IC(CHG)	VC=0V	-1.9	-2.5	-1.2	mA
, _	Charging Current		VC=5V	-1.2	-2.0	-0.5	mA
13	コントロール電圧 CONTROL Pin Voltage	VC(CNT)		6.2	5.7	6.6	V
*14	コントロール電圧ヒステリシス CONTROL Pin Voltage hysteresis	∠VC(CNT)		10			mV
15	最小ドレイン電圧 DRAIN Supply Voltage	VD(MIN)			36		V
[1	保護機能/CIRCUIT PROTECTION:*に	は設計保証	項目/Design Guarantee Item】				
16	過電流保護検出 Self-protection Current Limit	ILIMIT		1.5	1.35	1.65	Α
*17	オン時プランキング幅 Leading Edge Blanking Delay	ton(BLK)		0.25			μs
*18	過電流保護遅れ時間 Current Limit Delay	td(OCL)		0.1			μs
*19	過熱保護温度 Thermal Shutdown Temperature	ТОТР		140	130		°C
*20	ラッチリセット電圧 Power-up Reset Threshold Voltage	Vcreset	S2=OPEN	3.3	2.3	4.2	٧
[]	・ 出カ/OUTPUT:*は設計保証項目/Des	sign Guarar	ntee Item]	•	•	•	
21	オン抵抗 On-State Resistance	RDS(ON)	ID=0.3A (See Figure 2)	5.8		6.7	Ω
22	オフ時ト・レイン端子リーク電流 OFF-State Current	IDSS	VDS=650V, Vc=6.5V	10		250	μΑ
23	ト゛レイン耐圧 Breakdown Voltage	VDSS	ID=0.25mA, Vc=6.5V		700		٧
24	立ち上がり時間 Rise time	tr	(1)	0.1			μs
25	立ち下がり時間 Fall time	tf	(1)	0.1			μs
*26	熱抵抗 Thermal resistance (j-a)	Rth (j-a)	ェポ゚キシ基板(3cm×3cm)実装時 Ta=25℃	90			°C/W
			Surface Mounted on Epoxy Bord				

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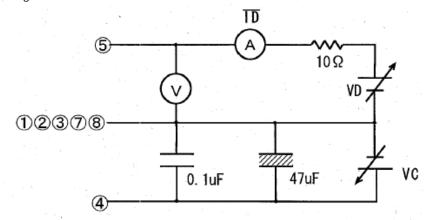
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■測定回路図-1/Figure 1



*本測定回路は、過電流保護検出値、出力特性の測定には使用できません。 This measurement circuit can't be useful for peak current and output characteristic measurement.

■測定回路図-2/Figure 2



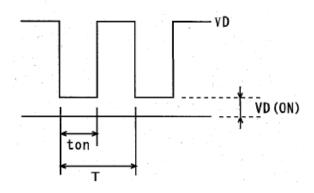
$$R \circ n = \frac{V D (0N)}{I D} \times \frac{t \circ n}{T}$$

端子説明/Terminal explanation

4: Control

 $\underbrace{12378}:\mathsf{Source}$

(5): Drain

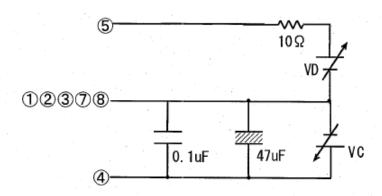


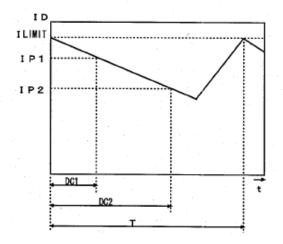
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■測定回路図-3/Figure 3





$$|L|M|T = \frac{DC2 \cdot IP1 - DC1 \cdot IP2}{DC2 - DC1}$$

端子説明/Terminal explanation

4: Control

 $\textcircled{1} \textcircled{2} \textcircled{3} \textcircled{7} \textcircled{8} : \mathsf{Source}$

⑤: Drain

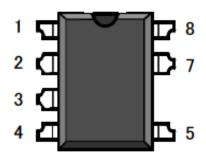
* DC1, DC2は、VDをそれぞれVD1, VD2にしたときのデューティーサイクル(0<DC1<DC2<MAXDC)

/ DC1,DC2 is duty cycle when VD is VD1 and VD2,respectively.

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■ピン配置図/Pin alignment

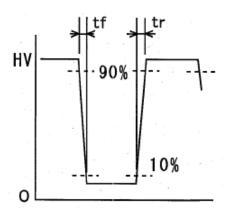


Pin No.	Pin name	
1	Source	
2	Source	
3	Source	
4	Control	
5	Drain	
6	-	
7	Source	
8	Source	

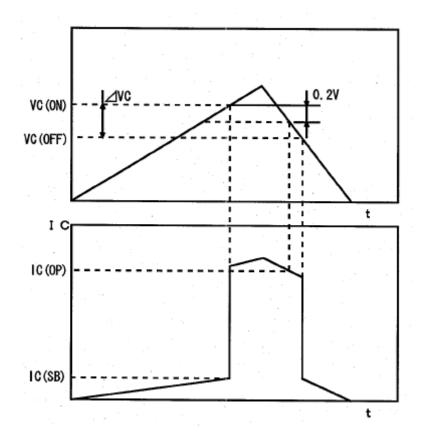
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NOTE:(1)



(2)



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