Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

Doc No. TD4-EA-01883 Revision. 1

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MIP2F10MTSCF

種別。	∕Type	シリコン Me	OS形集積回路/Silid	con MOSFET type Integrated Circuit					
用途/Application スイッチング電源制御		電源制御用/For Sv	即用/For Switching Power Supply Control						
構造	/Structure	CMOS形/	CMOS type						
等価	 回路/Equivalent	t Circuit	添付図/See Figu	ure. 6					
	✓Out Line	DIP7 — A		マーク記号/マーキング・/ Marking			MIP2F1		
71.1127	out Line			The Government of the Market	III'8	101	IVIIPZF I		
A. 絶	的 対最大定格/A	BSOLUTE MAX	KIMUM RATINGS (T	a=25℃±3℃)					
NO.	項目/Item		記号/ Symbol	定格/Ratings	単位/ Unit 備考/N		ote		
1	ドレイン電圧 DRAIN Volta		VD	−0.3 ~ 700	V	※1: 下記パルス幅以内で			
2	VCC電圧 VCC Voltage)	vcc	−0.3 ~ 45	V	-	d within		
3	VDD電圧 VDD Voltage フィードバック		VDD	−0.3 ~ 8	V	the pulse as below.) オン時ブランキング幅 +過電流保護遅れ時間 Leading Edge Blanking			
4	FEEDBACK フィードバック	Voltage	VFB	-0.3 ~ 8	V				
5	FEEDBACK		IFB	500	μΑ				
6	CL端子電圧 CL Voltage		VCL	−0.3 ~ 8	V	Delay	•		
7	CL端子電流 CL Current		ICL	150	μΑ	nA			
8	出力ピーク電 Output Peak	Current	IDP	450(※1)	mA				
9	チャネル部温/ Channel Temp		Tch	150	°C				
10	保存温度 Storage Temp	erature	Tstg	−55 ~ +150	°C				
B. 1	気的特性/ELE	CTRICAL CHA	RACTERISTICS	測定条件/Measure cond	ition (TC=	25°C±2	°C)		
No.	項目/Item		記号/ Symbol	測定条件/Measure Condition (測定図-1 参照/See Figure 1)	Тур.	Lir Min	nit Max	Unit	
【コント	_ ·ロール機能/C(ONTROL FUNC	TIONS	ı	1		ı	ı	
1	出力周波数		fosc	VCC=15 V, VD=5 V, IFB=20 μA, ICL=50 μA	100	90	110	kHz	
	Output Frequen		fosc(L)	VCC=15 V, VD=5 V, IFB:OPEN, ICL <icl1< td=""><td>12</td><td>9</td><td>15</td><td>kHz</td></icl1<>	12	9	15	kHz	
2	最大デューティ Maximum Duty	Cycle	MAXDC	VCC=15 V, VD=5 V, IFB=20 μA, ICL=50 μA	47.5	45	50	%	
3	VDD基準電圧 VDD Voltage		VDD	VCC=15 V, VD=5 V, IFB=20 μ A, ICL=50 μ A	5.9	5.4	6.4	V	
4	VDD停止電日 UV Lockout Thi		yuv	VD=5 V, IFB=20 μA, ICL=50 μA	5.1	4.6	5.6	V	

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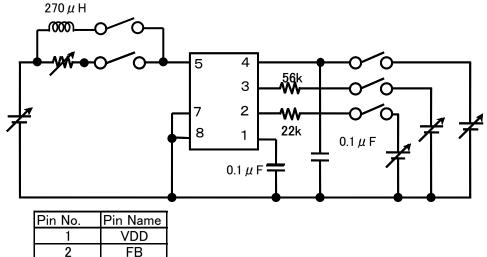
	-T.D. (1)	記号/	測定条件/Measure Condition		Limit		11.2
No.	項目/Item	Symbol	(測定図−1 参照/See Figure 1)	Тур.	Min	Max	Unit
【コント	ロール機能/CONTROL FUNCTIONS】						
_	VCC起動電圧						
5	VCC Start Voltage	VCC(ON)	VD=5 V, IFB=20	7.5	6.5	8.5	V
	VCC充電停止電圧						
6	VCC Charge Stop Threshold Voltage	VCC1	VD=40 V, FB:OPEN, CL:OPEN	12	11	13	V
	フィードバック電流		$ON \to OFF$				
7	Feedback Threshold Current	IFB1	VCC=15 V, VD=5 V, ICL=50 μA	48	25	70	μΑ
	フィードバック電流ヒステリシス						_
8	Feedback Hysteresis Current	IFBHYS	VCC=15 V, VD=5 V, ICL=50 μA	4			μΑ
9	重負荷時FB端子電流 FB Pin Current at Heavy Load	IFB0	ICCO → ICC	11	7	15	<i>μ</i> Λ
9		ILDO	VCC=15 V, VD=5 V, ICL=50 μA	11	7	15	μΑ
10	FB端子電圧 FB Pin Voltage	VFB	V00-15 V VD-5 V IEB-20 ((A 10)-50 ((A	1.0	0.7	1.3	V
	回路消費電流		VCC=15 V, VD=5 V, IFB=20 μ A, ICL=50 μ A	1.0	0.7	1.0	•
11	Supply Current	ICC	VCC=15 V, VD=5 V, IFB=20 μA, ICL=50 μA	0.32	0.22	0.42	mA
	軽負荷時回路消費電流		VCC=15 V, VD=5 V				
12	Supply Current at Light Load	ICC(OFF)	IFB=IFB1+5 μA, ICL=50 μA	0.30	0.215		mA
	重負荷時回路消費電流						
13	Supply Current at Heavy Load	ICC0	VCC=15 V, VD=5 V, IFB=OPEN, ICL=50 μ A	0.50	0.37	0.63	mA
	VDD充電電流	Ich1	VDD=0 V, VD=40 V, FB:OPEN, CL:OPEN	3.6	1.2	6.0	mA
14	VDD Charging Current		VBB-0 V, VB-40 V, I B.OI EN, OE.OI EN				
		Ich2	VDD=4 V, VD=40 V, FB:OPEN, CL:OPEN	1.1	0.3	1.9	mA
15	CL端子電圧 CL Pin Voltage	VCL	V00 45 V V0 5 V 50 005 V 10 45 V 4	2.3	2.0	2.6	V
10	fosc 低下時CL端子電流		VCC=15 V, VD=5 V, FB:OPEN, ICL=15 μ A fosc → fosc(L)	2.0	2.0	2.0	V
16	Dropped fosc CL Pin Current	ICL1	VCC=15 V, VD=5 V, FB:OPEN	11	8	14	μΑ
	fosc 低下時CL端子電流ヒステリシ		*Figure 3		-		,
17	CL Pin Hysteresis Current	ICLHYS	VCC=15 V, VD=5 V, FB:OPEN	1.0			μΑ
【保護機能/CIRCUIT PROTECTIONS:*は設計保証項目/Design Guarantee Item】							
	過電流保護検出		Figure 2/Figure 4				
18	Self Protection Current Limit	ILIMIT	VCC=15 V, FB:OPEN, ICL=50 μ A,DUTY=30%	0.25	0.225	0.275	Α
	ILIMIT 補正係数		*Figure 2/Figure 4				
19	ILIMIT modified coefficient	R_slope	VCC=15 V, FB:OPEN, ICL=50 μA	20			mA/ μ s
	最小ILIMIT		Ton=3 μ sec				
20	Minimum ILIMIT	ILIMITmi	VCC=15 V, FB:OPEN, ICL=0 μA	50	15	85	mA
*	軽負荷時ドレイン電流	/	Ton=3 μ sec			0-	
21	Drain Current at Light Load	ID(OFF)	VCC=15 V, IFB=IFB1-IFBHYS, ICL=50 μA	50	15	85	mA
* 22	オン時ブランキング幅	+ (DL 1/		240	170	310	n-
*	Leading Edge Blanking Delay 過電流保護遅れ時間	ton(BLK	VCC=15 V, FB:OPEN, ICL=50 μA	240	170	310	ns
23	四电流体度遅れ時间 Current Limit Delay	td(OCL)		150	100	200	ns
	過電圧保護検出	(. 30		
24	Over Voltage Protection	VCC(OV	VD=5 V, FB:OPEN, ICL=50 μA	24	21	27	V
*	過熱保護温度	, ,					
	Thermal Shutdown Temperature	TOTP	İ	140	130	150	°C

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N	西 日 (1)	記号/	測定条件/Measure Condition (測定図-1 参照/See Figure 1)	Тур.	Limit		11.5	
No.	項目/Item	Symbol			Min	Max	- Unit	
【出力》	【出力/OUTPUT】							
26	ラッチリセット電圧 Power-up Reset Threshold Voltage	VDDreset		2.6	1.8	3.5	V	
27	オン抵抗 27 ON-State Resistance		ID=50 mA	31		36	Ω	
28	オフ時ドレイン端子リーク電流 OFF-State Current	IDSS	VCC=27 V, VD=650 V, FB:OPEN, CL:OPEN	10		20	μΑ	
29	ドレイン耐圧 Breakdown Voltage	VDSS	VCC=27 V, ID=100 μ A, FB:OPEN, CL:OPEN		700		V	
30	立ち上がり時間 Rise Time	tr		100			ns	
31	立ち下がり時間 Fall Time	tf		50			ns	
【電源	【電源電圧/SUPPLY】							
32	最小ドレイン電圧 Drain Supply Voltage	VD(MIN)	VCC: OPEN, FB:OPEN, CL:OPEN	_	50		V	

【Fig. 1:測定回路図/Measure Circuit】

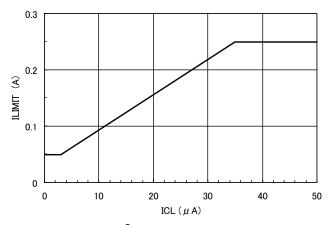


1	VDD			
2	FB			
3	CL			
4	VCC			
5	DRAIN			
6	_			
7	SOURCE			
8	SOURCE			

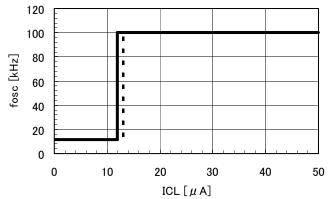
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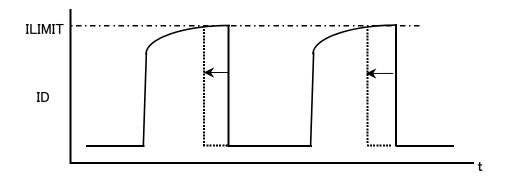
[Fig. 2:ILIMIT vs. ICL Typical Characteristic]



[Fig. 3:fosc vs. ICL Typical Characteristic]



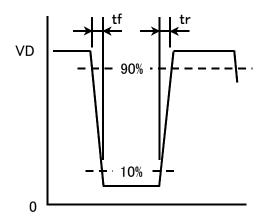
[Fig. 4: ILIMIT Measurement]



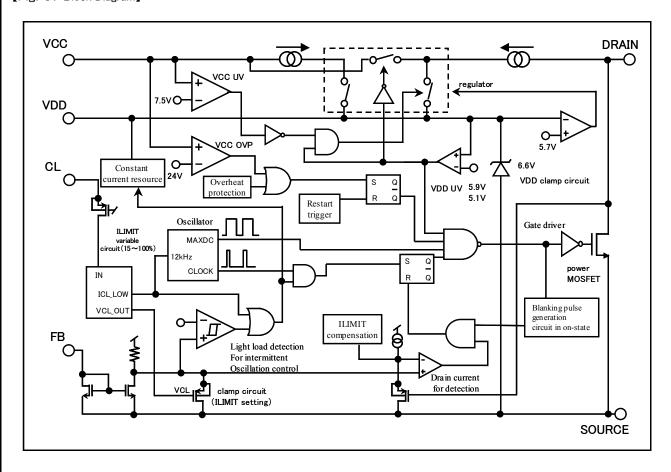
 $R_slope \; ; \; \{ (ILIMIT \; at \; Duty=30\%) \; - \quad (ILIMIT \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty$

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[Fig. 5 : tr、tf Measurement]



[Fig. 6: Block Diagram]



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【使用上の注意1/Precautions for Use 1】

VDD 端子-GND間には、 0.1μ Fのセラミックコンデンサを使用してください。 Connect a 0.1μ F ceramic capacitor between VDD pin and GND.

【使用上の注意2/Precautions for Use 2】

以下のような条件では破損し、場合によっては破裂、発煙の可能性があります。以下の使用は避けてください。
The IPD has risks for break-down or burst or giving off smoke in following conditions. Avoid the following use.

- (1) DRAIN 端子と VDD 端子を逆にして、電源基板へ挿入する。 Reverse the DRAIN pin and VDD pin connection to the power supply board.
- (2) DRAIN 端子と VDD 端子をショートする。 DRAIN pin short to VDD pin.
- (3) DRIN端子とFB 端子をショートする。 DRAIN pin short to FB pin.
- (4) DRIN端子とCL端子をショートする。 DRAIN pin short to CL pin.
- (5) DRIN端子と VCC 端子をショートする。 DRAIN pin short to VCC pin.
- (6) VCC 端子と VDD 端子をショートする。 VCC pin short to VDD pin.
- (7) VCC 端子と CL 端子をショートする。 VCC pin short to CL pin.
- (8) VCC 端子と FB 端子をショートする。 VCC pin short to FB pin.

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 Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
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