Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

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MIP2F20MTSCF

種別。	∕Type	シリコン MG	OS形集積回路/Silid	con MOSFET type Integrated Circuit						
用途	/Application	スイッチング	˙電源制御用╱For Sぃ	witching Power Supply Control						
構造。	∕Structure	CMOS形/	CMOS type							
等価[回路/Equivalen	t Circuit	添付図/See Figu	ure. 6						
外形。	外形/Out Line DIP7-A1-B			マーク記号/マーキング・/Marking			MIP2F2			
Δ総	対最大定格 / Δ	RSOLLITE MAX	(IMUM RATINGS (T	a=25°C+3°C)						
NO.			記号/ Symbol	定格/Ratings	単位/ Unit		備考/Note			
1	ドレイン電圧 DRAIN Voltag	e	VD	−0.3 ~ 700	V	※1: 下記パルス幅以内での				
2	VCC電圧 VCC Voltage		vcc	−0. 3 ~ 45	V	保証と (It is g	d within			
3	VDD電圧 VDD Voltage		VDD	−0.3 ~ 8	V	the	below.)			
4	フィードバック FEEDBACK V	_	VFB	−0.3 ~ 8	V	オン時ブランキング幅 +過電流保護遅れ時間 Leading Edge Blankir Pulse + Current Limit Delay ton(BLK)+td(OCL)				
5	フィードバック FEEDBACK C		IFB	500	uA					
6	CL端子電圧 CL Voltage		VCL	-0.3 ~ 8	V					
7	CL端子電流 CL Current		ICL	150	uA					
8	出力ピーク電 Output Peak		IDP	650(※1)	mA					
9	チャネル部温	度			°C					
	Channel Temp 保存温度		Tch	150	င					
10	Storage Temp	erature	Tstg	−55 ~ +150						
B. 電	気的特性/ELE	CTRICAL CHA	RACTERISTICS	測定条件/Measure condition (TC=25℃±2℃)						
No.	項目/Item		記号/	測定条件/Measure Condition	Тур.	Limit		Unit		
			Symbol	(測定図−1 参照/See Figure 1)	,,,,,	Min	Max	3,		
トコント		DIVINOL FUNC			,					
1	出力周波数 Output Frequency		fosc(L)	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA VCC=15 V, VD=5 V, IFB:OPEN, ICL <icl1< td=""><td>100</td><td>90</td><td>110 15</td><td>kHz kHz</td></icl1<>	100	90	110 15	kHz kHz		
2	最大デューティサイクル Maximum Duty Cycle		MAXDC	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	47.5	45	50	%		
3	VDD基準電圧 VDD Voltage		VDD	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	5.9	5.4	6.4	V		
4	VDD停止電圧 UV Lockout Th	reshold Voltage		VD=5 V, IFB=20 uA, ICL=50 uA	5.1	4.6	5.6	V		

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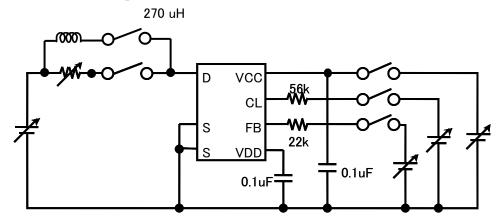
	#F #	記号/	測定条件/Measure Condition		Limit		11.1		
No.	項目/Item	Symbol	(測定図−1 参照/See Figure 1)	Тур.	Min	Max	Unit		
【コントロール機能/CONTROL FUNCTIONS】									
	VCC起動電圧								
5	VCC Start Voltage	VCC(ON)	VD=5 V, IFB=20 uA, ICL=50 uA	7.5	6.5	8.5	V		
	VCC充電停止電圧								
6	VCC Charge Stop Threshold Voltage	VCC1	VD=40 V, FB:OPEN, CL:OPEN	12	11	13	V		
	フィードバック電流		ON → OFF						
7	Feedback Threshold Current	IFB1	VCC=15 V, VD=5 V, ICL=50 uA	45	25	65	uA		
	フィードバック電流ヒステリシス								
8	Feedback Hysteresis Current	IFBHYS	VCC=15 V, VD=5 V, ICL=50 uA	2			uA		
	重負荷時FB端子電流		ICC0 → ICC						
9	FB Pin Current at Heavy Load	IFB0	VCC=15 V, VD=5 V, ICL=50 uA	11	7	15	uA		
	FB端子電圧								
10	FB Pin Voltage	VFB	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	1.0	0.7	1.3	V		
	回路消費電流								
11	Supply Current	ICC	VCC=15 V, VD=5 V, IFB=20 uA, ICL=50 uA	0.35	0.25	0.45	mA		
	軽負荷時回路消費電流		VCC=15 V, VD=5 V						
12	Supply Current at Light Load	ICC(OFF)	IFB=IFB1+5 uA, ICL=50 uA	0.25	0.18	0.32	mA		
	重負荷時回路消費電流								
13	Supply Current at Heavy Load	ICC0	VCC=15 V, VD=5 V, IFB=OPEN, ICL=50 uA	0.54	0.4	0.68	mA		
	VDD充電電流	Ich1	VDD=0 V, VD=40 V, FB:OPEN, CL:OPEN	3	1	4.6	mA		
14	VDD Charging Current			4.0		4.7			
		Ich2	VDD=4 V, VD=40 V, FB:OPEN, CL:OPEN	1.0	0.3	1.7	mA		
15	CL端子電圧	\/OI		0.0	0.0	0.0			
15	CL Pin Voltage	VCL	VCC=15 V, VD=5 V, FB:OPEN, ICL=15 uA	2.3	2.0	2.6	V		
16	fosc 低下時CL端子電流 Dropped fosc CL Pin Current	ICL1	fosc → fosc(L)	11	8	14	uA		
10	fosc 低下時CL端子電流ヒステリシス	IOLI	VCC=15 V, VD=5 V, FB:OPEN	- ''	0	14	uA		
17		ICLHYS	※Figure 3	1.0			Δ		
	7 YOUTO Y, YE O Y, TE OF EN								
【保護	機能/CIRCUIT PROTECTIONS:*は設言	†保証項目/	Design Guarantee Item】						
	過電流保護検出		≫Figure 2/Figure 4						
18	Self Protection Current Limit	ILIMIT	VCC=15 V, FB:OPEN, ICL=50 uA,DUTY=30%	0.35	0.315	0.385	Α		
	ILIMIT 補正係数		≫Figure 2/Figure 4				. ,		
19	ILIMIT modified coefficient	R_slope	VCC=15 V, FB:OPEN, ICL=50 uA	28			mA/us		
	最小ILIMIT		Ton=3 usec			100			
20	Minimum ILIMIT	ILIMITmin	VCC=15 V, FB:OPEN, ICL=0 uA	55	20	100	mA		
*	軽負荷時ドレイン電流	\	Ton=3 usec	00	00	150			
21	Drain Current at Light Load	ID(OFF)	VCC=15 V, IFB=IFB1+IFBHYS, ICL=50 uA	90	30	150	mA		
*	オン時ブランキング幅	(D) (C)		240	170	210	NJ-		
22	Leading Edge Blanking Delay	ton(BLK)	VCC=15 V, FB:OPEN, ICL=50 uA	240	170	310	Ns		
*	過電流保護遅れ時間	+4(001)		150	100	200	NJ-		
23	Current Limit Delay	td(OCL)		150	100	200	Ns		
24	過電圧保護検出 Over Voltage Protection	V(CC(C)\/)	VOD 5 V ED ODEN 101 -5	24	21	27	V		
*	Over Voltage Protection 過熱保護温度	VCC(OV)	VDD=5 V, FB:OPEN, ICL=50 uA	24	<u> </u>	21	_ v		
* 25	週款休護温度 Thermal Shutdown Temperature	ТОТР		140	130	150	°C		
20	25 Thermal Shutdown Temperature 1			170	100	130	U		

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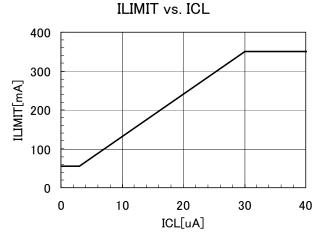
N		記号/ Symbol	測定条件/Measure Condition (測定図-1 参照/See Figure 1)	Тур.	Limit		11.5
No.	項目/Item				Min	Max	Unit
【出力	∕OUTPUT]						
	ラッチリセット電圧						
26	Power-up Reset Threshold Voltage	VDDreset		2.6	1.8	3.5	V
	オン抵抗						
27	ON-State Resistance	RDS(ON)	ID=50 mA	20		27	Ω
	オフ時ドレイン端子リーク電流						
28	OFF-State Current	IDSS	VCC=27 V, VD=650 V, FB:OPEN, CL:OPEN	10		20	uА
	ドレイン耐圧						
29	Breakdown Voltage	VDSS	VCC=27 V, ID=100 uA, FB:OPEN, CL:OPEN		700		V
	立ち上がり時間		≫Figure 5				
30	Rise Time	tr	VCC=15 V, VD=5 V, FB:OPEN, ICL=50 uA	100			Ns
	立ち下がり時間		≫Figure 5				
31	Fall Time	tf	VCC=15 V, VD=5 V, FB:OPEN, ICL=50 uA	50			Ns
【電源	電圧/SUPPLY】						
	最小ドレイン電圧						
32	Drain Supply Voltage	VD(MIN)	VCC: OPEN, FB:OPEN, CL:OPEN		50		V

【Fig. 1:測定回路図/Measure Circuit】

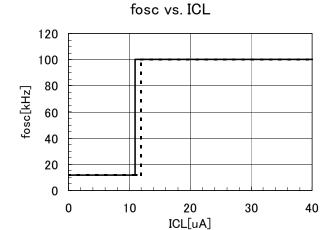


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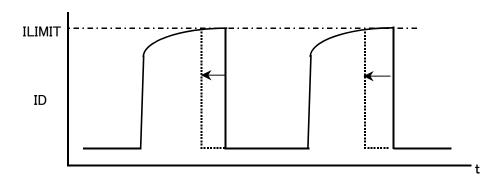




[Fig. 3:fosc vs. ICL Typical Characteristic]



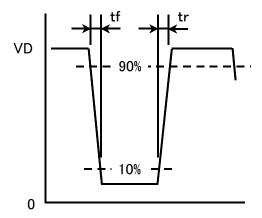
[Fig. 4: ILIMIT Measurement]



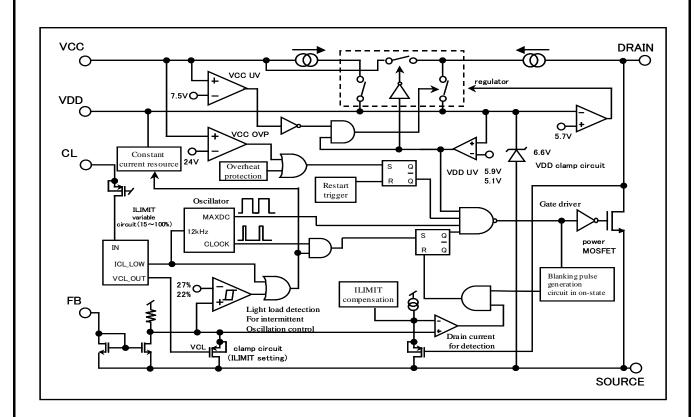
 $R_slope \; ; \; \{ (ILIMIT \; at \; Duty=30\%) \; - \quad (ILIMIT \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \} \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; - \quad (Ton \; at \; Duty=10\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=30\%) \; / \; \{ (Ton \; at \; Duty=3$

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[Fig. 5 : tr, tf Measurement]



[Fig. 6: Block Diagram]



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【使用上の注意1/Precautions for Use 1】

VDD 端子-GND間には、O. 1uFのセラミックコンデンサを使用してください。 Connect a 0.1uF ceramic capacitor between VDD pin and GND.

【使用上の注意2/Precautions for Use 2】

以下のような条件では破損し、場合によっては破裂、発煙の可能性があります。以下の使用は避けてください。 The IPD has risks for break-down or burst or giving off smoke in following conditions. Avoid the following use.

- (1) DRAIN 端子と VDD 端子を逆にして、電源基板へ挿入する。 Reverse the DRAIN pin and VDD pin connection to the power supply board.
- (2) DRAIN 端子と VDD 端子をショートする。 DRAIN pin short to VDD pin.
- (3) DRIN端子と FB 端子をショートする。 DRAIN pin short to FB pin.
- (4) DRIN端子とCL端子をショートする。 DRAIN pin short to CL pin.
- (5) DRIN端子と VCC 端子をショートする。 DRAIN pin short to VCC pin.
- (6) VCC 端子と VDD 端子をショートする。 VCC pin short to VDD pin.
- (7) VCC 端子と CL 端子をショートする。 VCC pin short to CL pin.
- (8) VCC 端子と FB 端子をショートする。 VCC pin short to FB pin.

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 Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. We do not guarantee quality for disassembled products or the product re-mounted after removing from the mounting board. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
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