Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

Doc No. TD4-EA-01870 Revision. 1

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MIP3E30MTSCF

種別	∕Type	シリコンMOS	形集積回路	∕Silicon MOSFET type Integrate	d Circui	t			
		スイッチング・電源制御	御用/For a Switching Power Supply Control						
構造	∕Structure	CMOS形/CMC	OS Type						
外形/Out line DIP7-A		1-B				IP3E3			
Α.	絶対最大定格/	Absolute Maximu	ım Ratings						
No.	項目/Item		記号 /Symbol	定格/Ratings			単 位 ∕Unit		
1	ドレイン電圧 DRAIN Voltage		V D	7 0 0			٧		
2	コントロール電圧 CONTROL Voltage		VС	8			V		
3	出力電流 Output Current		I D	0.88			А		
4	出力ピーク電流 Output Peak Current		IDP	1. 7			А		
5	コントロール電流 CONTROL Current		I C	0. 1			А		
6	チャネル部温度		Tch	150			°C		
	7 保存温度 Storage Temperature			-55 ~ +150			°C		
7		rature	Tstg	-55 ~ +150			°C		
	Storage Tempe	rature ectrical charac		-55 ~ +150 測定条件/Measure condition	(Tc=25°C	±3°C)			
	Storage Tempe 電気的特性/EI				(Tc=25°C	±3°C) Lim Min)	Unit	
В.	Storage Tempe 電気的特性/EI 項目	ectrical charac	記号 /Symbol	測定条件/Measure condition 測定条件/Measure Condition	Тур	Lim) iit	Unit	
В.	Storage Tempe 電気的特性/EI 項目	ectrical charac /Item 機能/Control fu	記号 /Symbol	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1)	Тур	Lim) iit	Unit	
В.	Storage Tempe 電気的特性/EI 項目 【コントロール 出力周波数	ectrical charac /Item 機能/Control fo	記号 /Symbol	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee	Typ Item]	Lim Min	it Max		
В.	Storage Tempe 電気的特性/EI 項目 【コントロール 出力周波数 Output Frequer 最大デューティーサイク	ectrical charac /Item 機能/Control fo	記号 /Symbol unctions:	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V,VD=5V	Typ Item] 100	Lim Min 90	it Max	kHz	
B. No.	Rまでは、 Tempe 電気的特性/El 項目 コントロール 出力周波数 Output Frequer 最大デューティーサイク Maximum Duty (PWM Gain	ectrical charac /Item 機能/Control fo ncy bu Cycle	記号 /Symbol unctions: fosc MAXDC	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)-0.2V, VD=5V	Typ Item] 100 47.5	Lim Min 90 45	Max 110 50	kHz % dB	
B. No.	Rまでは、 Tempe 電気的特性/El 項目 コントロール 出力周波数 Output Frequer 最大デューティーサイク Maximum Duty (PWM Gain	ectrical charac /Item 機能/Control funcy かん Cycle :*は設計保証項目	記号 /Symbol unctions: fosc MAXDC	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)-0.2V, VD=5V	Typ Item] 100 47.5	Lim Min 90 45	Max 110 50	kHz % dB	
B. No. 1 2 *3	ままりでは 電気的特性/El 項目 【コントロール 出力周波数 Output Frequer 最大デューティーサイク Maximum Duty (PWMゲイン PWM Gain 【電源/Supply 起動前動作電流	ectrical charace /Item 機能/Control for charace で 機能/Control for charace で は で は で で で で で で で で で で で で で で で	記号 /Symbol unctions: fosc MAXDC GPWM	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)	Typ Item] 100 47.5 11 e (1) Sh	Lim Min 90 45	110 50 0.4/6	kHz % dB	
B. No. 1 2 *3	電気的特性/El 項目 【コントロール 出力周波数 Output Frequer 最大デューティーサイク Maximum Duty (PWMが ? ひ PWM Gain 【電源/Supply 起動前動作電流 Before Auto-re 軽負荷停止時電	ectrical charace /Item 機能/Control form Cycle : *は設計保証項目 Eestart Current 流流 te Current	記号 /Symbol unctions: fosc MAXDC GPWM 目/Design (測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT) Guarantee Item】【(1)refer to Not VC <vc(on), (1)<="" td="" vd="5V"><td>Typ Item] 100 47.5 11 e (1) Si 0.4</td><td>90 45 neet N</td><td>110 50 0. 4/6)</td><td>kHz % dB</td></vc(on),>	Typ Item] 100 47.5 11 e (1) Si 0.4	90 45 neet N	110 50 0. 4/6)	kHz % dB	
B. No. 1 2 *3 4 5	電気的特性/EI 項目 【コントロール 出力周波数 Output Frequer 最大デューティーサイク Maximum Duty (PWMが か PWM Gain 【電源/Supply 起動前動作電流 Before Auto-re 軽負荷停止時電 After Off-stat 動作時電流	ectrical charace /Item 機能/Control for the control c	記号 Symbol Unctions: fosc MAXDC GPWM IC(SB)1 IC(SB)2 IC(OP)	測定条件/Measure condition 測定条件/Measure Condition (別紙測定図-1参照/See Figure 1) *は設計保証項目/Design Guarantee Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT)-0.2V, VD=5V Vc=Vc(CNT) Guarantee Item】【(1)refer to Not VC <vc(on), (1)<="" td="" vd="5V"><td>Typ Item] 100 47.5 11 e (1) Si 0.4 0.5</td><td>Min 90 45 0.2 0.2</td><td>110 50 0. 4/6) 0. 7 0. 8</td><td>kHz % dB mA mA</td></vc(on),>	Typ Item] 100 47.5 11 e (1) Si 0.4 0.5	Min 90 45 0.2 0.2	110 50 0. 4/6) 0. 7 0. 8	kHz % dB mA mA	

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9	起動/停止ヒステリシス電圧 Auto-restar hysteresis Voltage	⊿VC	VD=5V (1)	1. 3	0.8	1.8	٧
10	コントロール端子クランプ。電圧 Control Clump Voltage	VC (CLP)	IC=1mA, VD=5V	6. 2	5. 7	6. 7	٧
11	間欠動作時間比 Auto-restart duty cycle	TSW/TTIM	S1=0PEN	4			%
12	間欠動作周波数 Auto-restart frequency	fTIM	S1=0PEN	3			Hz
13	コントロール端子充電電流 CONTROL Pin Charging Current	IC (CHG)	VC=0V, VD=40V VC=5V, VD=40V	−2. 5 −1. 0	-3. 6 -2. 0	-1. 0 -0. 4	mA mA
14	コントロール電圧 CONTROL Pin Voltage	VC (CNT)	VD=5V (1)	5. 8	5. 4	6. 3	٧
*15	コントロール電圧とステリシス CONTROL Pin Voltage hysteresis	∠ VC (CNT)	VD=5V	10			mV
16	最小ドレイン電圧 DRAIN Supply Voltage	VD (MIN)	S1=0PEN		36		٧
	【保護機能/Circuit protection	า:*は設計	保証項目/Design Guarantee Item】				
17	過電流保護検出 Self-protection Current Limit	ILIMIT	See Figure 2, S1=OPEN	0.8	0. 72	0. 88	Α
*18	オン時ブランキング 幅 Leading Edge Blanking Delay	ton (BLK)		300	220	380	n s
*19	過電流保護遅れ時間 Current Limit Delay	td(OCL)		100	50	150	n s
*20	過熱保護温度 Thermal Shutdown Temperature	ТОТР		140	130	150	°C
*21	ラッチリセット電圧 Power-up Reset Threshold Voltage	Vcreset		3. 3	2. 2	3.8	٧
	【出力/Output:*は設計保証項	目/Design	Guarantee Item】 【(2)refer to Note	(2) S	heet N	lo. 4/6]
22	か抵抗 On-State Resistance	RDS (ON)	ID=0. 2A	8. 0		10	Ω
23	オフ時ドレイン端子リーク電流 OFF-State Current	IDSS	VDS=650V, Vc=6.5V	10		250	uA
24	ト゛レイン耐圧 Breakdown Voltage	VDSS	ID=1mA, Vc=6.5V		700		٧
25	立ち上がり時間 Rise tim	tr	VC=VC (CNT) -0. 2V, VD=5V (2)	200			ns
26	立ち下がり時間 Fall time	tf	VC=VC (CNT) -0. 2V, VD=5V (2)	50			ns
*27	熱抵抗 Thermal resistance (j-a)	Rth (J-a)	Iポキシ基板(3cm×3cm)実装時 Ta=25℃ Surface Mounted on Energy Pard	90			°C/W
<u> </u>			Surface Mounted on Epoxy Bord				

【使用上の注意1/Precautions for Use 1】

Control端子-Source端子間には、0.1uFのセラミックコンデンサを使

用してください。

Connect a 0.1uF ceramic capacitor between Control pin and Source pin.

【使用上の注意2/Precautions for Use 2】

以下のような条件では破損し、場合によっては破裂、発煙の可能性がありま

す。以下の使用は避けてください。

The IPD is possibility of break-down or burst or giving off smoke as follows. Avoid the following use.

DRAIN端子とControl端子をショートする。

DRAIN pin short to Control pin.

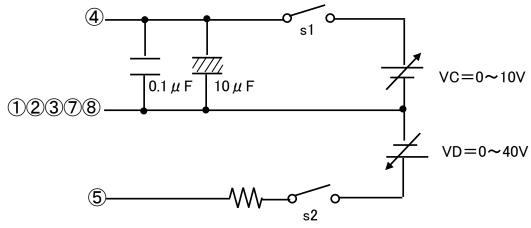
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【Figure 1:測定回路図/Measure Circuit】

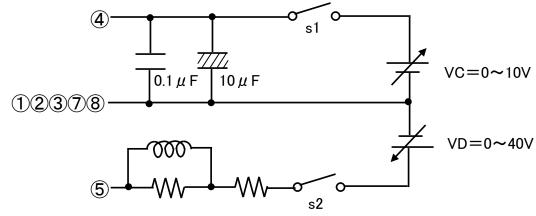


*本測定回路図は、過電流保護検出値の測定には使用できません。
This measurement circuit can't be useful for ILIMIT measurment

端子説明/Terminal explanation

4 : Control (1)2(3)7(8) : Source (5) : Drain

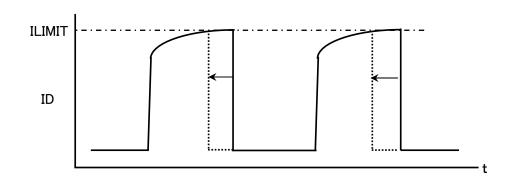
【Figure 2:測定回路図/Measure Circuit】



端子説明/Terminal explanation

4 : Control 12378 : Source

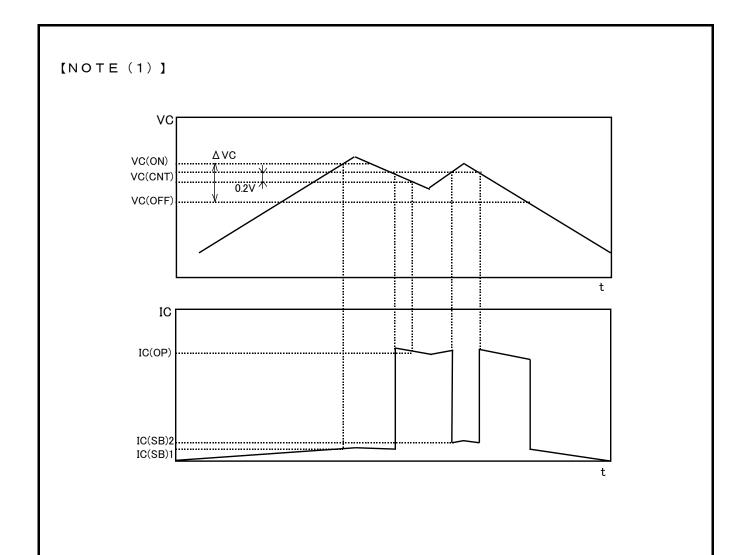
⑤: Drain



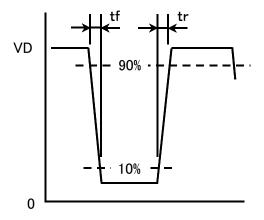
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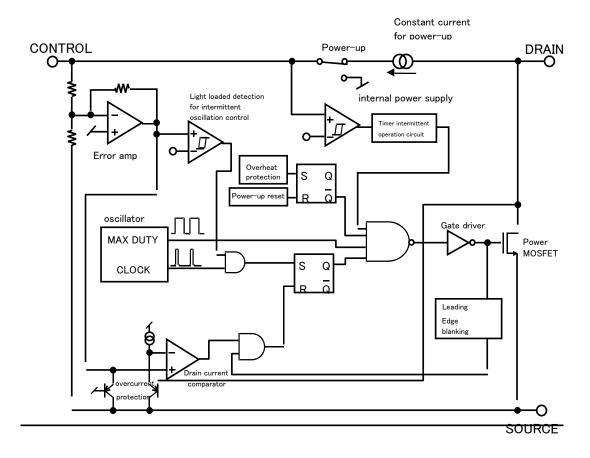
[NOTE (2)]



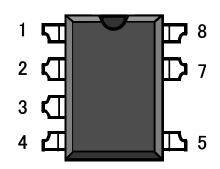
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■ブロック図/Block Figure



■ピン配置図/Pin alignment



Pin No.	Pin name	
1	Source	
2	Source	
3	Source	
4	Control	
5	Drain	
6	_	
7	Source	
8	Source	

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