Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

* Except below description page

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Nuvoton Technology Corporation Japan

MN103SB9 Series

Туре	MN103SB9N	MN103SFB9R			
Internal ROM type	Mask ROM	FLASH			
ROM (byte)	512K	1024K			
RAM (byte)	32K				
Package (Lead-free)	TQFP128-P-1414A				
Minimum Instruction Execution Time	16.7 ns (at 2.7 V to 3.6 V, 60 MHz)				

Interrupts

RESET. IRQ × 9. NMI. Timer × 28. I²C × 3. SIF × 10. DMA × 12. WDT. A/D. Time base timer × 2. System error

Timer Counter

8-bit timer $\times 10$

Reload-down count. Cascade connection possible (usable as a 16-bit to 32-bit timer) 16-bit timer × 6 Up-down count. Input capture. PWM output. Compare/capture register 2 channnels Time base timer × 1 Watchdog timer × 1

■ Serial interface

UART/Synchronous/Multi-master I²C interface selective \times 3 UART/Synchronous interface selective \times 2

DMA controller

Number of channels: 4 channels Unit of transfer: 8/16/32 bits Maximum transfer cycles: 65535 Starting factor: External interrupt. Timer. Serial transmission/reception. A/D conversion finish. I²C transmission/reception. External transmission request. Software Transfer method: 2-bus cycle transfer Adressing modes: Fixed. Increment. Decrement Transfer mode: Word transfer. Burst transfer. Intermittent transfer

Extended Calculation

Multiply and accumulate arithmetic. Multiplication. Saturated arithmetic

■ I/O Pins

I/O 104 : Common use

■ A/D converter

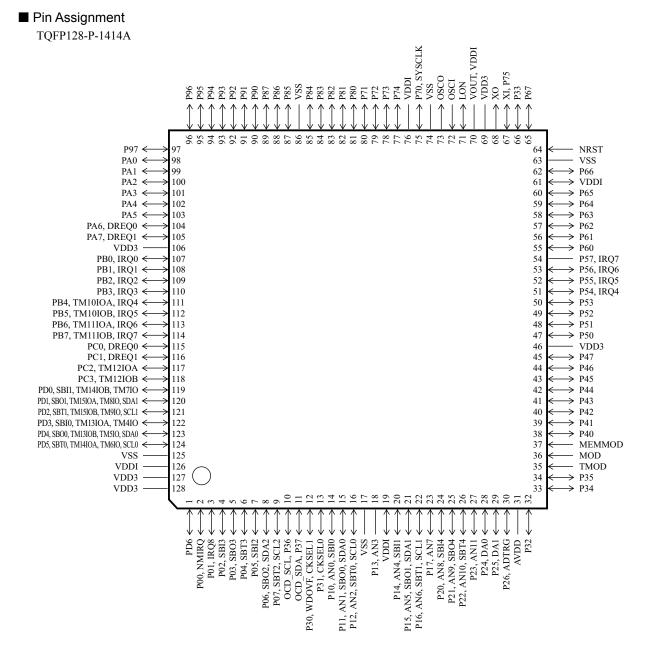
10-bit \times 12 channels

ROM Correction

8 channels

Electrical Charactreistics (A/D converter characteristics)

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	Unit
Resolution					10	Bits
Non-linear error		- AVDD = 3.3 V. VSS = 0 V			±4	LSB
Differential non-linearity error					±4	LSB



MAF00023DEM

Panasonic

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- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation, Nuvoton Technology Corporation Japan or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information de-scribed in this book.
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