Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

MOS FET

SK8603160L

Panasonic

SK8603160L

Silicon N-channel MOS FET

For Load-switching / For DC-DC Converter

■ Features

- Low Drain-source On-state Resistance : RDS(on) typ = $3.3 \text{ m}\Omega$ (VGS = 4.5 V)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)
- Marking Symbol : 16

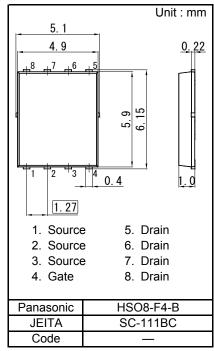
■ Packaging

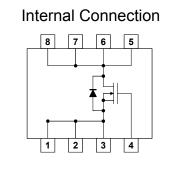
Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter			Symbol	Rating	Unit		
Drain to Source Voltage			VDS	30	V		
Gate to Source Voltage			VGS	±20	V		
Drain Current	Ta = 25 °C, t = 10 s *1		ID	34			
	Ta = 25 °C, DC *1			22	Α		
	Tc = 25 °C			70			
	Pulsed	d, Tch < 150 °C ^{*2}		102			
Total Power			PD	2.8	W		
Dissipation		Ta = 25 °C, DC *1 Tc = 25 °C	PD	28			
Thermal Resistance		Channel to Ambient	Rth(ch-a)	44	°C / W		
memai Resisi	ance	Channel to Case	Rth(ch-c)	4.5	-C / W		
Channel Temperature			Tch	150			
Operating ambient temperature			Topr	-40 to +85	°C		
Storage Temperature Range			Tstg	-55 to +150			
Avalanche Current (Single pulse) *3		IAR	17	Α			
Avalanche Energy (Single pulse) *3			EAR	36	mJ		

- Note *1 Device mounted on a glass-epoxy board in Figure 1
 - *2 Pulse test: Ensure that the channel temperature does not exceed 150 °C
 - *3 VDD = 24 V, VGS = 10 to 0 V, L = 0.1 mH, Tch = 25 $^{\circ}$ C (initial)





Pin Name

5. Drain

- 1. Source
- Source
 Source
 Drain
 Drain
- 4. Gate 8. Drain



Figure 1 FR4 Glass-Epoxy Board 25.4 mm × 25.4 mm × 0.8 mm

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■ Electrical Characteristics Ta = 25 °C ± 3 °C

Static Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	30			V
Zero Gate Voltage Drain Current	IDSS	VDS = 30 V, VGS = 0 V			10	μΑ
Gate-source Leakage Current	IGSS	VGS = ± 16 V, VDS = 0 V			±10	μΑ
Gate-source Threshold Voltage	Vth	ID = 3.35 mA, VDS = 10 V	1.3		3	V
Drain-source On-state Resistance	RDS(on)1	ID = 17 A, VGS = 10 V		2.5	3.3	mΩ
Diani-source On-state Resistance	RDS(on)2	ID = 17 A, VGS = 4.5 V		3.3	4.3	

Dynamic Characteristics

Damanastan	Command and	0	N 4:	т	N.4	1.1
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Input Capacitance	Ciss	VDS = 10 V, VGS = 0 V		2 800	3 920	
Output Capacitance	Coss	f = 1 MHz		330	462	pF
Reverse Transfer Capacitance	Crss	1 = 1 MHZ		230	368	
Turn-on Delay Time *1	td(on)	VDD = 15 V, VGS = 0 to 10 V ID = 17 A		13		ns
Rise Time *1	tr			12		
Turn-off Delay Time *1	td(off)	VDD = 15 V, VGS = 10 to 0 V		52		no
Fall Time *1	tf	ID = 17 A		8		ns
Total Gate Charge	Qg	VDD = 15 V VCS = 0 to 4 5 V		22		
Gate to Source Charge	Qgs	VDD = 15 V, VGS = 0 to 4.5 V ID = 17 A		7		nC
Gate to Drain Charge	Qgd	ID - 17 A		9		
Gate resistance	rg	f = 5 MHz		1.2	3	Ω

Body Diode Characteristic

Established: 2012-12-10

: 2013-05-31

Revised

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Diode Forward Voltage	VSD	IS = 17 A, VGS = 0 V		0.8	1.2	V

Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

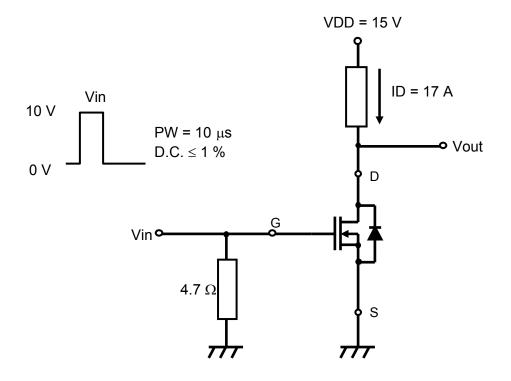
2. *1 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time

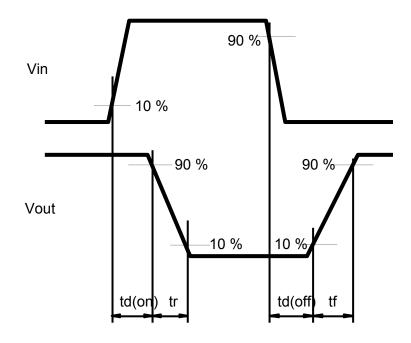
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*1 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time



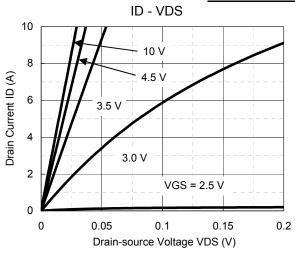


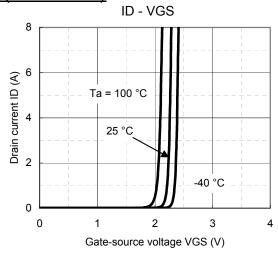
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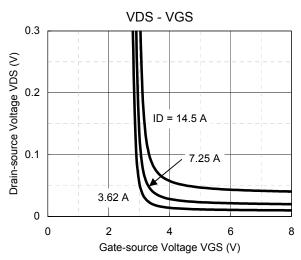
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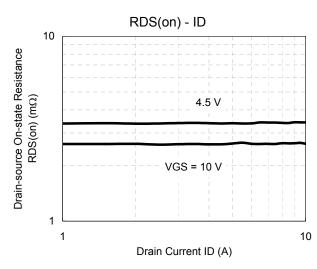
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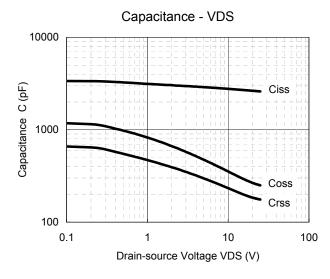
Technical Data (reference)

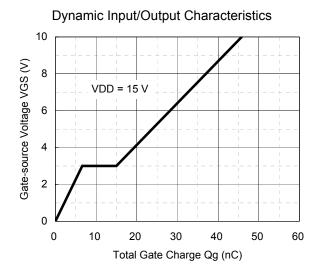








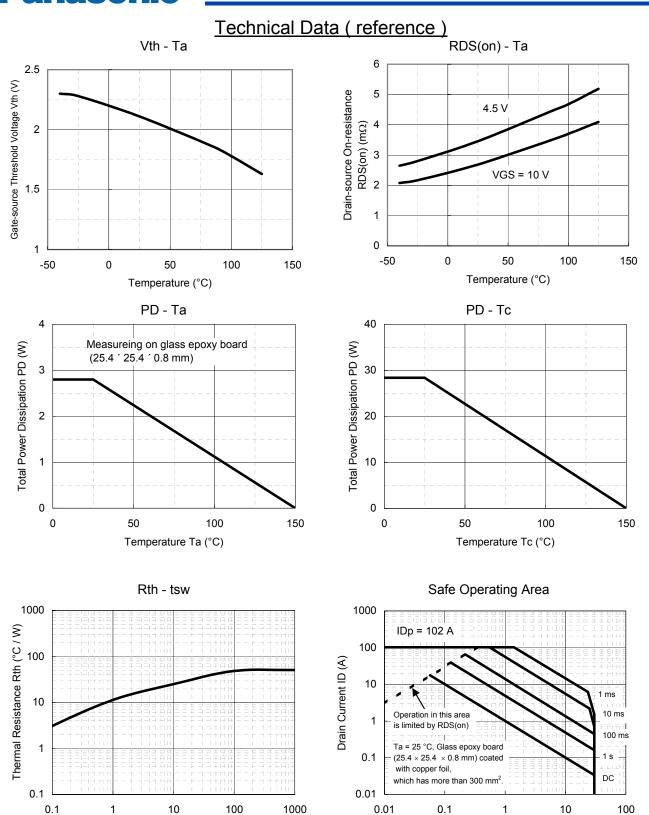




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Drain-source Voltage VDS (V)

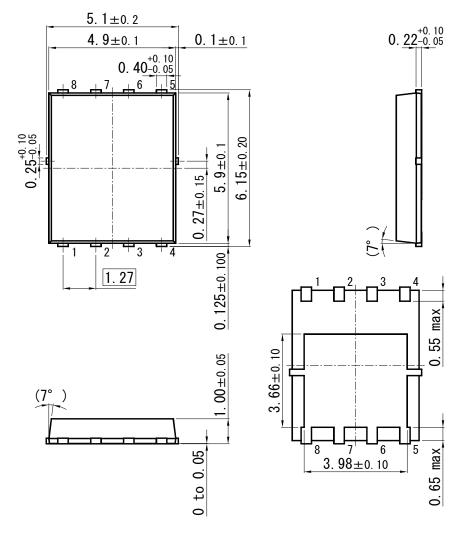
Pulse Width tsw (s)

MOS FET

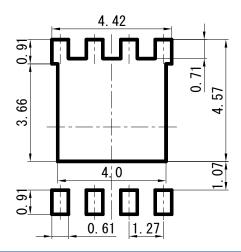
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HSO8-F4-B



■ Land Pattern (Reference) (Unit : mm)



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Established: 2012-12-10 Revised: 2013-05-31

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