UNR91A6G

Silicon PNP epitaxial planar type

For digital circuits

■ Features

- Optimum for high-density mounting and downsizing of the equipment
- Contribute to low power consumption

■ Absolute Maximum Ratings $T_a = 25$ °C

ol Rating	Unit	
-50	V	
-50	V	
-80	mA	
125	mW	
125	°C	
-55 to +125	°C	
)	-50 -50 -80 125	

■ Package

- Code
- SSMini3-F3
- Pin Name
 - 1: Base
 - 2: Emitter
 - 3: Collector

■ Marking Symbol: CN

■ Internal Connection

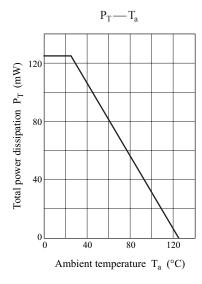
$$B \circ \longrightarrow W \longrightarrow C$$

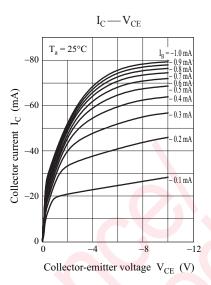
■ Electrical Characteristics $T_a = 25$ °C±3°C

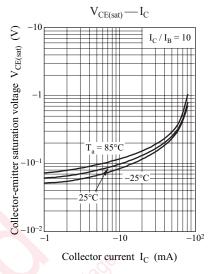
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_{\rm C} = -10 \mu\text{A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$			- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = -50 \text{ V}, I_{B} = 0$			- 0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = -6 \text{ V}, I_C = 0$			-0.01	mA
Forward current transfer ratio	h_{FE}	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	160		460	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -0.3 \text{ mA}$			- 0.25	V
Output voltage high-level	V _{OH}	$V_{CC} = -5 \text{ V}, V_B = -0.5 \text{ V}, R_L = 1 \text{ k}\Omega$	-4.9			V
Output voltage low-level	V _{OL}	$V_{CC} = -5 \text{ V}, V_{B} = -2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$			- 0.2	V
Input resistance	R_1		-30%	4.7	+30%	kΩ
Transition frequency	f_T	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

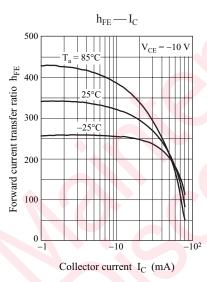
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

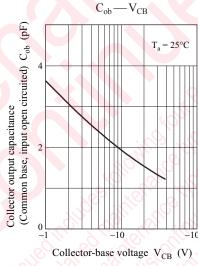
UNR91A6G Panasonic

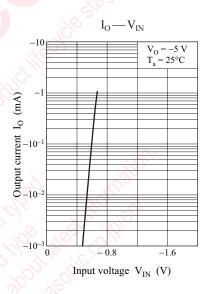


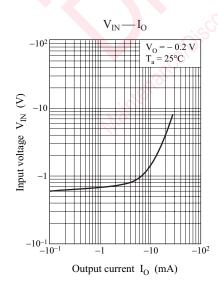








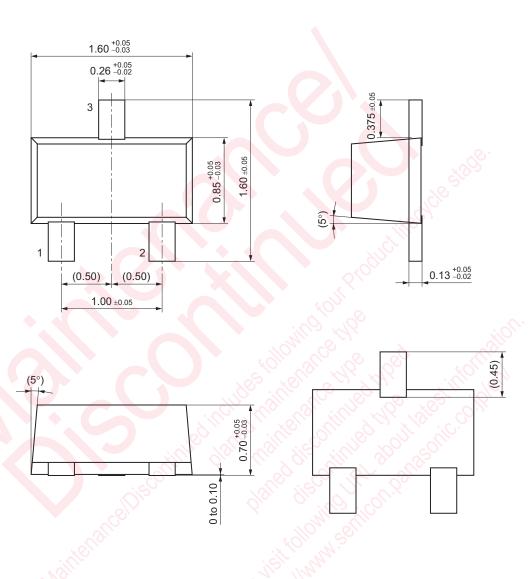




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Panasonic UNR91A6G

SSMini3-F3 Unit: mm



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