UP0121MG

Silicon NPN epitaxial planar type

For switching circuits For digital circuits

■ Features

- Two elements incorporated into one package (Emitter-coupled transistors with built-in resistor)
- SSMini type package, reduction of the mounting area and assembly cost

■ Basic Part Number

• UNR221M × 2

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V_{CBO}	50	V	
Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Collector current	$I_{\rm C}$	100	mA	
Total power dissipation	P_{T}	125	mW	
Junction temperature	T _j	125	°C	
Storage temperature	T _{stg}	-55 to +125	°CO	

■ Package

Code

SSMini5-F3

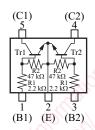
Pin Name

1: Base (Tr1) 4: Collector (Tr2) 2: Emitter 5: Collector (Tr1)

3: Baser (Tr2)

■ Marking Symbol: EM

■ Internal Connection

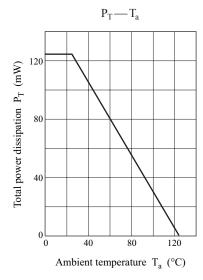


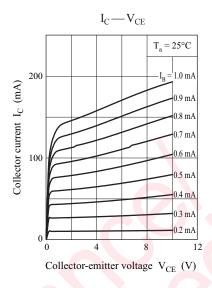
■ Electrical Characteristics $T_a = 25$ °C±3°C

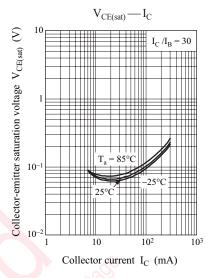
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_{\rm C} = 10 \mu\text{A}, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 50 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = 6 \text{ V}, I_{C} = 0$			0.2	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	80			_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$			0.25	V
Output voltage high-level	V _{OH}	$V_{CC} = 5 \text{ V}, V_{B} = 0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$	4.9			V
Output voltage low-level	V _{OL}	$V_{CC} = 5 \text{ V}, V_{B} = 2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$			0.2	V
Input resistance	R_1		-30%	2.2	+30%	kΩ
Resistance ratio	R_1/R_2			0.047		_
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_{E} = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

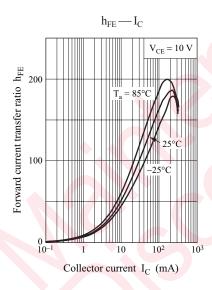
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

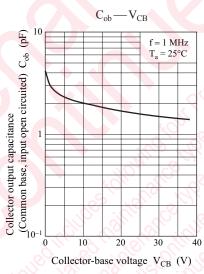
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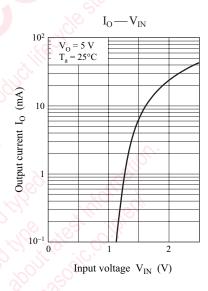


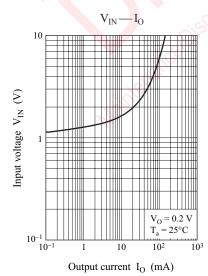








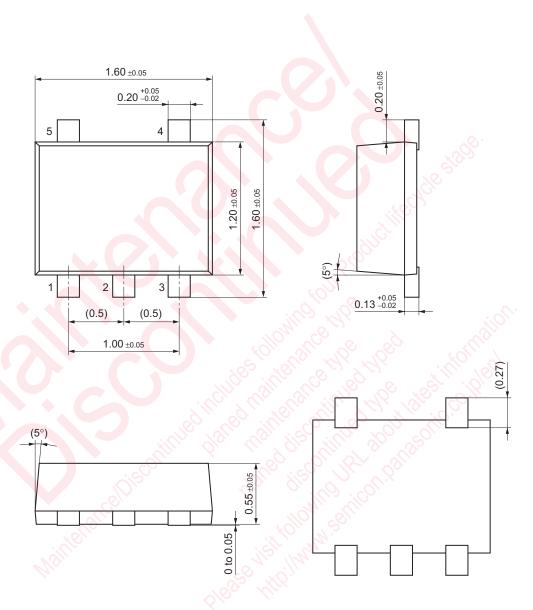




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SSMini5-F3 Unit: mm



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