Ρ	J٧	<b>V7</b> N	106	A-A	U

## 60V N-Channel Enhancement Mode MOSFET

Current

6.6 A

### Features

Voltage

- R<sub>DS(ON)</sub>, V<sub>GS</sub>@10V, I<sub>D</sub>@6A<34mΩ
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@3A<40mΩ
- Advanced Trench Process Technology

60 V

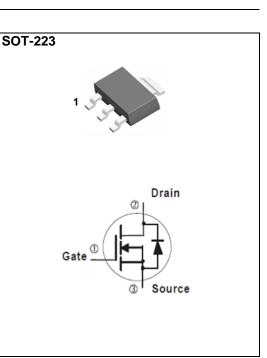
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123grams

#### **Maximum Ratings and Thermal Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMET	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V <sub>DS</sub>	60		
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V	
Quality During Quality (Note 4)	T <sub>A</sub> =25°C		6.6	A	
Continuous Drain Current (Note 4)	T <sub>A</sub> =70°C	I <sub>D</sub>	5.3		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	26.4		
	T <sub>A</sub> =25°C	_	3.7		
Power Dissipation	T <sub>A</sub> =70°C	- P <sub>D</sub>	2.6	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~175	°C	
Typical Thermal Resistance - Junction to Ambient <sup>(Note 4,5)</sup>		R <sub>eJA</sub>	40.3	°C/W	
Limited only By Maximum Juncti	on Temperature			1	











# PJW7N06A-AU

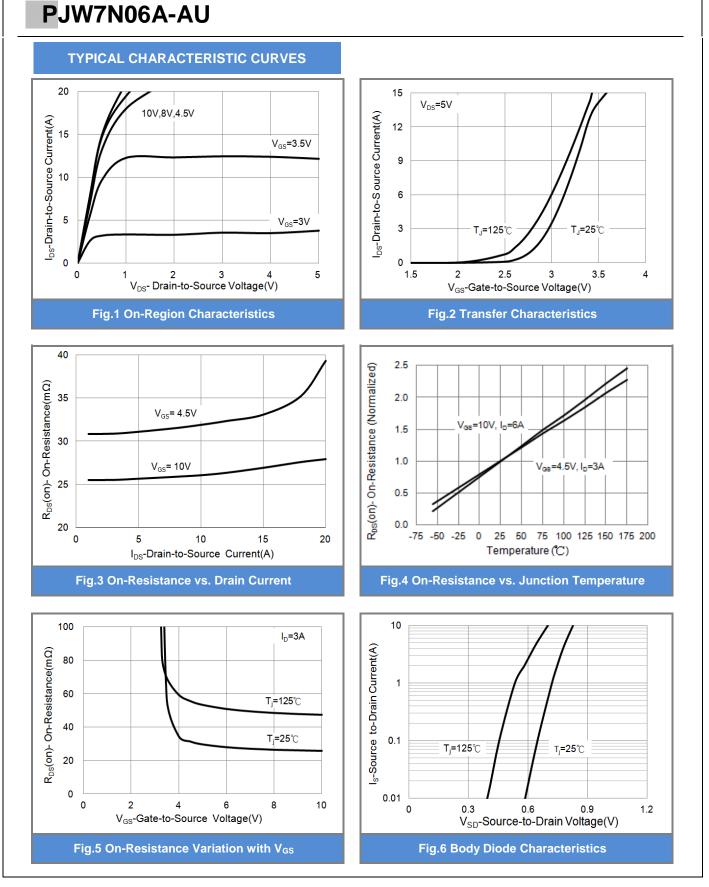
### **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static			<u>.</u>	•	•		
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	v	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	1	1.83	2.5	v	
	R <sub>DS(on)</sub>	$V_{GS}$ =10V, $I_{D}$ =6A	-	28	34		
Drain-Source On-State Resistance		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	33	40	mΩ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 6)							
Total Gate Charge	$Q_{g}$	V <sub>DS</sub> =30V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V <sup>(Note 2,3)</sup>	-	20	-	nC	
Gate-Source Charge	$Q_{gs}$		-	3.8	-		
Gate-Drain Charge	$Q_gd$	V <sub>GS</sub> =10V	-	3.9	-		
Input Capacitance	Ciss		-	1173	-	pF	
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHZ	-	63	-		
Reverse Transfer Capacitance	Crss		-	44	-		
Turn-On Delay Time	td <sub>(on)</sub>		-	7.1	-		
Turn-On Rise Time	tr	$V_{DD}=15V, I_{D}=1A,$	-	25	-		
Turn-Off Delay Time	td <sub>(off)</sub>	V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω (Note 2,3)	-	31	-		
Turn-Off Fall Time	tf		-	20	-		
Drain-Source Diode			-	•	•		
Maximum Continuous Drain-Source	I.		_	-	6.6	А	
Diode Forward Current	I <sub>S</sub>						
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.72	1.2	V	

NOTES :

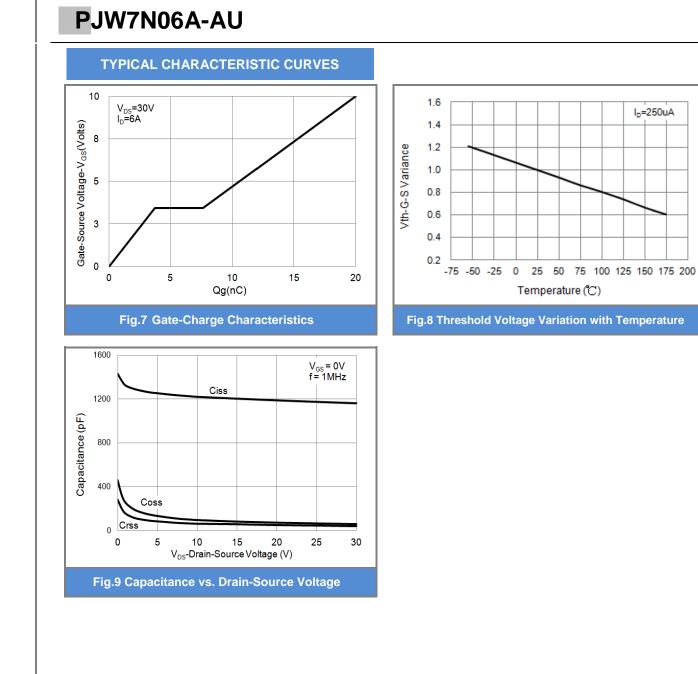
- 1. Pulse width</br>200us, Duty cycle2%.
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited.
- 5. R<sub>®JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.

March 28,2019-REV.00













Ip=250uA



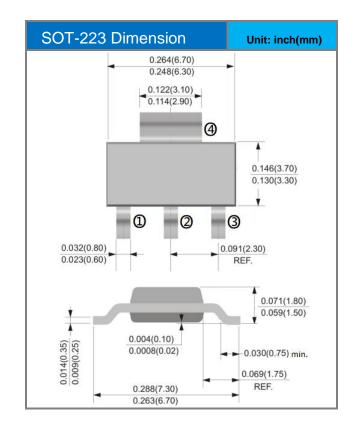


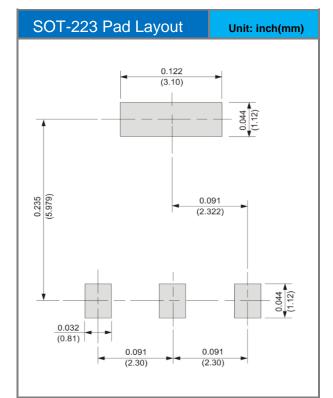
# PJW7N06A-AU

#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJW7N06A-AU_R2_000A1	SOT-223	2,500pcs / 13" reel	W7N06A	Halogen free

### Packaging Information & Mounting Pad Layout







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# PJW7N06A-AU

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