



20V P-Channel Enhancement Mode MOSFET

Voltage

-20 V

Current

-5.2A

Features

- R_{DS(ON)}, V_{GS}@-4.5V, I_D@-5.2A<48mΩ
- R_{DS(ON)}, V_{GS}@-2.5V, I_D@-3.0A<60mΩ
- $R_{DS(ON)}$, $V_{GS}@-1.8V$, $I_{D}@-1.5A<88m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

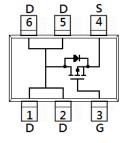
• Case: SOT-23 6L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0005 ounces, 0.0142 grams

SOT-23 6L





Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage		V _G s	<u>+</u> 12		
Continuous Drain Current (Note 4)		ID	-5.2	А	
Pulsed Drain Current (Note 1)		I _{DM}	-20.8		
Power Dissipation	T _a =25°C	P _D	2	W	
	Derate above 25°C		16	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal resistance - Junction to Ambient (Note 3,4)		Reja	62.5	°C/W	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.74	-1.3	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-5.2A	-	40	48	mΩ
		V _{GS} =-2.5V, I _D =-3A	-	50	60	
		V _{GS} =-1.8V, I _D =-1.5A	-	75	88	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-16V, V _{GS} =0V	1	-	-1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_g	V _{DS} =-10V, I _D =-5.2A, V _{GS} =-4.5V (Note 1,2)	-	10	-	nC
Gate-Source Charge	Q_{gs}		-	1.7	-	
Gate-Drain Charge	Q_{gd}		-	2.4	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V,	-	980	-	pF
Output Capacitance	Coss		-	100	-	
Reverse Transfer Capacitance	Crss	f=1MHZ	-	81	-	
Switching						
Turn-On Delay Time	td _(on)		-	9.8	-	ns
Turn-On Rise Time	tr	$V_{DD}\text{=-}10V, \ I_{D}\text{=-}5.2A,$ $V_{GS}\text{=-}4.5V,$ $R_{G}\text{=-}6\Omega \text{ (Note 1,2)}$	-	54	-	
Turn-Off Delay Time	td _(off)		-	44	-	
Turn-Off Fall Time	tf		-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	Is		_	_	-2	А
Diode Forward Current	IS			_	-2	
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V	-	-0.78	-1.2	V

NOTES:

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

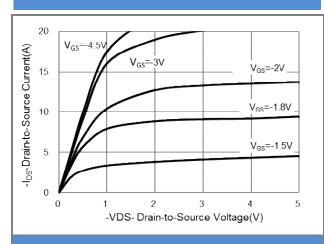


Fig.1 On-Region Characteristics

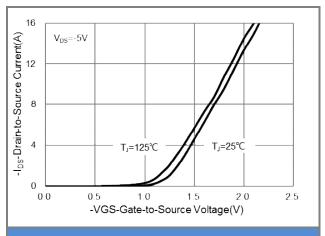


Fig.2 Transfer Characteristics

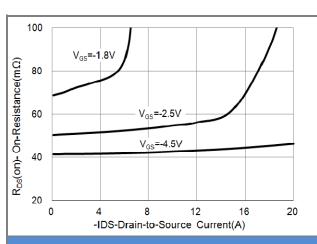


Fig.3 On-Resistance vs. Drain Current

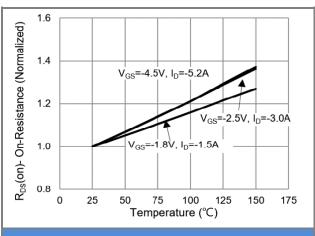
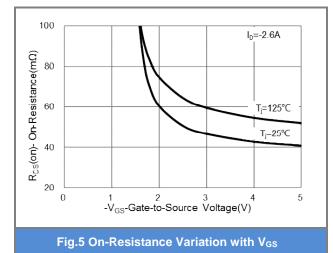


Fig.4 On-Resistance vs. Junction temperature



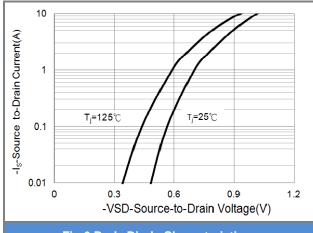


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

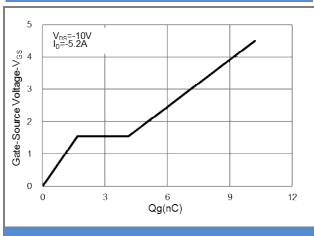


Fig.7 Gate-Charge Characteristics

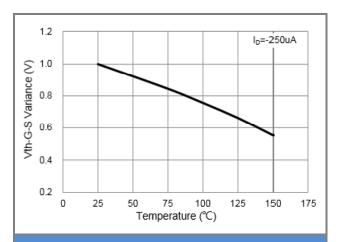


Fig.8 Threshold Voltage Variation with Temperature

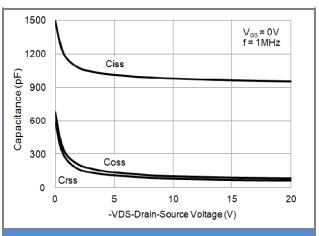


Fig.9 Capacitance vs. Drain-Source Voltage

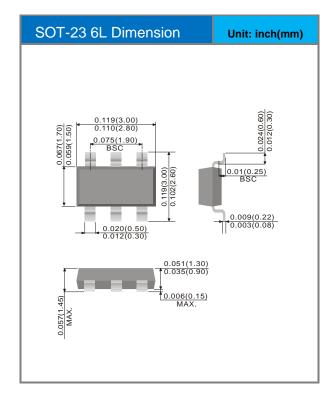


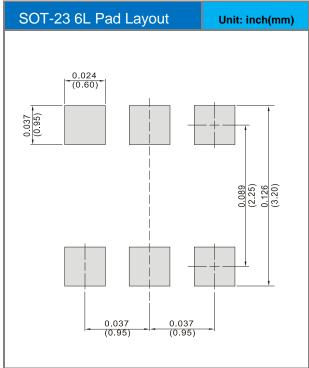


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6415A-AU_S1_000A1	SOT-23 6L	3K pcs / 7" reel	S5A	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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