ΡΛΝ	JIT
	SEMI
	CONDUCTOR

Current

Features

Voltage

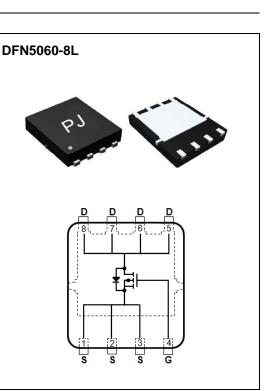
• $R_{DS(ON)}$, V_{GS} @-10V, I_D @-10A<8.5m Ω

-30 V

- $R_{DS(ON)}$, V_{GS} @-4.5V, I_D @-8A<14m Ω
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN5060-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.08 grams



Maximum Ratings and Thermal Characteristics (T_A=25^oC unless otherwise noted)

-60 A

PARAMETE	R	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current ^(Note 4)	Tc=25°C		-60	A	
	Tc=100°C	I _D	-38		
Pulsed Drain Current ^(Note 1)	Tc=25°C	I _{DM}	-240		
Power Dissipation	Tc=25°C	6	63	w	
	Tc=100°C	PD	25		
Continuous Drain Current ^(Note 4)	T _A =25⁰C		-11	А	
	T _A =70°C	lo	-8.8	А	
Power Dissipation	T _A =25⁰C	2.0	2.0		
Power Dissipation	T _A =70°C	PD	1.3	W	
Operating Junction and Storage	Temperature Range	T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	2.0	°C/W	
	Junction to Ambient	R _{θJA}	62.5		

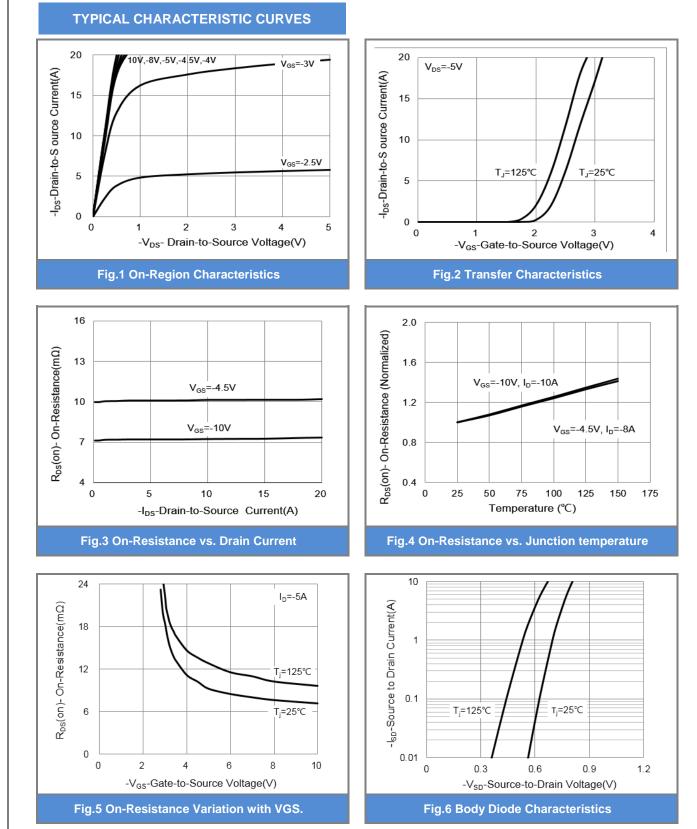


Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V,I_{D}=-250uA$	-30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250uA	-1.0	-1.5	-2.5	V
		V _{GS} =-10V,I _D =-10A	-	7.1	8.5	mΩ
Drain-Source On-State Resistance	RDS(on)	V _{GS} =-4.5V,I _D =-8A	-	10	14	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1.0	uA
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 6)						
Total Gate Charge	Qg	V _{DS} =-15V, I _D =-10A, V _{GS} =-4.5V ^(Note 1,2)	-	27	-	nC
Gate-Source Charge	Q _{gs}		-	8.4	-	
Gate-Drain Charge	Q _{gd}		-	8.7	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	3228	-	pF
Output Capacitance	Coss		-	396	-	
Reverse Transfer Capacitance	Crss		-	254	-	
Turn-On Delay Time	td _(on)	V _{DS} =-15V,ID=-1A, V _{GS} =-10V, R _G =6Ω	-	10	-	
Turn-On Rise Time	tr		-	13	-	
Turn-Off Delay Time	td _(off)		-	111	-	ns
Turn-Off Fall Time	t _f		-	51	-	
Drain-Source Diode						
Maximum Continuous Drain-Source						
Diode Forward Current	ls		-	-	-60	A
Diode Forward Voltage	V _{SD}	I _S =-1A,V _{GS} =0V	-	-0.7	-1	V

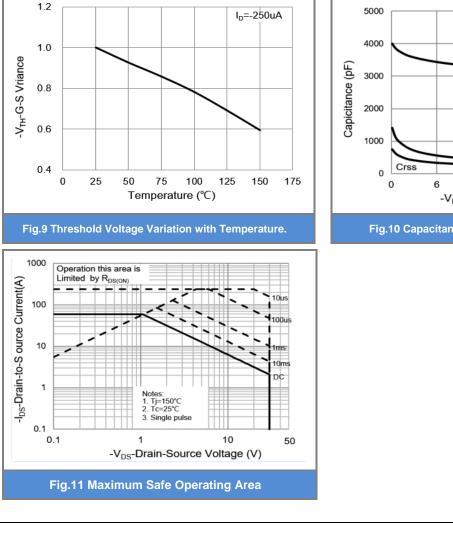
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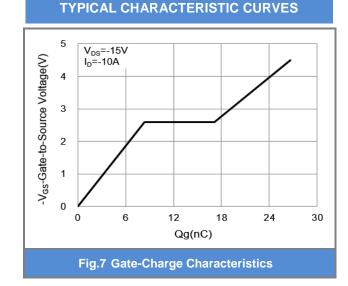
- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J=25°C.
- 4. The maximum current rating is package limited
- 5. Roja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing



SEMI

PANJ

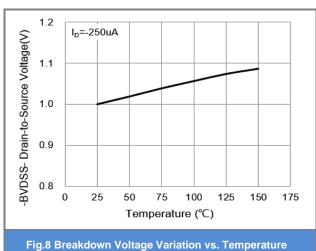




PANJ

SEMI CONDUCTOR

PJQ5423-AU



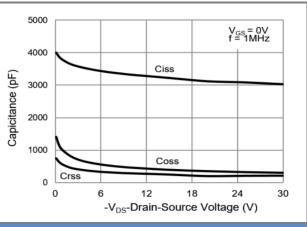
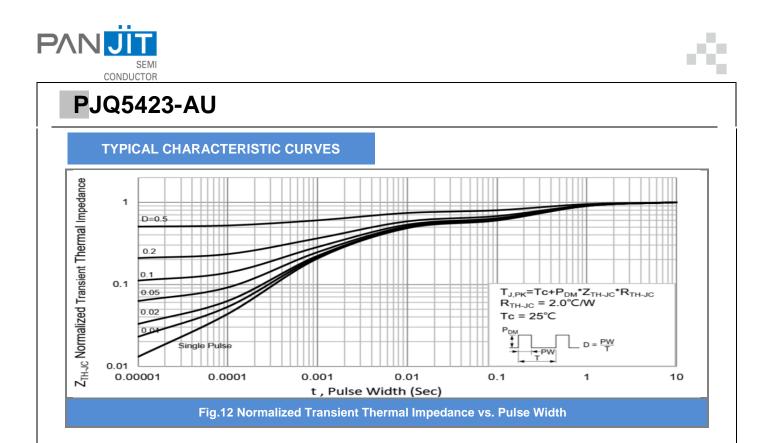


Fig.10 Capacitance vs. Drain-Source Voltage.

PJQ5423-AU-REV.01



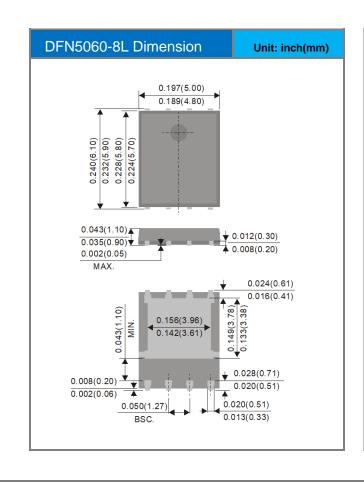
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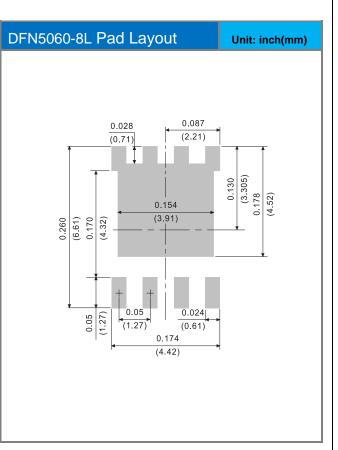


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ5423-AU_R2_000A1	DFN5060-8L	3000pcs / 13" reel	Q5423	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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