

PJL9401

30V P-Channel Enhancement Mode MOSFET

Current

-3.5A

Features

Voltage

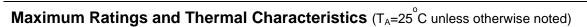
• $R_{DS(ON)}$, V_{GS} @-10V, I_D @-3A<110m Ω

-30 V

- $R_{DS(ON)}$, V_{GS} @-4.5V, I_D @-2A<150m Ω
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOP-8 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



SOP-8

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	Ň	
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _A =25°C		-3.5		
	T _A =70°C	I _D	-2.8	А	
Pulsed Drain Current (Note 1)		I _{DM}	-14	7	
Power Dissipation	T _A =25°C		2.1	w	
	T _A =70°C	P _D	1.3		
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 5)		R _{θJA}	59.5	°C/W	





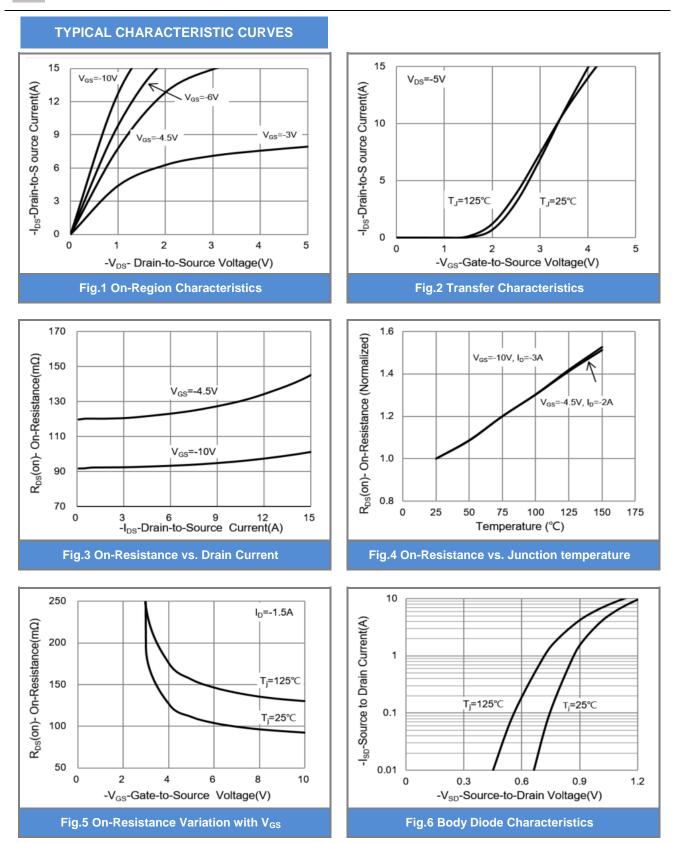
Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

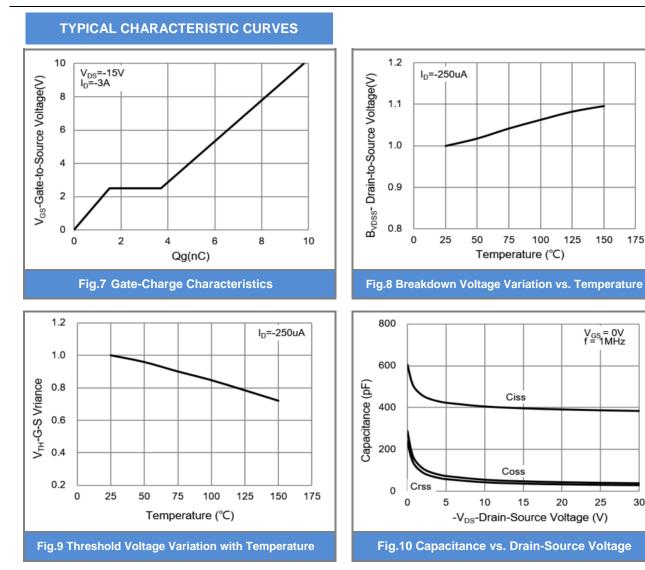
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	- V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1	-1.31	-2.1	
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =-10V, I _D =-3A	-	92	110	mΩ
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =-4.5V, I _D =-2A	-	120	150	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)				•		-
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-3A, V _{GS} =-10V ^(Note 1,2)	-	9.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	
Gate-Drain Charge	Q_gd		-	2.2	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	396	-	pF ns
Output Capacitance	Coss		-	47	-	
Reverse Transfer Capacitance	Crss		-	36	-	
Turn-On Delay Time	td _(on)	V_{DD} =-15V, I _D =-3A, V_{GS} =-10V, R_{G} =6 Ω ^(Note 1,2)	-	5.1	-	
Turn-On Rise Time	tr			30		
Turn-Off Delay Time	td _(off)		-	25	-	
Turn-Off Fall Time	tf		-	7.8	-	
Drain-Source Diode			-			
Maximum Continuous Drain-Source	I _S		-	-	-3.5	A
Diode Forward Current	۶					
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V	-	-0.77	-1.2	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.







125

150

V_{GS} = 0V f = 1MHz

25

30

175

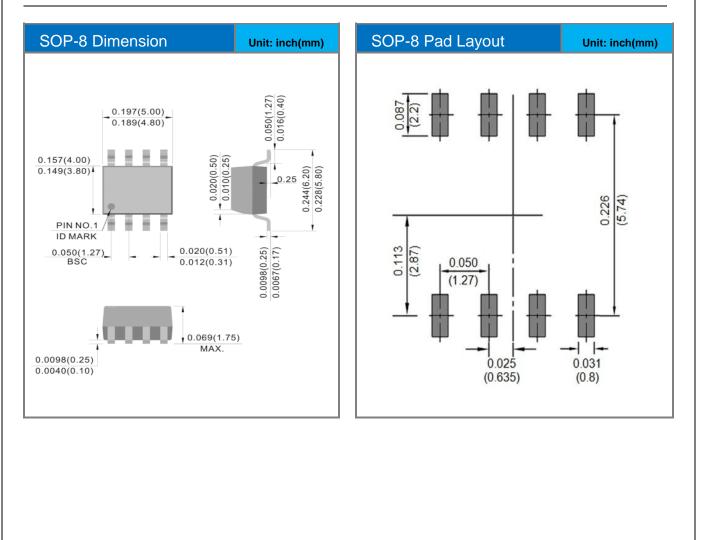




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9401_R2_00001	SOP-8	2.5K pcs / 13" reel	L9401	Halogen free

Packaging Information & Mounting Pad Layout







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