PΛN	ĴΪΤ
	SEMI CONDUCTOR

#### 20V P-Channel Enhancement Mode MOSFET -20 V Current -1.3A

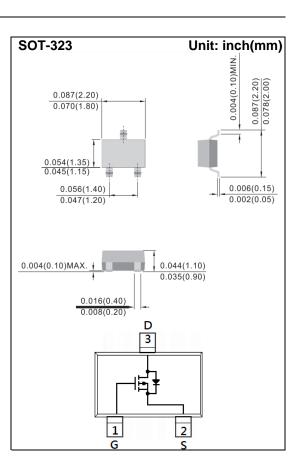
#### Features

Voltage

- RDS(ON), VGS@-4.5V, ID@-1.3A<125mΩ</li>
- RDS(ON), VGS@-2.5V, ID@-1.0A<150mΩ
- RDS(ON), VGS@-1.8V, ID@-0.5A<200mΩ •
- Advanced Trench Process Technology •
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

#### **Mechanical Data**

- Case : SOT-323 Package
- Terminals : Solderable per MIL-STD-750, Method 2026 ٠
- Approx. Weight: 0.0002 ounces, 0.005 grams •
- Marking : C07



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	<u>+</u> 12	V	
Continuous Drain Current		I <sub>D</sub>	-1.3	А
Pulsed Drain Current (Note 4)		I <sub>DM</sub>	-5.2	А
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C
Typical Thermal resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>θJA</sub>	357	°C/W

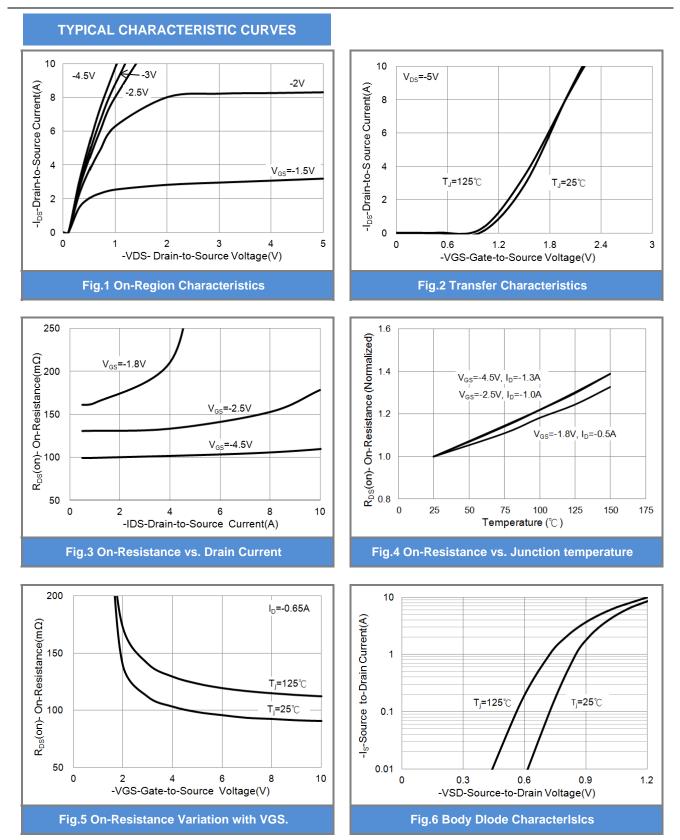


### **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250$ uA	-0.4	-0.69	-1.2	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.3A	-	101	125	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1.0A	-	120	150	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-0.5A	-	139	200	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.3A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	5.4	-	nC
Gate-Source Charge	$Q_gs$		-	0.7	-	
Gate-Drain Charge	$Q_gd$		-	1.4	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V,	-	416	-	
Output Capacitance	Coss		-	43	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	32	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>		-	3.9	-	
Turn-On Rise Time	tr	$V_{DD}$ =-10V, I <sub>D</sub> =-1.3A, $V_{GS}$ =-4.5V, $R_{G}$ =6 $\Omega$ <sup>(Note 1,2)</sup>		27	-	ns
Turn-Off Delay Time	$td_{(off)}$			78	-	
Turn-Off Fall Time	tf	$R_{G}=0\Omega$	-	45	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					0.5	٨
Diode Forward Current	I <sub>S</sub>		-	-	-0.5	A
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V		-0.8	-1.2	V

NOTES :

- 1. Pulse width200us, Duty cycle
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.





TYPICAL CHARACTERISTIC CURVES

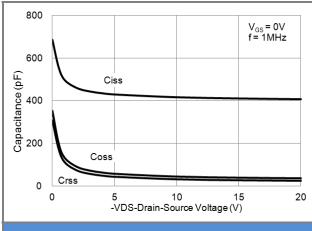


Fig.9 Threshold Voltage Variation with Temperature.

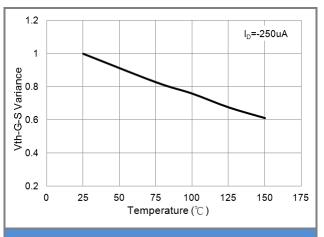


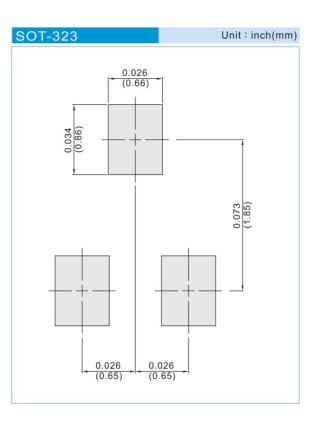
Fig.8 Threshold Voltage Variation with Temperature.

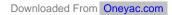


### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJC7407_R1_00001	SOT-323	3K pcs / 7" reel	C07	Halogen free
PJC7407_R2_00001	SOT-323	12K pcs / 13" reel	C07	Halogen free

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