



### **30V N-Channel Enhancement Mode MOSFET**

Voltage

30 V

Current

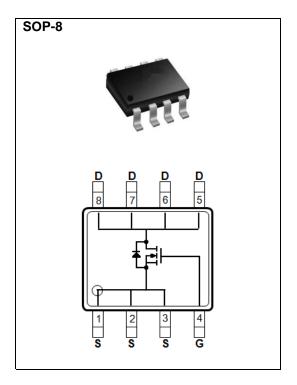
6 A

#### **Features**

- $R_{DS(ON)}$ ,  $V_{GS}$  @10V, $I_D$ @6A<28m $\Omega$
- $\bullet \ \ R_{DS(ON)}, \ V_{GS}@4.5V, \bar{I}_{D}@4A{<}43m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

#### **Mechanical Data**

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



# **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current	T <sub>A</sub> =25°C		6.0	А	
	T <sub>A</sub> =70°C	l <sub>D</sub>	4.8		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	15.4	А	
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	1.7	W	
	T <sub>A</sub> =70°C		1.1		
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 5)		$R_{\theta JA}$	73.5	°C/W	





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	30	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250uA$	1.0	1.3	2.1	V		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =6A	-	24	28	mΩ		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V,I <sub>D</sub> =4A	-	37	43	mΩ		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V	-	-	1.0	uA		
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 6)								
Total Gate Charge	$Q_g$	V <sub>DS</sub> =15V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V <sup>(Note 3)</sup>	-	7.8	-	nC		
Gate-Source Charge	$Q_gs$		-	1.2	-			
Gate-Drain Charge	$Q_gd$		-	1.5	-			
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	343	-	pF		
Output Capacitance	Coss		-	48	-			
Reverse Transfer Capacitance	Crss		-	34	-			
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}$ =15V, $I_{D}$ =6A, $V_{GS}$ =10V, $R_{G}$ =3 $\Omega$ (Note 3)	-	3	-	ns		
Turn-On Rise Time	tr		-	40	-			
Turn-Off Delay Time	td <sub>(off)</sub>		-	38	-			
Turn-Off Fall Time	tf		-	39	-			
Drain-Source Diode								
Maximum Continuous Drain-Source	,		-	-	6.0	A		
Diode Forward Current	I <sub>S</sub>							
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.78	1.2	V		

#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





#### **TYPICAL CHARACTERISTIC CURVES**

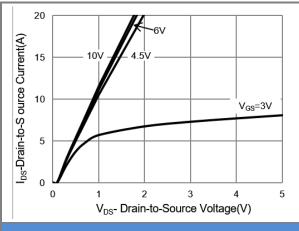
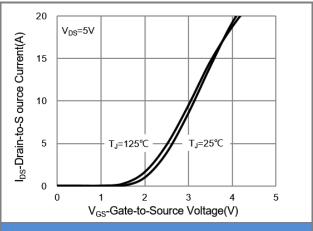


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

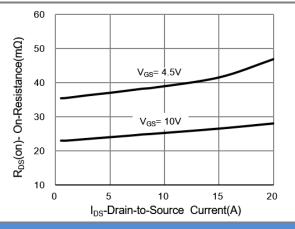


Fig.3 On-Resistance vs. Drain Current

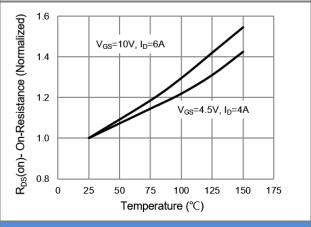


Fig.4 On-Resistance vs. Junction temperature

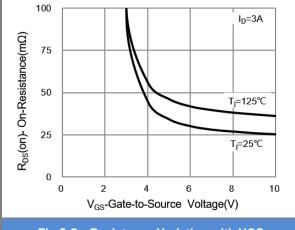
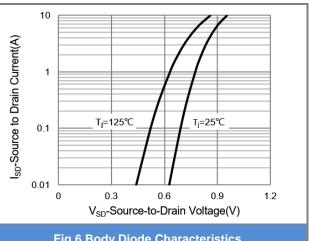


Fig.5 On-Resistance Variation with VGS.

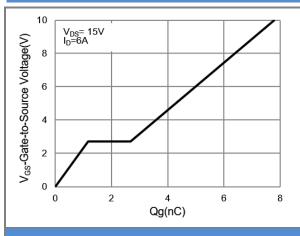


**Fig.6 Body Diode Characteristics** 





#### **TYPICAL CHARACTERISTIC CURVES**



**Fig.7 Gate-Charge Characteristics** 

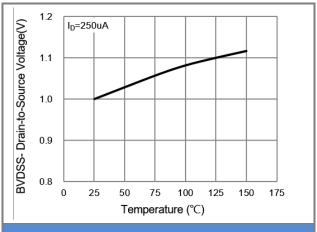


Fig.8 Breakdown Voltage Variation vs. Temperature

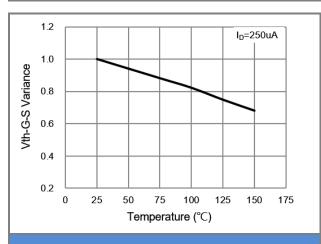


Fig.9 Threshold Voltage Variation with Temperature.

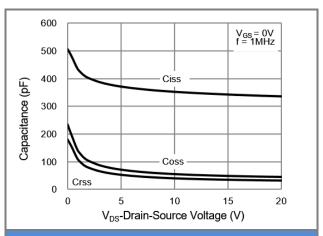


Fig.10 Capacitance vs. Drain-Source Voltage.

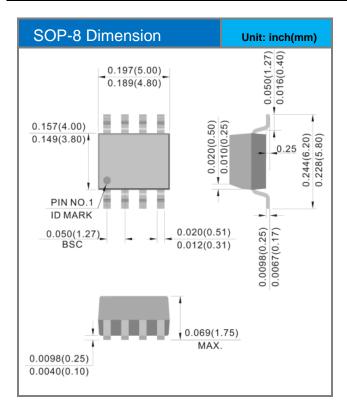


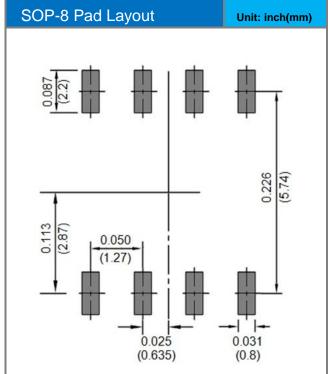


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9404_R2_00001	SOP-8	2.5K pcs / 13" reel	L9404	Halogen free

### **Packaging Information & Mounting Pad Layout**









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