



PJS6806-AU

30V N-Channel Enhancement Mode MOSFET

Voltage	30 V	Current	4A
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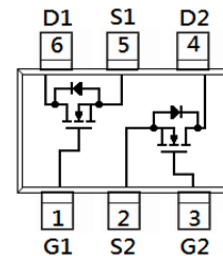
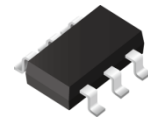
Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@4.0A < 48m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@2.8A < 70m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-23 6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0005 ounces, 0.014 grams

SOT-23 6L



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^(Note 4)		I_D	4	A
Pulsed Drain Current ^(Note 1)		I_{DM}	16	
Power Dissipation	$T_a=25^\circ\text{C}$	P_D	1.25	W
	Derate above 25°C		10	mW/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance		$R_{\theta JA}$	100	$^\circ\text{C/W}$
- Junction to Ambient ^(Note 3,4)				



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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1	1.37	2.1	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =4A	-	34	48	mΩ
		V _{GS} =4.5V, I _D =2.8A	-	50	70	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic (Note 5)						
Total Gate Charge	Q _g	V _{DS} =15V, I _D =4A, V _{GS} =10V (Note 1,2)	-	5.8	-	nC
Gate-Source Charge	Q _{gs}		-	1	-	
Gate-Drain Charge	Q _{gd}		-	1	-	
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1MHZ	-	235	-	pF
Output Capacitance	C _{oss}		-	36	-	
Reverse Transfer Capacitance	C _{rss}		-	24	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =15V, I _D =4A, V _{GS} =10V, R _G =3Ω (Note 1,2)	-	2.5	-	ns
Turn-On Rise Time	t _r		-	39	-	
Turn-Off Delay Time	t _{d(off)}		-	23	-	
Turn-Off Fall Time	t _f		-	28	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	1.5	A
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.75	1.2	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch² FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

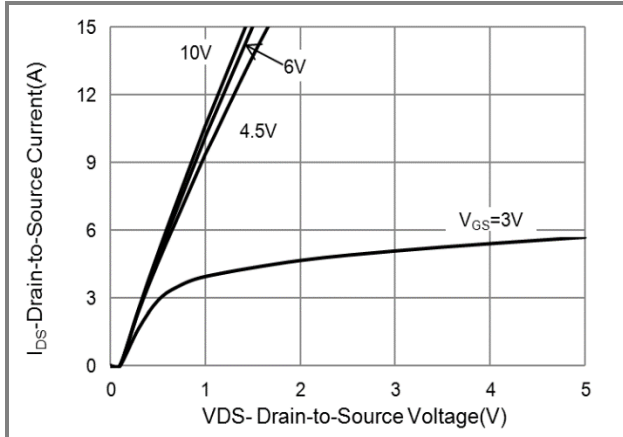


Fig.1 On-Region Characteristics

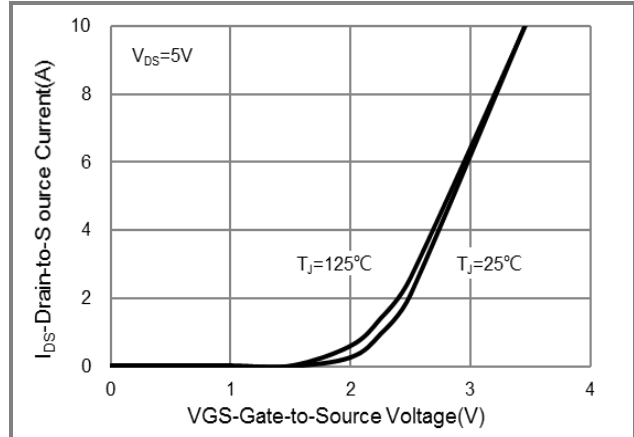


Fig.2 Transfer Characteristics

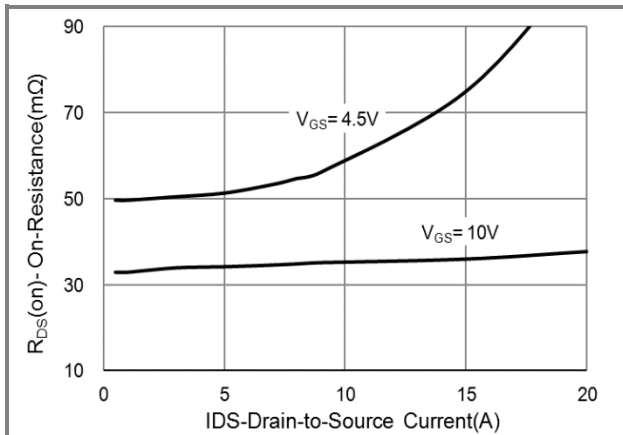


Fig.3 On-Resistance vs. Drain Current

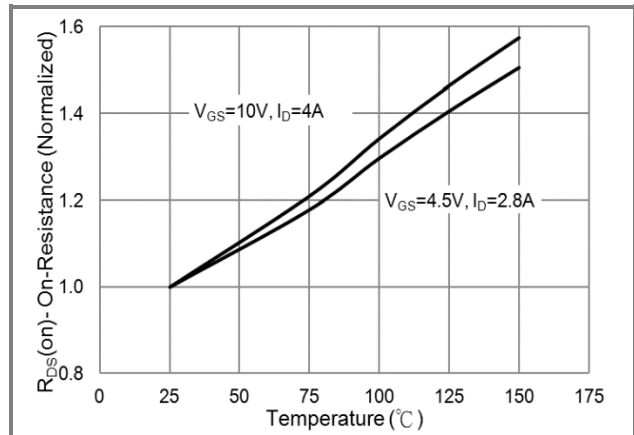


Fig.4 On-Resistance vs. Junction temperature

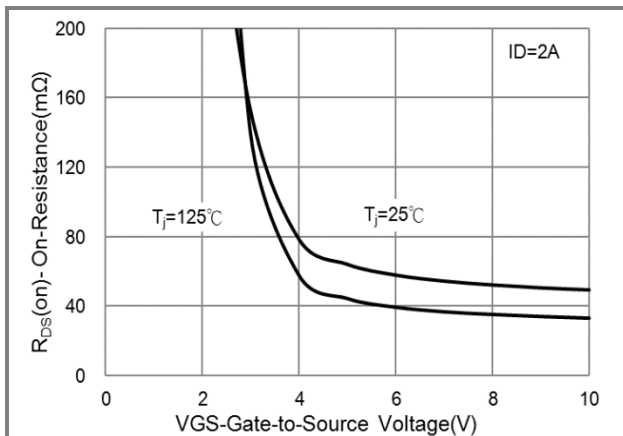


Fig.5 On-Resistance Variation with VGS

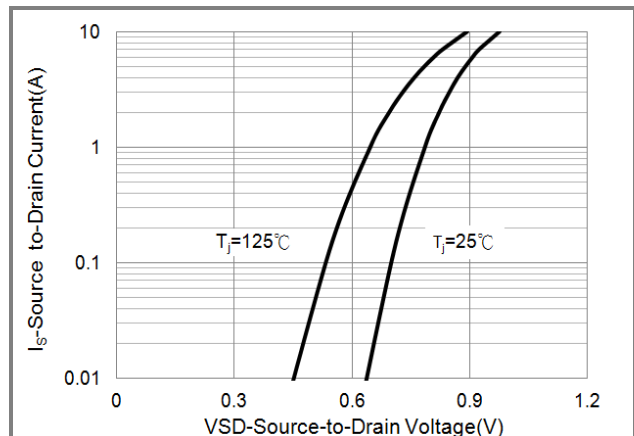


Fig.6 Body Diode Characteristics



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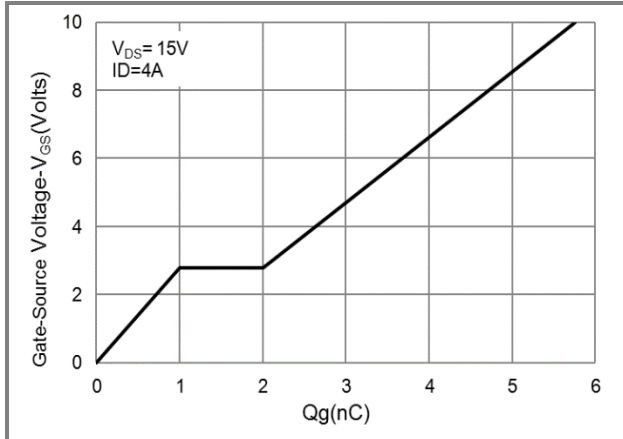


Fig.7 Gate-Charge Characteristics

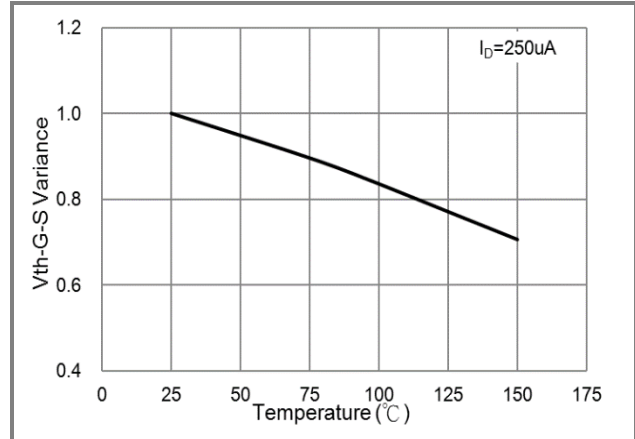


Fig.8 Threshold Voltage Variation with Temperature

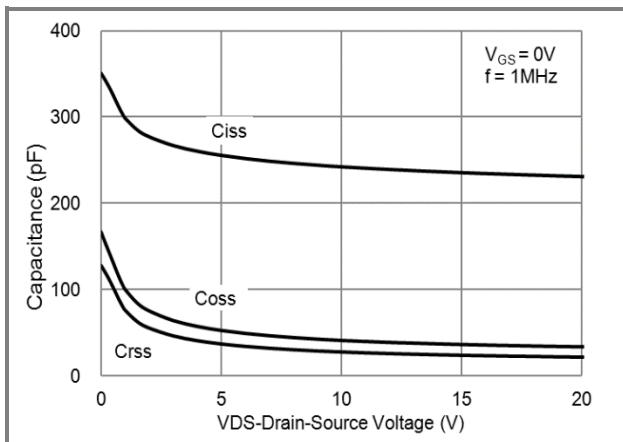


Fig.9 Capacitance vs. Drain-Source Voltage

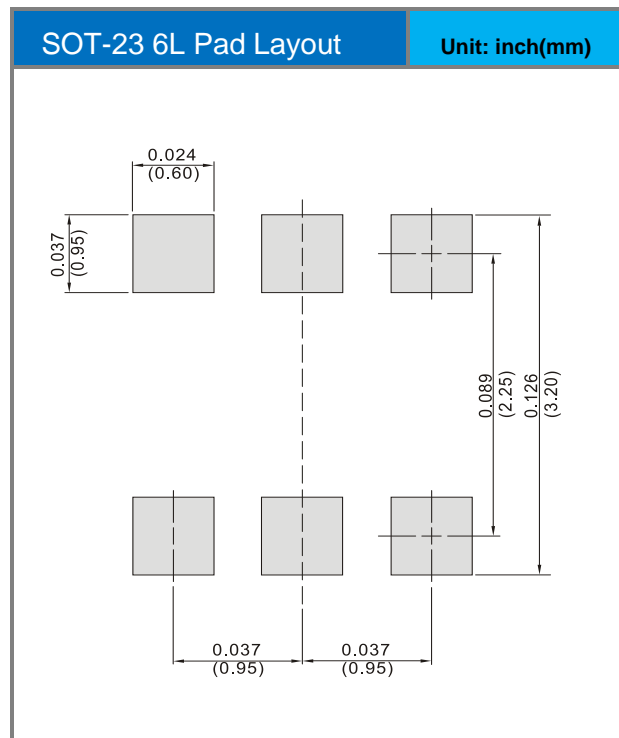
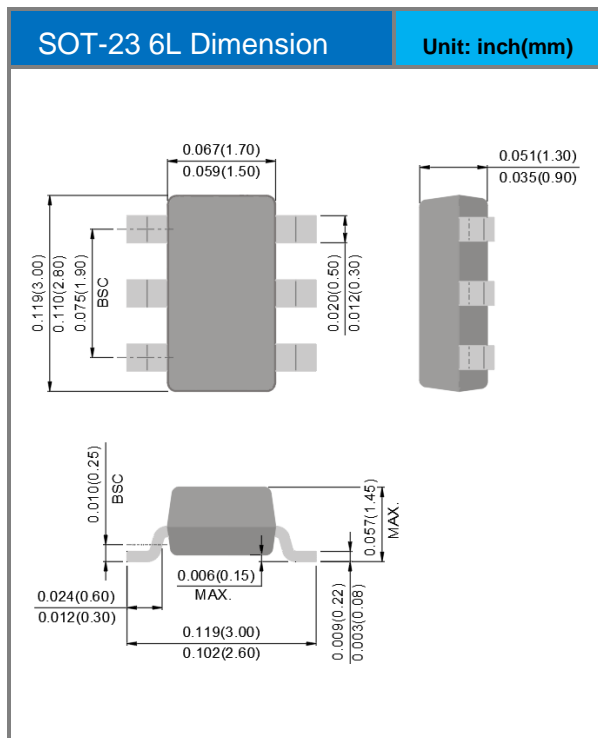


PJS6806-AU

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6806-AU_S1_000A1	SOT-23 6L	3K pcs / 7" reel	ST6	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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