| ΡΛΝ | ĴΤ |
|-----|-------------------|
| | SEMI CONDUCTOR |

30V P-Channel Enhancement Mode MOSFET



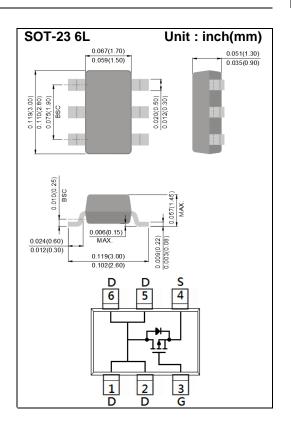
Features

Voltage

- R_{DS(ON)}, V_{GS}@-10V, I_D@-4A<32mΩ
- $R_{DS(ON)}, V_{GS}@-4.5V, I_D@-2A<46m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-23 6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0005 ounces, 0.014 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNITS |
|--|-------------------|------------------|---------|-------|
| Drain-Source Voltage | V _{DS} | -30 | V | |
| Gate-Source Voltage | V _{GS} | <u>+</u> 20 | | |
| Continuous Drain Current(Note 4) | ١D | -6.4 | | |
| Pulsed Drain Current ^(Note 1,3) | ldм | -46 | A | |
| Power Dissipation | T₂=25°C | PD | 2 | W |
| | Derate above 25°C | | 16 | mW/°C |
| Operating Junction and Storage Temperature Range | | TJ,TSTG | -55~150 | °C |
| Typical Thermal Resistance - Junction to Ambient ^(Note 5) | | R _{θJA} | 62.5 | °C/W |

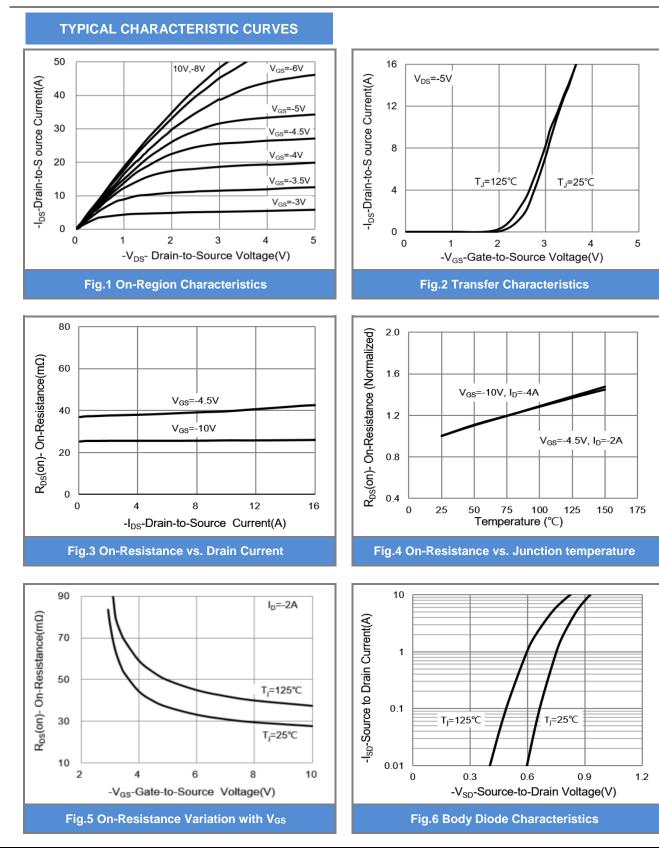


| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS |
|----------------------------------|---------------------|---|------|-------|--------------|------------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =-250uA | -30 | - | - | - V 2.5 |
| Gate Threshold Voltage | $V_{GS(th)}$ | V _{DS} =V _{GS} , I _D =-250uA | -1 | -1.6 | -2.5 | |
| | | V _{GS} =-10V, I _D =-4A | - | 27 | 32 | mΩ |
| Drain-Source On-State Resistance | RDS(on) | V _{GS} =-4.5V, I _D =-2A | - | 38 | 46 | |
| Zero Gate Voltage Drain Current | I _{DSS} | V_{DS} =-30V, V_{GS} =0V | - | - | -1 | uA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = <u>+</u> 20V, V _{DS} =0V | - | - | <u>+</u> 100 | nA |
| Dynamic ^(Note 6) | | | | | | |
| Total Gate Charge | Qg | | - | 7.8 | - | |
| Gate-Source Charge | Q _{gs} | V _{DS} =-15V, I _D =-5A, V _{GS} =-4.5V ^(Note 2,3) | - | 2.7 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 2.8 | - | |
| Input Capacitance | Ciss | V _{DS} =-15V, V _{GS} =0V, f=1MHZ | - | 870 | - | |
| Output Capacitance | Coss | | - | 130 | - | pF |
| Reverse Transfer Capacitance | Crss | | - | 93 | - | |
| Turn-On Delay Time | td _(on) | V_{DD} =-15V, I _D =-1A, V _{GS} =-10V, R _G =6 $\Omega^{(Note 2,3)}$ | - | 6.5 | - | |
| Turn-On Rise Time | tr | | - | 8.8 | - | |
| Turn-Off Delay Time | td _(off) | | - | 73 | - | ns |
| Turn-Off Fall Time | tf | KG=012(1000 2,0) | - | 44 | - | |
| Drain-Source Diode | | | | | | |
| Maximum Continuous Drain-Source | | | | | -2 | А |
| Diode Forward Current | ls | | - | - | -2 | A |
| Diode Forward Voltage | V_{SD} | I _S =-1A, V _{GS} =0V | | -0.75 | -1 | V |

NOTES :

- 1. Pulse width</br>200us, Duty cycle2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing







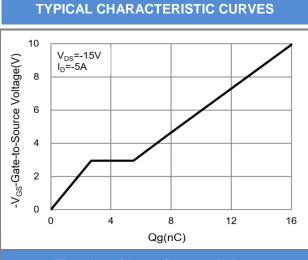
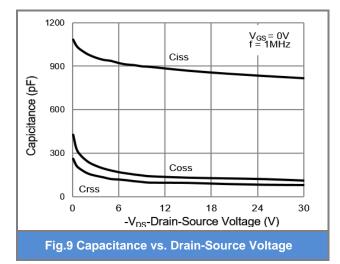
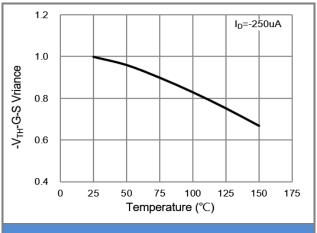


Fig.7 Gate-Charge Characteristics





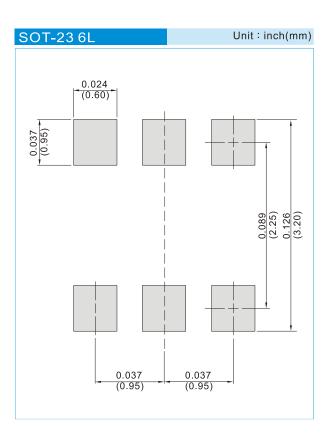




Part No. Packing Code Version

| Part No. Packing Code | Package Type | Packing Type | Marking | Version |
|-----------------------|--------------|------------------|---------|--------------------------------|
| PJS6403_S1_00001 | SOT-23 6L | 3K pcs / 7" reel | S03 | Halogen free RoHS compliant |

Mounting Pad Layout







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