



30V N-Channel Enhancement Mode MOSFET

Voltage 30 V Current 300mA

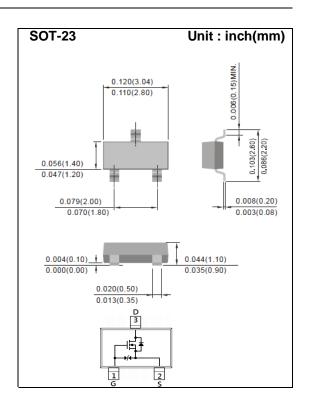
Features

- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V_{GS}	<u>+</u> 10	V
Continuous Drain Current		I _D	300	mA
Pulsed Drain Current		I _{DM}	600	mA
Power Dissipation	T _A =25°C	P_{D}	500	mW
	Derate above 25°C		4	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	250	°C/W

November 8,2016-REV.00 Page 1





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	30	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	0.4	0.75	1.0	V		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =300mA	-	0.7	1.2	Ω		
		V _{GS} =2.5V,I _D =200mA	-	0.8	1.6			
		V _{GS} =1.8V,I _D =100mA	-	0.9	2.0			
		V _{GS} =1.5V,I _D =50mA	-	1.1	3.0			
		V _{GS} =1.2V,I _D =20mA	-	1.5	4.0			
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} =24V,V _{GS} =0V	-	-	1	uA		
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 8V,V _{DS} =0V	-	-	<u>+</u> 10	uA		
Dynamic (Note 4)								
Total Gate Charge	Q_g	V _{DS} =10V, I _D =300mA, V _{GS} =4.5V	-	0.9	-	nC		
Gate-Source Charge	Q_gs		-	0.3	-			
Gate-Drain Charge	Q_gd		-	0.2	-			
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	45	-	pF		
Output Capacitance	Coss		-	14	-			
Reverse Transfer Capacitance	Crss	I=I.UIVIMZ	-	0.8	-			
Turn-On Delay Time	td _(on)	\/ 40\/ 200m A	-	8.3	-			
Turn-On Rise Time	tr	$\begin{array}{c} V_{DD}{=}10V,\ I_{D}{=}300mA,\\ V_{GS}{=}4V,\\ R_{G}{=}10\Omega \end{array}$	-	5.7	-	ns		
Turn-Off Delay Time	td _(off)		-	35	-			
Turn-Off Fall Time	tf		-	12	-			
Drain-Source Diode								
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	300	mA		
Diode Forward Voltage	V_{SD}	I _S =300mA, V _{GS} =0V	-	0.9	1.3	V		

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing





TYPICAL CHARACTERISTIC CURVES

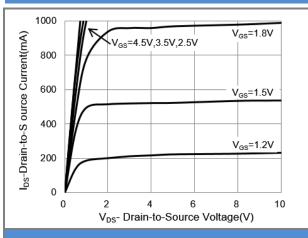


Fig.1 On-Region Characteristics

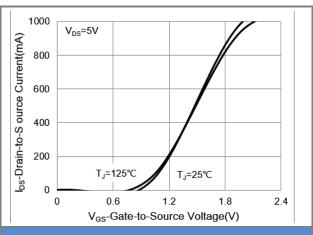


Fig.2 Transfer Characteristics

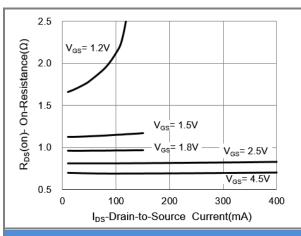


Fig.3 On-Resistance vs. Drain Current

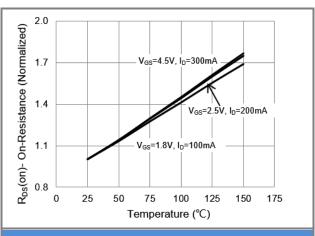


Fig.4 On-Resistance vs. Junction temperature

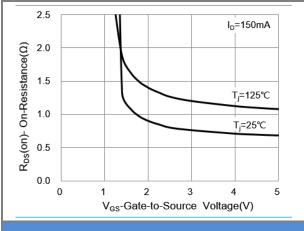
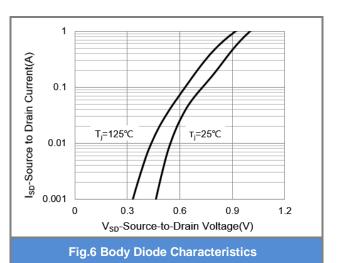


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

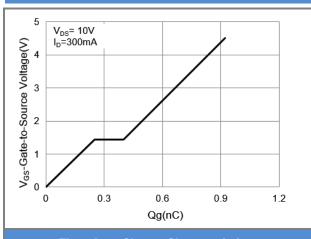


Fig.7 Gate-Charge Characteristics

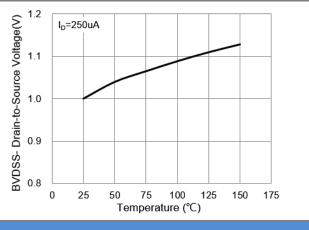


Fig.8 Breakdown Voltage Variation vs. Temperature

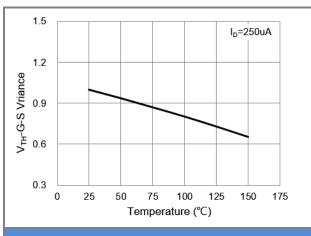


Fig.9 Threshold Voltage Variation with Temperature.

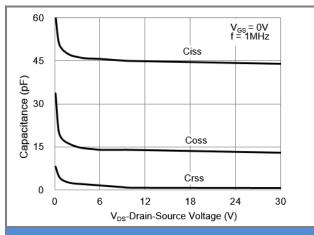


Fig.10 Capacitance vs. Drain-Source Voltage.

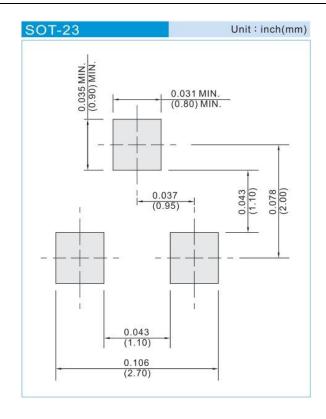




PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing Type	Marking	Version
PJA3428_R1_00001	SOT-23	3K pcs / 7" reel	A28	Halogen free
PJA3428_R2_00001	SOT-23	12K pcs / 13" reel	A28	Halogen free

MOUNTING PAD LAYOUT







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