

20V P-Channel Enhancement Mode MOSFET - ESD Protected

Voltage -20 V Current -1.5A

Features

- RDS(ON), VGS@-4.5V, ID@-1.5A<325m Ω
- R_{DS(ON)} , V_{GS}@-2.5V, I_D@-1.2A<420mΩ
- RDS(ON), VGS@-1.8V, ID@-0.5A<600m Ω
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in comply with EU RoHS 2011/65/EU directives.
- Green molding compound as per IEC61249 Std. (Halogen Free)

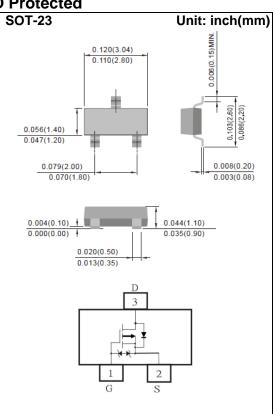
Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A31



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	-1.5	Α
Pulsed Drain Current ^(Note 4)		I _{DM}	-4	Α
Power Dissipation	T _a =25°C		1.25	W
	Derate above 25°C	P _D	10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		Reja	100	°C/W



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA -0		-0.64	-1.0	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-1.5A	-	240	325	mΩ	
		V _{GS} =-2.5V, I _D =-1.2A	-	295	420		
		V _{GS} =-1.8V, I _D =-0.5A	-	405	600		
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-20V, V _{GS} =0V	-	-0.02	-1	uA	
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 3.5	<u>+</u> 10	uA	
Dynamic							
Total Gate Charge	Qg	101/ 1 154	-	1.7	-	nC	
Gate-Source Charge	Qgs	V _{DS} =-10V, I _D =-1.5A, V _{GS} =-4.5V ^(Note 1,2)	-	0.35	-		
Gate-Drain Charge	Q_gd	VGS=-4.5 V(Note 1,2)	-	0.43	-		
Input Capacitance	Ciss		-	165	-	pF	
Output Capacitance	Coss	V _{DS} =-10V, V _{GS} =0V,	-	25	-		
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	14.7	-		
Switching							
Turn-On Delay Time	td _(on)		-	11	-		
Turn-On Rise Time	tr	V _{DD} =-10V, I _D =-1.5A,	-	38	-		
Turn-Off Delay Time	td _(off)	$V_{GS}=-4.5V$, $R_{G}=6\Omega^{(Note 1,2)}$	-	130	-	ns	
Turn-Off Fall Time	tf	RG=012(1000 1,2)	-	75	-	<u> </u>	
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	-1.6	А	
Diode Forward Voltage	V _{SD}	Is=-1.6A, V _{GS} =0V	-	-1.03	-1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch FR-4 with 2oz.square pad of copper.
- 4. The maximum current rating is package limited.



TYPICAL CHARACTERISTIC CURVES

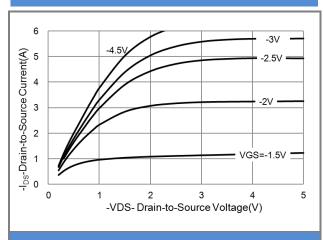


Fig.1 On-Region Characteristics

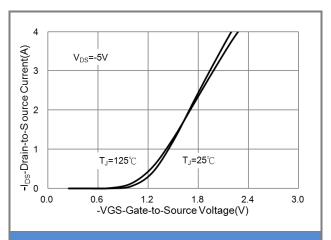


Fig.2 Transfer Characteristics

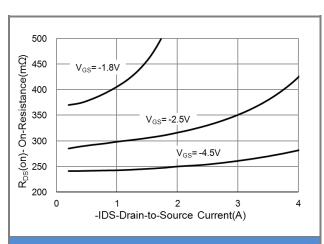


Fig.3 On-Resistance vs. Drain Current

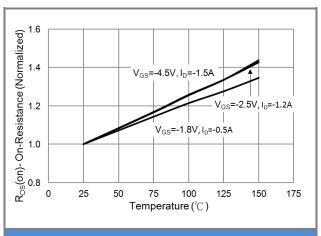
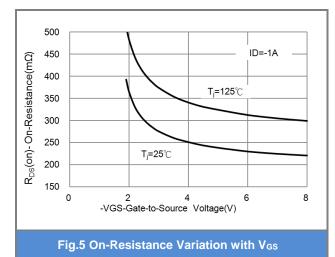
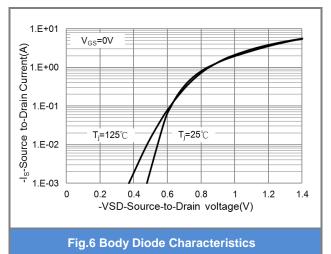


Fig.4 On-Resistance vs. Junction temperature







TYPICAL CHARACTERISTIC CURVES

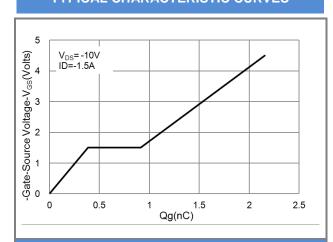


Fig.7 Gate-Charge Characteristics

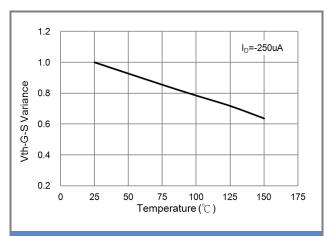


Fig.8 Threshold Voltage Variation with Temperature

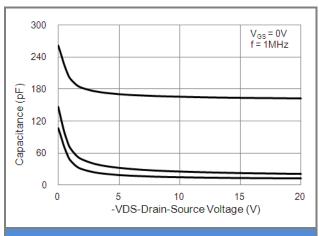


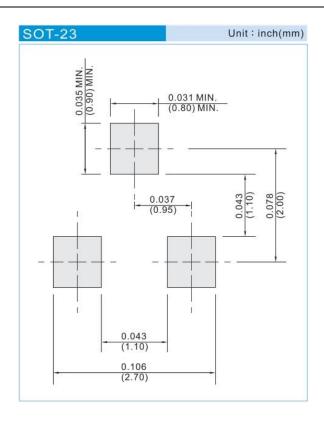
Fig.9 Capacitance vs. Drain-Source Voltage



Product and Packing Information

Part No.	Package Type	Packing Type	Marking	
PJA3431	SOT-23	3K pcs / 7" reel	A31	

Mounting Pad Layout





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