



30V P-Channel Enhancement Mode MOSFET - ESD Protected

Voltage

-30 V

Current

-0.5A

Features

- RDS(ON), VGS@-4.5V, ID@-0.5A<390mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.3A<560mΩ
- RDS(ON), VGS@-1.8V, ID@-0.1A<990mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

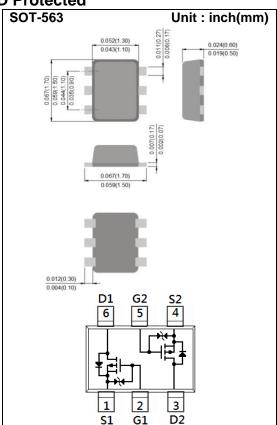
Mechanical Data

Case: SOT-563 Package

Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0026 grams

Marking : X05



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	-0.5	Α
Pulsed Drain Current		I _{DM}	-2.0	Α
Power Dissipation	T _a =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	417	°C/W
ounction to Ambient				





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	ı	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.98	-1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-0.5A	-	318	390	mΩ
		V _{GS} =-2.5V, I _D =-0.3A	-	427	560	
		V _{GS} =-1.8V, I _D =-0.1A	-	853	990	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 3.2	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-0.5A, V _{GS} =-4.5V ^(Note 1,2)	-	1.6	ı	nC
Gate-Source Charge	Q_{gs}		-	0.5	-	
Gate-Drain Charge	Q_{gd}		-	0.3	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	137	-	pF
Output Capacitance	Coss		-	23	-	
Reverse Transfer Capacitance	Crss	I=1.UIVIHZ	-	10	-	
Switching						
Turn-On Delay Time	td _(on)	\/ 45\/ L 0.5A	-	11	-	ns
Turn-On Rise Time	tr	$\begin{array}{c} V_{DD}\text{=-}15\text{V}, \ I_{D}\text{=-}0.5\text{A}, \\ V_{GS}\text{=-}4.5\text{V}, \\ R_{G}\text{=-}6\Omega^{(Note\ 1,2)} \end{array}$	-	52	-	
Turn-Off Delay Time	td _(off)		-	65	-	
Turn-Off Fall Time	tf		-	46	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	-0.4	Α
Diode Forward Voltage	V _{SD}	Is=-1.0A, V _G s=0V	-	-0.93	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

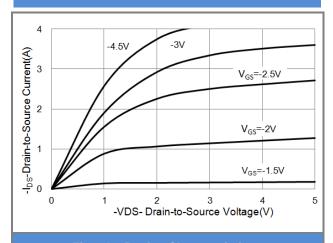


Fig.1 On-Region Characteristics

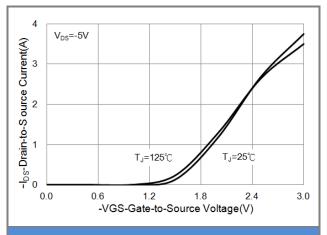


Fig.2 Transfer Characteristics

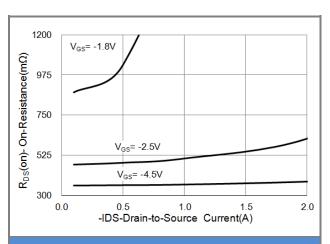


Fig.3 On-Resistance vs. Drain Current

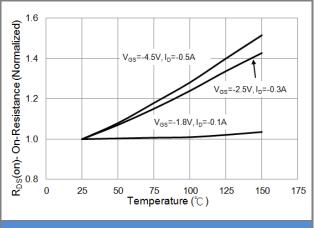
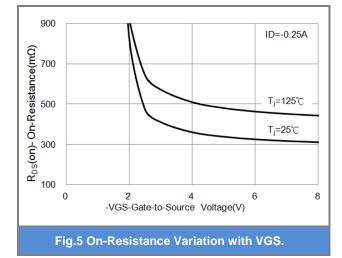
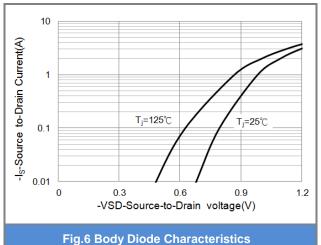


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

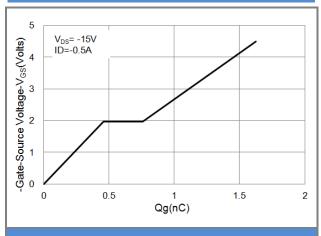


Fig.7 Gate-Charge Characteristics

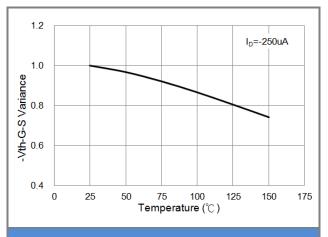


Fig.8 Threshold Voltage Variation with Temperature

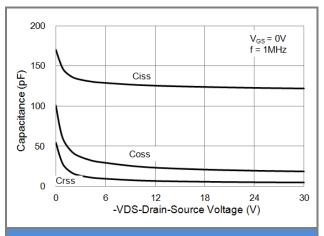


Fig.9 Capacitance vs. Drain-Source Voltage.

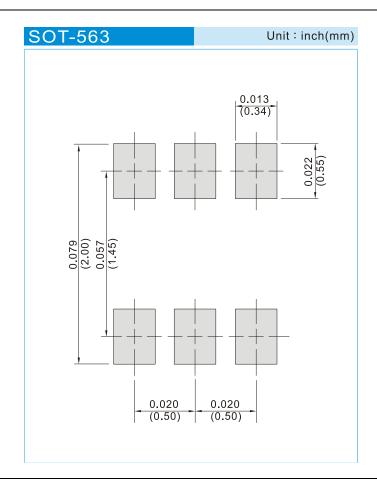




Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJX8805_R1_00002	SOT-563	4K pcs / 7" reel	X05	Halogen free RoHS compliant
PJX8805_R2_00002	SOT-563	10K pcs / 13" reel	X05	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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