

20V N-Channel Enhancement Mode MOSFET - ESD Protected

Voltage 20 V Current 2 A

Features

- RDS(ON) , VGS@4.5V, ID@2.0A<150mΩ
- RDS(ON), VGS@2.5V, ID@1.5A<215mΩ
- RDS(ON), VGS@1.8V, ID@0.5A<400mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in comply with EU RoHS 2011/65/EU directives.
- Green molding compound as per IEC61249 Std. (Halogen Free)

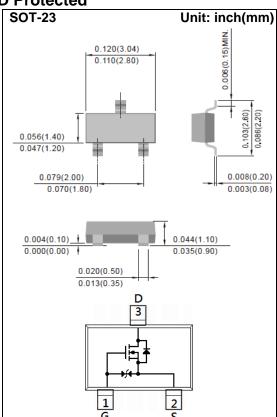
Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A30



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _G s	<u>+</u> 8	V
Continuous Drain Current		ΙD	2	Α
Pulsed Drain Current(Note 4)		I _{DM}	8	А
Power Dissipation	T _a =25°C		1.25	W
	Derate above 25°C	P _D	10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient ^(Note 3)		Reja	100	°C/W



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS} V _{GS} =0V, I _D =250uA		20	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.5	0.8	1.0	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =2A	-	105	150	mΩ	
		V _{GS} =2.5V, I _D =1.5A	-	150	215		
		V _{GS} =1.8V, I _D =0.5A	-	250	400		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	0.01	1	uA	
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 2	<u>+</u> 10	uA	
Dynamic							
Total Gate Charge	Q_g	\/ 40\/ L 0A	-	1.8	-	nC	
Gate-Source Charge	Qgs	V _{DS} =10V, I _D =2A,	-	0.4	-		
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V ^(Note 1,2)	-	0.45	-		
Input Capacitance	Ciss	\/ 40\/ \/ 0\/	-	92	-	pF	
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V,	-	25	-		
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	9.1	-		
Switching							
Turn-On Delay Time	td _(on)	\/ 40\/ L 0A	-	6.5	-		
Turn-On Rise Time	tr	V _{DD} =10V, I _D =2A,	-	26.5	-	ns	
Turn-Off Delay Time	td _(off)	V _{GS} =4.5V, R _G =6Ω ^(Note 1,2)	-	43	-		
Turn-Off Fall Time	tf	KG=012(Note 1,2)	-	34	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	Is		-	-	1.6	А	
Diode Forward Current							
Diode Forward Voltage	V_{SD}	I _S =1.6A, V _{GS} =0V		0.9	1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.



TYPICAL CHARACTERISTIC CURVES

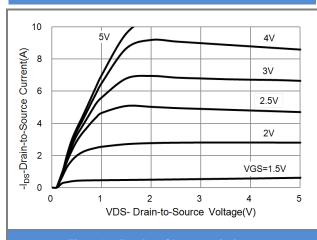


Fig.1 On-Region Characteristics

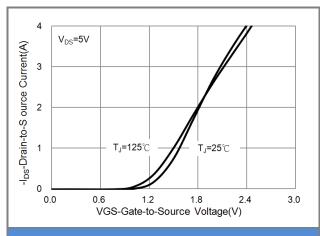


Fig.2 Transfer Characteristics

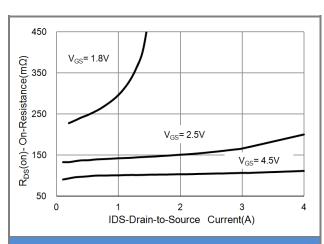


Fig.3 On-Resistance vs. Drain Current

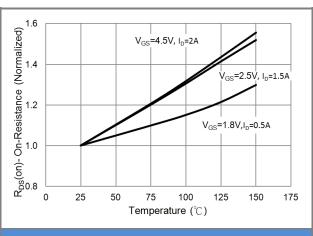
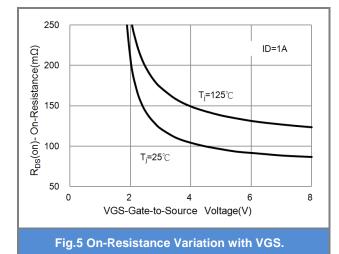
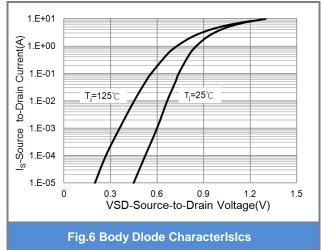


Fig.4 On-Resistance vs. Junction temperature







TYPICAL CHARACTERISTIC CURVES

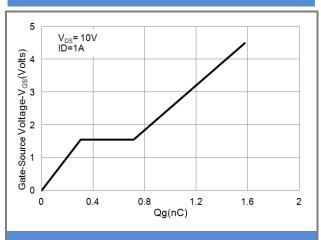


Fig.7 Gate-Charge Characteristics

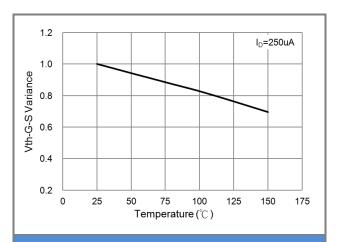


Fig.8 Threshold Voltage Variation with Temperature.

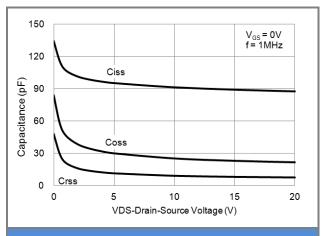


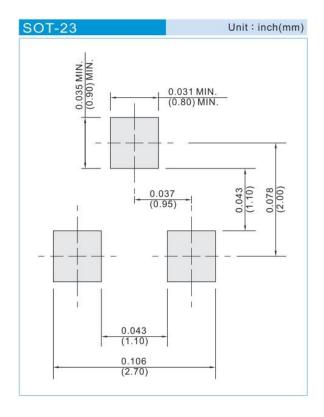
Fig.9 Capacitance vs. Drain-Source Voltage.



Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3430	SOT-23	3K pcs / 7" reel	A30

MOUNTING PAD LAYOUT





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